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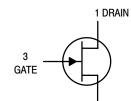
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Onsemi

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JFET Switching N-Channel — Depletion



2 SOURCE



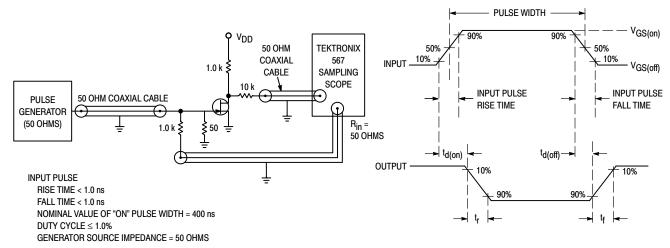
MAXIMUM RATINGS

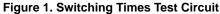
Rating	Symbol	Value	Unit
Drain–Source Voltage	V _{DS}	25	Vdc
Drain–Gate Voltage	V _{DG}	25	Vdc
Gate-Source Voltage	V _{GS}	25	Vdc
Forward Gate Current	I _{GF}	10	mAdc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	350 2.8	mW mW/°C
Junction Temperature Range	ТJ	-65 to +150	°C
Storage Temperature Range	T _{stg}	-65 to +150	°C



ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERIS	TICS				•
Gate-Source Breakdow	V _(BR) GSS	25	_	Vdc	
Gate Reverse Current (V _{GS} = 15 Vdc, V _{DS} = 0)	IGSS		1.0	nAdc
Drain Cutoff Current (V _E (V _E	ID(off)	_	10 2.0	nAdc μAdc	
ON CHARACTERIST	ICS			·	
Zero–Gate–Voltage Dra (V _{DS} = 15 Vdc, V _{GS}	IDSS	15	_	mAdc	
	$\frac{\text{Ate-Source Forward Voltage}}{ I_{G(f)} = 1.0 \text{ mAdc}, V_{DS} = 0 } \qquad \qquad$				Vdc
Drain–Source On–Voltage ($I_D = 7.0 \text{ mAdc}, V_{GS} = 0$)		VDS(on)	_	1.5	Vdc
Static Drain–Source On (I _D = 0.1 mAdc, V _{GS}	^r DS(on)	_	150	Ohms	
1. Pulse Test: Pulse Widt	h < 300 μs, Duty Cycle < 3.0%.	•		•	•
SMALL-SIGNAL CH	ARACTERISTICS				
Small–Signal Drain–Source "ON" Resistance $(V_{GS} = 0, I_D = 0, f = 1.0 \text{ kHz})$		^r ds(on)	_	150	Ohms
Input Capacitance (V _{DS} = 15 Vdc, V _{GS}	C _{iss}	_	5.0	pF	
Reverse Transfer Capacitance ($V_{DS} = 0$, $V_{GS} = 10$ Vdc, f = 1.0 MHz)		C _{rss}	—	1.2	pF
SWITCHING CHARA	CTERISTICS	· ·		•	
Turn–On Delay Time	$(V_{DD} = 10 \text{ Vdc}, I_{D(on)} = 7.0 \text{ mAdc},$	^t d(on)	_	5.0	ns
Rise Time	$V_{GS(on)} = 0$, $V_{GS(off)} = -10$ Vdc) (See Figure 1)	t _r	—	5.0	ns
Turn–Off Delay Time	$(V_{DD} = 10 \text{ Vdc}, I_{D(on)} = 7.0 \text{ mAdc},$	^t d(off)	_	15	ns
Fall Time	$V_{GS(on)} = 0, V_{GS(off)} = -10 \text{ Vdc}$ (See Figure 1)	t _f	_	10	ns





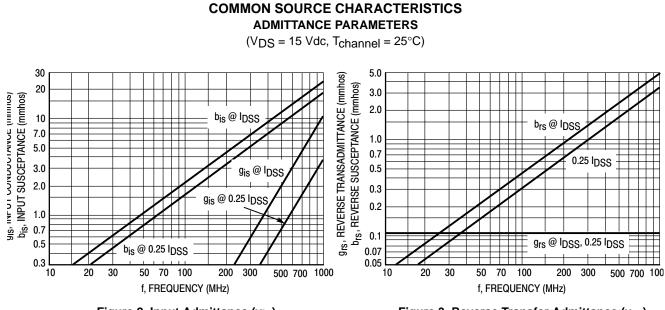
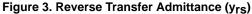
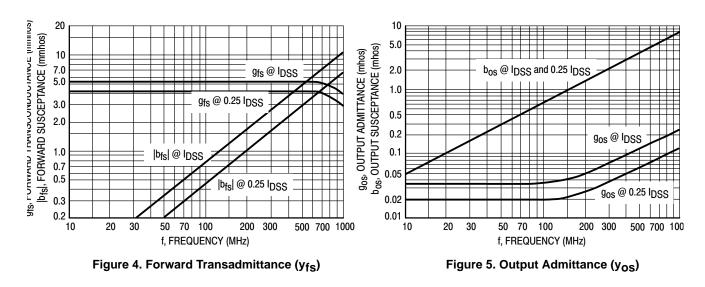


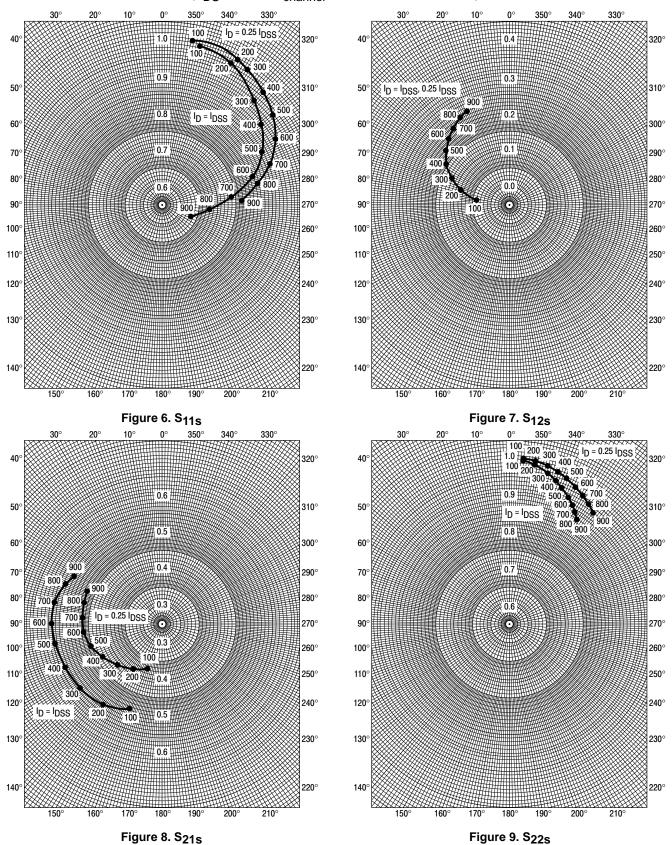
Figure 2. Input Admittance (yis)





COMMON SOURCE CHARACTERISTICS S-PARAMETERS

(V_{DS} = 15 Vdc, T_{channel} = 25°C, Data Points in MHz)



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COMMON GATE CHARACTERISTICS ADMITTANCE PARAMETERS

 $(V_{DG} = 15 \text{ Vdc}, T_{channel} = 25^{\circ}C)$

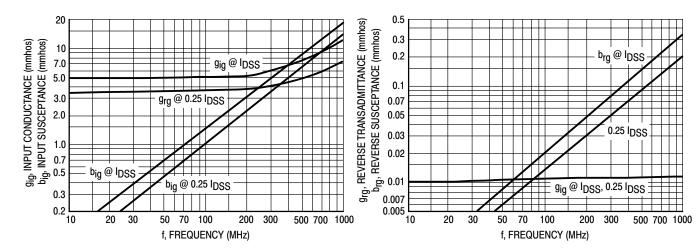


Figure 10. Input Admittance (yiq)

Figure 11. Reverse Transfer Admittance (yrg)

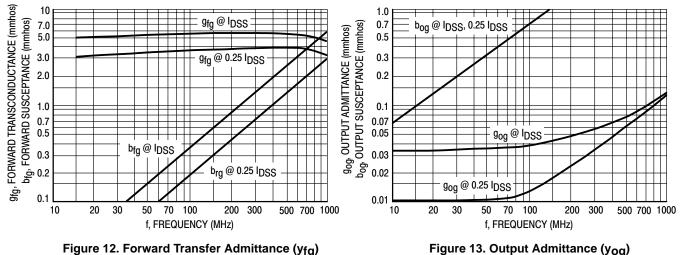
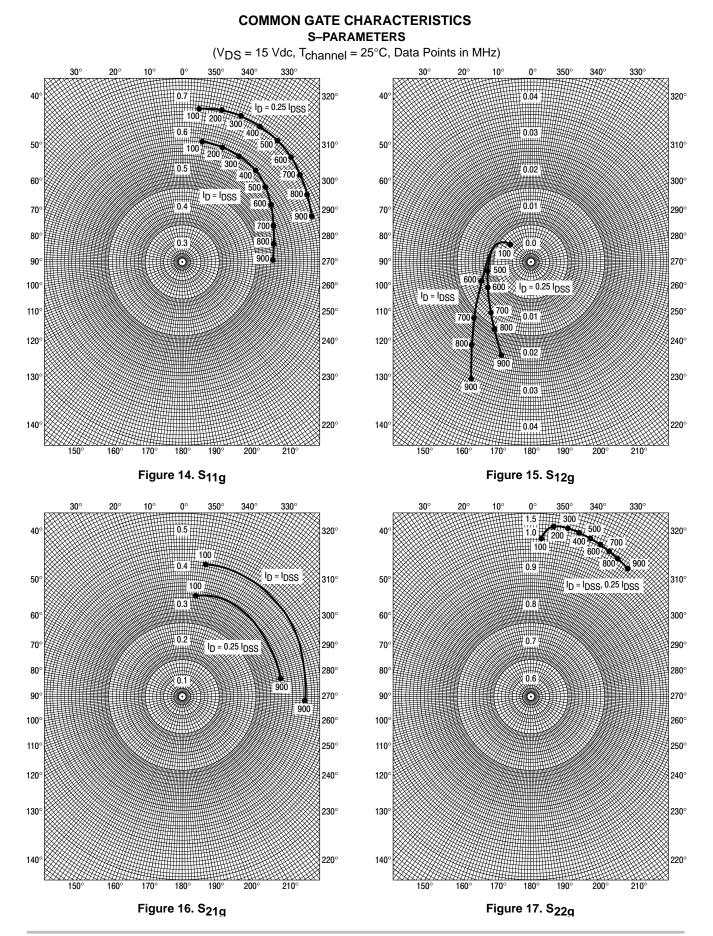
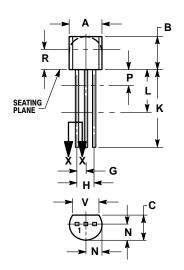


Figure 12. Forward Transfer Admittance (yfg)



PACKAGE DIMENSIONS

TO-92 (TO-226AA) CASE 29-11 ISSUE AL

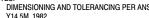






STYLE 5: PIN 1. DRAIN 2. SOURCE 3. GATE





- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.115		2.93	
۷	0.135		3.43	

<u>Notes</u>

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