onsemi

2SA1774G, S2SA1774G

This PNP transistor is designed for general purpose amplifier applications. This device is housed in the SC-75/SOT-416 package which is designed for low power surface mount applications, where board space is at a premium.

Features

- Reduces Board Space
- High h_{FE}, 210–460 (typical)
- Low V_{CE(sat)}, < 0.5 V
- Available in 8 mm, 7-inch/3000 Unit Tape and Reel
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{(BR)CBO}	-60	Vdc
Collector – Base Voltage	V _{(BR)CEO}	-50	Vdc
Emitter – Base Voltage	V _{(BR)EBO}	-6.0	Vdc
Collector Current – Continuous	Ι _C	-100	mAdc

THERMAL CHARACTERISTICS

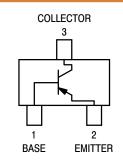
Characteristic	Symbol	Max	Unit
Power Dissipation (Note 1)	PD	150	mW
Junction Temperature	ТJ	150	°C
Storage Temperature Range	T _{stg}	-55 ~ +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

 Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.



SC-75/SOT-416 CASE 463 STYLE 1



MARKING DIAGRAM



F9 = Device Code

M = Date Code*

= Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
2SA1774G	SC–75 (Pb–Free)	3,000 / Tape & Reel
S2SA1774G	SC–75 (Pb–Free)	3,000 / Tape & Reel
2SA1774T1G	SC–75 (Pb–Free)	3,000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

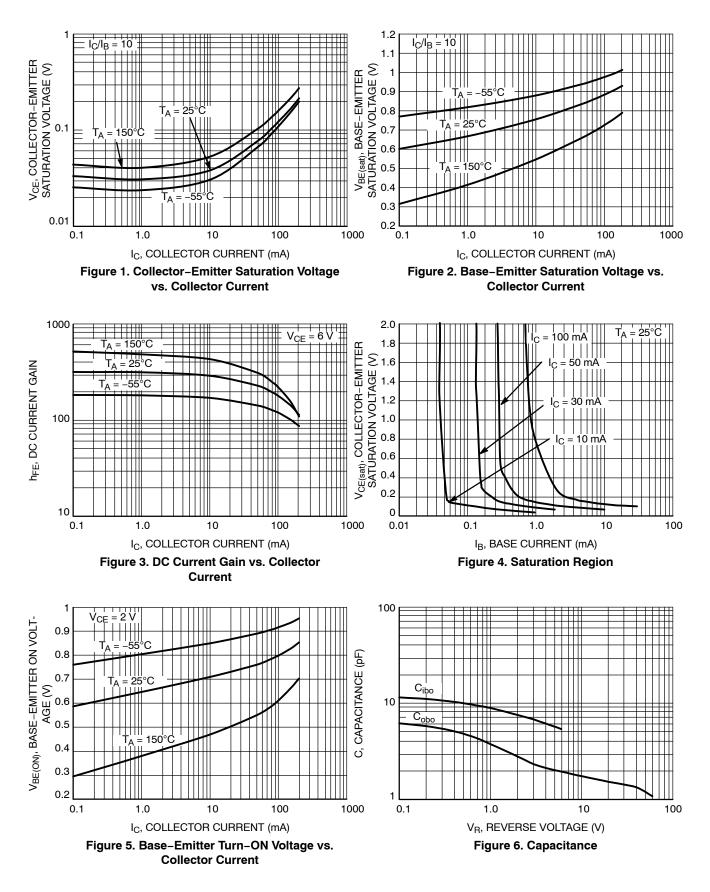
Characteristic	Symbol	Min	Тур	Max	Unit
Collector-Base Breakdown Voltage $(I_C = -50 \ \mu Adc, I_E = 0)$	V _{(BR)CBO}	-60	_	-	V
Collector–Emitter Breakdown Voltage $(I_C = -1.0 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	-50	_	-	V
Emitter-Base Breakdown Voltage $(I_E = -50 \ \mu Adc, I_E = 0)$	V _{(BR)EBO}	-6.0	_	-	V
Collector–Base Cutoff Current ($V_{CB} = -30$ Vdc, $I_E = 0$)	I _{CBO}	-	_	-0.5	μΑ
Emitter–Base Cutoff Current ($V_{EB} = -5.0 \text{ Vdc}, I_B = 0$)	I _{EBO}	-	_	-0.5	μΑ
Collector–Emitter Saturation Voltage (Note 2) $(I_C = -50 \text{ mAdc}, I_B = -5.0 \text{ mAdc})$	V _{CE(sat)}	-	_	-0.5	V
DC Current Gain (Note 2) $(V_{CE} = -6.0 \text{ Vdc}, I_C = -1.0 \text{ mAdc})$	h _{FE}	120	_	560	_
Transition Frequency (V _{CE} = -12 Vdc, I _C = -2.0 mAdc, f = 30 MHz)	f _T	-	140	-	MHz
Output Capacitance (V _{CB} = -12 Vdc, I _E = 0 Adc, f = 1 MHz)	C _{OB}	_	3.5	-	pF

ELECTRICAL CHARACTERISTICS (T_A = 25° C)

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 2. Pulse Test: Pulse Width \leq 300 µs, D.C. \leq 2%.

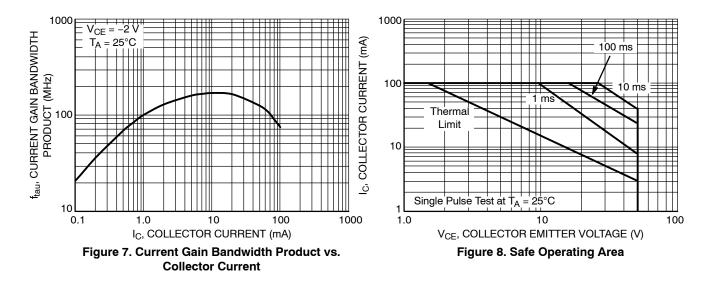
2SA1774G, S2SA1774G

TYPICAL ELECTRICAL CHARACTERISTICS



2SA1774G, S2SA1774G

TYPICAL ELECTRICAL CHARACTERISTICS (Continued)



MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

SC75-3 1.60x0.80x0.80, 1.00P **CASE 463 ISSUE H** DATE 01 FEB 2024 NOTES: Α D DIMENSIONING AND TOLERANCING CONFORM 1. В TO ASME Y14.5-2018. ALL DIMENSION ARE IN MILLIMETERS. 2. F MILLIMETERS F DIM MIN. MAX. NOM. 0.70 0.800.90 А 3X b Α1 0.00 0.05 0.10 \oplus 0.20 \oplus C A B е A2 0.80 REF. 0.15 0.20 b 0.30 TOP VIEW С 0.10 0.15 0.25 A2 D 1.55 1.60 1.65 E 1.50 1.60 1.70 E1 0.70 0.80 0.90 С 1.00 BSC е SEATING Ċ A1 L 0.20 PLANE 0.10 0.15 -0.356 END VIEW SIDE VIEW GENERIC **MARKING DIAGRAM*** 1.803 0.787XXM XX = Specific Device Code Μ = Date Code 0.508 = Pb-Free Package 1.000 *This information is generic. Please refer to device data sheet for actual part marking. RECOMMENDED MOUNTING FOOTPRINT* Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY not follow the Generic Marking. AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES STYLE 3: PIN 1. ANODE 2. ANODE STYLE 1: PIN 1. BASE 2. EMITTER STYLE 2: PIN 1. ANODE 2. N/C REFERENCE MANUAL, SOLDERRM/D. 3. COLLECTOR 3. CATHODE 3. CATHODE STYLE 4: STYLE 5: PIN 1. CATHODE 2. CATHODE PIN 1. GATE 2. SOURCE 3. ANODE 3. DRAIN Electronic versions are uncontrolled except when accessed directly from the Document Repository. DOCUMENT NUMBER: 98ASB15184C Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. **DESCRIPTION:** SC75-3 1.60x0.80x0.80, 1.00P PAGE 1 OF 1 onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

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