

Common Cathode Silicon Dual Switching Diode

DAN222M3T5G

This Common Cathode Silicon Epitaxial Planar Dual Diode is designed for use in ultra high speed switching applications. This device is housed in the SOT-723 package which is designed for low power surface mount applications, where board space is at a premium.

Features

- Fast t_{rr}
- Low C_D
- Available in 4 mm Tape and Reel
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS $(T_A = 25 \, ^{\circ}C)$

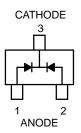
Rating	Symbol	Value	Unit
Reverse Voltage	V_R	80	V
Peak Reverse Voltage	V_{RM}	80	V
Forward Current	I _F	100	mA

THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Power Dissipation	P _D	260	mW
Junction Temperature	TJ	150	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. $t = 1.0 \mu s$.



MARKING DIAGRAM



SOT-723 CASE 631AA STYLE 3



N9 = Specific Device Code M = Date Code

ORDERING INFORMATION

Device	Package	Shipping [†]
DAN222M3T5G	SOT-723 (Pb-Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS $(T_A = 25 \, ^{\circ}C)$

Characteristic	Symbol	Condition	Min	Max	Unit
Reverse Voltage Leakage Current (Note 2)	I _R	V _R = 70 V	-	0.1	μΑ
Forward Voltage	V _F	I _F = 100 mA	-	1.2	V
Reverse Breakdown Voltage	V _R	I _R = 100 μA	80	-	V
Diode Capacitance	C _D	V _R = 6.0 V, f = 1.0 MHz	-	3.5	pF
Reverse Recovery Time (Note 3)	t _{rr}	$I_F = 5.0 \text{ mA}, V_R = 6.0 \text{ V},$ $R_L = 100 \Omega, I_{rr} = 0.1 I_R$	-	4.0	ns

- 2. For each diode while other is not forward biased.
- 3. t_{rr} Test Circuit on following page.

TYPICAL ELECTRICAL CHARACTERISTICS

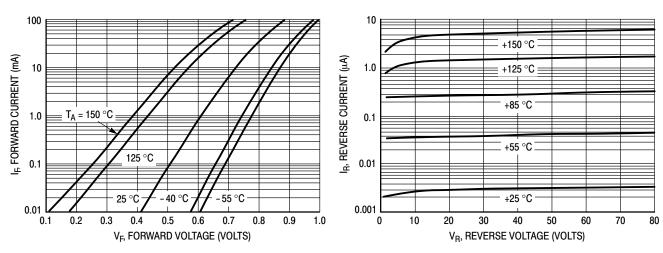


Figure 1. Forward Voltage

Figure 2. Reverse Current

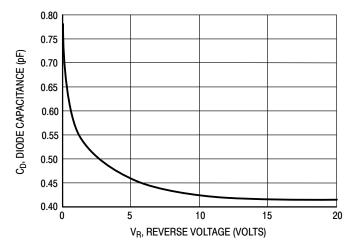
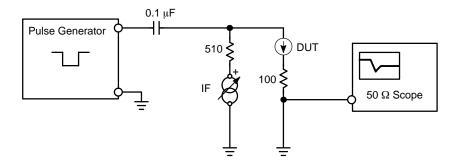


Figure 3. Diode Capacitance

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RECOVERY TIME EQUIVALENT TEST CIRCUIT

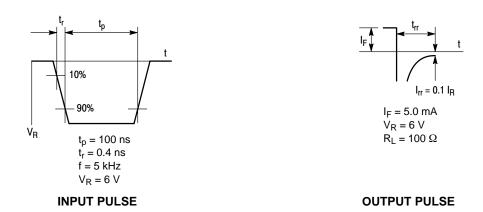


Figure 4. Reverse Recovery Time Test Circuit





SOT-723 1.20x0.80x0.50, 0.40P CASE 631AA ISSUE E

DATE 24 JAN 2024

MAX.

0.55

0.27

0.37

0.17

1.25

0.85

1.25

0.25

MILLIMETERS

 $N\square M$.

0.50

0.21

0.31

0.12

1.20

0.80

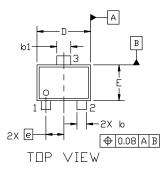
0.40 BSC

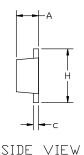
1.20

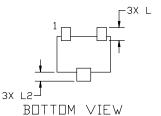
0.29 REF

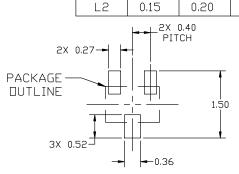
NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH, MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.









DIM

Α

b

b1

c D

Ε

e H

L

MIN.

0.45

0.15

0.25

0.07

1.15

0.75

1.15

RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

GENERIC MARKING DIAGRAM*



XX = Specific Device Code M = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:
PIN 1. BASE	PIN 1. ANODE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. GATE
2. EMITTER	2. N/C	2. ANODE	CATHODE	SOURCE
COLLECTOR	CATHODE	CATHODE	ANODE	DRAIN

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DESCRIPTION:	SOT-723 1.20x0.80x0.50, 0.40P		PAGE 1 OF 1	

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