Onsemi

Silicon Carbide (SiC) **Schottky Diode** – EliteSiC, 30 A, 650 V, D1, TO-247-3L

FFSH3065ADN-F155

Description

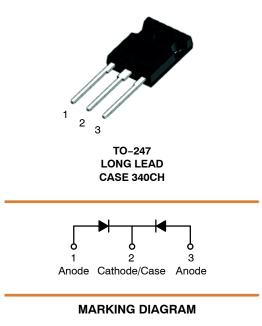
Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

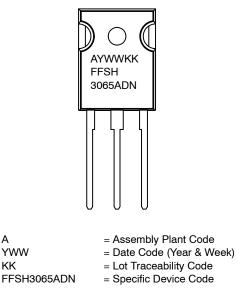
Features

- Max Junction Temperature 175°C
- Avalanche Rated 81 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery/No Forward Recovery
- This Device is Pb-Free, Halogen Free/BFR Free and RoHS Compliant

Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits





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ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Symbol	Parameter	Value	Unit	
V _{RRM}	Peak Repetitive Reverse Voltage	650	V	
E _{AS}	Single Pulse Avalanche Energy (Note 1)	81	mJ	
١ _F	Continuous Rectified Forward Current @ T _C < 148°C (Note 2)		16*/30**	А
	Continuous Rectified Forward Current @ T _C < 135°C		23*/36**	
I _{F,Max}	Non-Repetitive Peak Forward Surge Current	T _C = 25°C, 10 μs	1000	А
		T _C = 150°C, 10 μs	900	А
I _{F,SM}	Non-Repetitive Forward Surge Current	Half-Sine Pulse, t _p = 8.3 ms	90	А
I _{F,RM}	Repetitive Forward Surge Current	Half-Sine Pulse, t _p = 8.3 ms	50	А
Ptot	Power Dissipation	$T_{C} = 25^{\circ}C$	165	W
		T _C = 150°C	28	W
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
	TO247 Mounting Torque, M3 Screw		60	Ncm

Table 1. ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. EAS of 81 mJ is based on starting T_J = 25°C, L = 0.5 mH, IAS = 18 A, V = 50 V.

2. Limited by per device.

Table 2. THERMAL CHARACTERISTICS

Symbol	Parameter	Rating	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	0.91*/0.4**	°C/W

* Per Leg ** Per Device

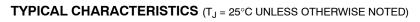
Table 3. OPERATING CHARACTERISTICS (T_C = 25°C, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V _F	Forward Voltage	$I_F = 16 \text{ A}, \text{ T}_C = 25^{\circ}\text{C}$	-	1.5	1.75	V
		$I_F = 16 \text{ A}, T_C = 125^{\circ}\text{C}$	-	1.6	2.0	
		$I_F = 16 \text{ A}, \text{ T}_C = 175^{\circ}\text{C}$	-	1.72	2.4	
I _R	Reverse Current	V_R = 650 V, T_C = 25°C	-	-	200	μΑ
		$V_{R} = 650 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$	-	-	400	
		$V_{R} = 650 \text{ V}, \text{ T}_{C} = 175^{\circ}\text{C}$	-	-	600	
Q _C	Total Capacitive Charge	V = 400 V	-	52	-	nC
С	Total Capacitance	V _R = 1 V, f = 100 kHz	-	887	-	pF
		V _R = 200 V, f = 100 kHz	-	95	-	
		V _R = 400 V, f = 100 kHz	-	72	-]

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

PART MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FFSH3065ADN-F155	FFSH3065ADN	TO-247 Long Lead	Tube	N/A	N/A	30 units



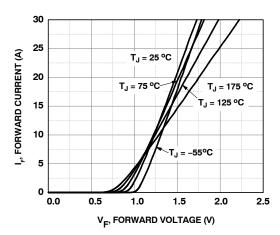


Figure 1. Forward Characteristics

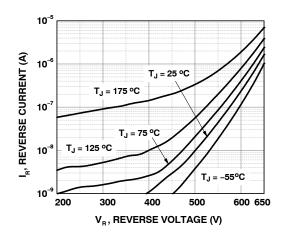


Figure 2. Reverse Characteristics

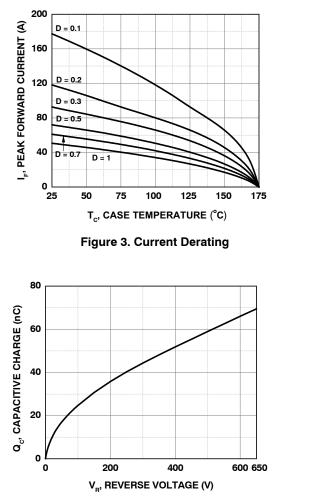


Figure 5. Capacitive Charge vs. Reverse Voltage

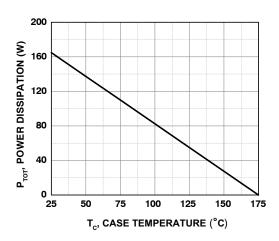
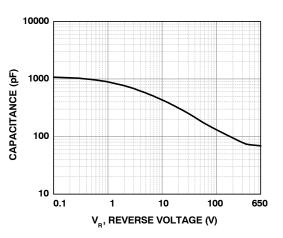
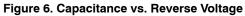


Figure 4. Power Derating





TYPICAL CHARACTERISTICS (T_J = 25°C UNLESS OTHERWISE NOTED)

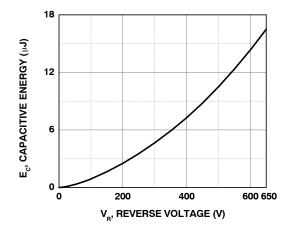


Figure 7. Capacitance Stored Energy

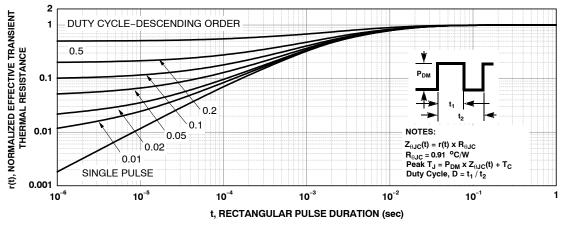


Figure 8. Junction-to-Case Transient Thermal Response Curve

TYPICAL CHARACTERISTICS (T_J = 25°C UNLESS OTHERWISE NOTED)

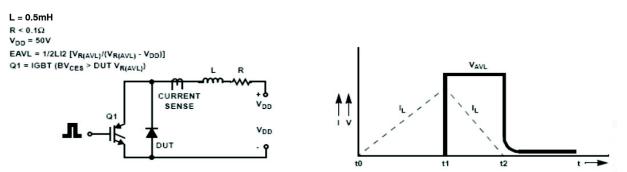
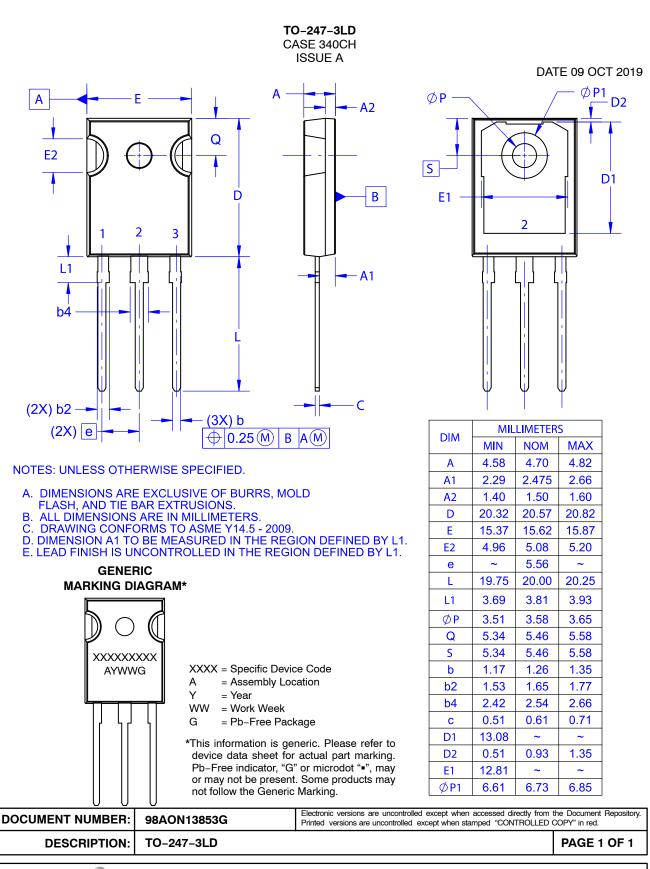


Figure 9. Unclamped Inductive Switching Test Circuit & Waveform





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