# **IGBT - Short-Circuit Rated**

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Non–Punch Through (NPT) Trench construction, and provides superior performance in demanding switching applications. Offering both low on state voltage and minimal switching loss, the IGBT is well suited for motor drive control and other hard switching applications. Incorporated into the device is a rugged co–packaged reverse recovery diode with a low forward voltage.

#### Features

- Low Saturation Voltage Resulting in Low Conduction Loss
- Low Switching Loss in Higher Frequency Applications
- Soft Fast Reverse Recovery Diode
- 10 µs Short Circuit Capability
- Excellent Current versus Package Size Performance Density
- This is a Pb–Free Device

#### **Typical Applications**

- White Goods Appliance Motor Control
- General Purpose Inverter
- AC and DC Motor Control

#### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-emitter voltage	V <sub>CES</sub>	600	V
Collector current @ Tc = $25^{\circ}$ C @ Tc = $100^{\circ}$ C	Ι <sub>C</sub>	30 15	A
Pulsed collector current, $T_{pulse}$ limited by $T_{Jmax}$	I <sub>CM</sub>	120	A
Diode forward current @ Tc = $25^{\circ}$ C @ Tc = $100^{\circ}$ C	I <sub>F</sub>	30 15	A
Diode pulsed current, $T_{pulse}$ limited by $T_{Jmax}$	I <sub>FM</sub>	120	A
Gate-emitter voltage	V <sub>GE</sub>	±20	V
Power dissipation @ Tc = 25°C @ Tc = 100°C	P <sub>D</sub>	117 47	W
Short circuit withstand time $V_{GE}$ = 15 V, $V_{CE}$ = 400 V, $T_J \le +150^{\circ}C$	t <sub>SC</sub>	10	μS
Operating junction temperature range	TJ	–55 to +150	°C
Storage temperature range	T <sub>stg</sub>	–55 to +150	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T <sub>SLD</sub>	260	°C

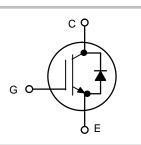
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

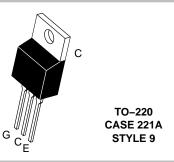


### **ON Semiconductor®**

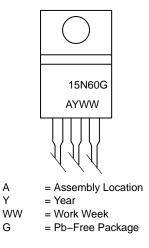
www.onsemi.com

15 A, 600 V V<sub>CEsat</sub> = 1.7 V





#### MARKING DIAGRAM



#### ORDERING INFORMATION

Device	Package	Shipping
NGTB15N60EG	TO–220 (Pb–Free)	50 Units / Rail

Semiconductor Components Industries, LLC, 2015 January, 2015 – Rev. 8

#### THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction to case, for IGBT	$R_{ ext{ heta}JC}$	1.06	°C/W
Thermal resistance junction to case, for Diode	$R_{\theta JC}$	3.76	°C/W
Thermal resistance junction to ambient	$R_{ hetaJA}$	60	°C/W

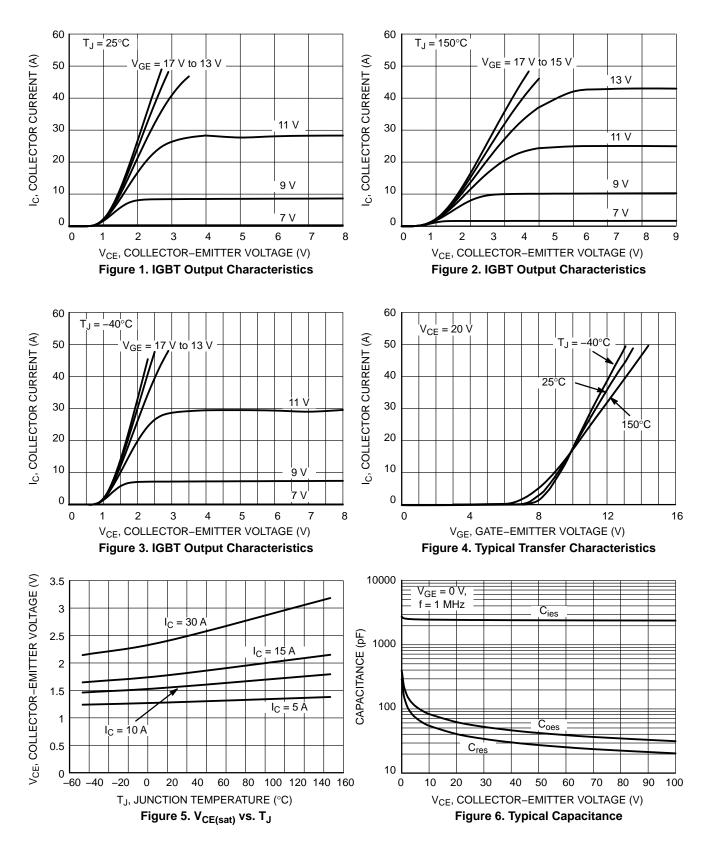
#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

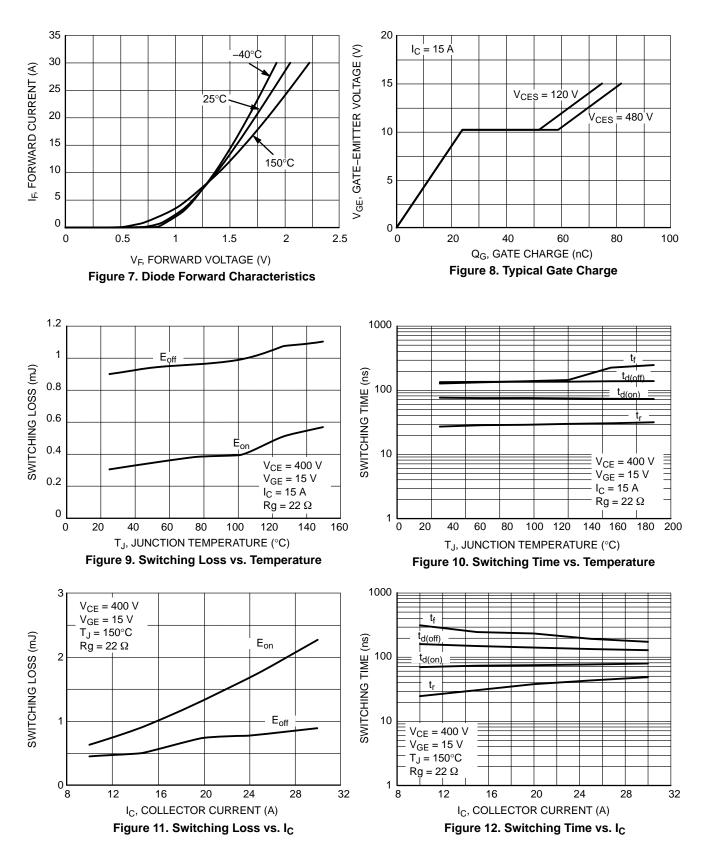
Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC						
Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE}$ = 0 V, I <sub>C</sub> = 500 $\mu$ A	V <sub>(BR)CES</sub>	600	-	-	V
Collector-emitter saturation voltage	$V_{GE}$ = 15 V , I <sub>C</sub> = 15 A V <sub>GE</sub> = 15 V , I <sub>C</sub> = 15 A, T <sub>J</sub> = 150°C	V <sub>CEsat</sub>	1.45 1.8	1.7 2.1	1.95 2.4	V
Gate-emitter threshold voltage	$V_{GE}$ = $V_{CE}$ , $I_{C}$ = 250 $\mu A$	V <sub>GE(th)</sub>	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate-emitter short-circuited	$V_{GE} = 0 V, V_{CE} = 600 V$ $V_{GE} = 0 V, V_{CE} = 600 V, T_{J} = 150^{\circ}C$	I <sub>CES</sub>	-	10 -	_ 200	μΑ
Gate leakage current, collector-emitter short-circuited	$V_{GE}$ = 20 V, $V_{CE}$ = 0 V	I <sub>GES</sub>	_	-	100	nA
Forward Transconductance	$V_{CE} = 20 \text{ V}, \text{ I}_{C} = 15 \text{ A}$	<b>g</b> fs	_	10.1	-	S
DYNAMIC CHARACTERISTIC						
Input capacitance		C <sub>ies</sub>	_	2600	-	
Output capacitance	$V_{CE}$ = 20 V, $V_{GE}$ = 0 V, f = 1 MHz	C <sub>oes</sub>	-	64	-	pF
Reverse transfer capacitance		C <sub>res</sub>	-	42	-	
Gate charge total		Qg	-	80	-	
Gate to emitter charge	$V_{CE}$ = 480 V, I <sub>C</sub> = 15 A, V <sub>GE</sub> = 15 V	Q <sub>ge</sub>	_	24	-	nC
Gate to collector charge		Q <sub>gc</sub>	-	33	-	
SWITCHING CHARACTERISTIC , INDUCTIVE	LOAD					
Turn-on delay time		t <sub>d(on)</sub>	_	78	-	
Rise time		t <sub>r</sub>	_	30	-	
Turn-off delay time	$T_J = 25^{\circ}C$	t <sub>d(off)</sub>	_	130	-	ns
Fall time	$V_{CC} = 400 \text{ V}, I_{C} = 15 \text{ A}$	t <sub>f</sub>	_	120	-	
Turn-on switching loss	R <sub>g</sub> = 22 Ω V <sub>GE</sub> = 0 V / 15 V	Eon	_	0.900	-	
Turn-off switching loss		E <sub>off</sub>	_	0.300	-	mJ
Total switching loss		E <sub>ts</sub>	_	1.200	-	
Turn–on delay time		t <sub>d(on)</sub>	_	76	-	
Rise time	$T_{J} = 150^{\circ}C$ V <sub>CC</sub> = 400 V, I <sub>C</sub> = 15 A R <sub>g</sub> = 22 Ω V <sub>GE</sub> = 0 V / 15 V	t <sub>r</sub>	_	33	-	
Turn-off delay time		t <sub>d(off)</sub>	_	133	-	ns
Fall time		t <sub>f</sub>	-	223	-	
Turn-on switching loss		Eon	-	1.10	-	
Turn-off switching loss		E <sub>off</sub>	-	0.510	-	mJ
Total switching loss		E <sub>ts</sub>	-	1.610	-	
DIODE CHARACTERISTIC		<b>I</b>				
Forward voltage	V <sub>GE</sub> = 0 V, I <sub>F</sub> = 15 A V <sub>GE</sub> = 0 V, I <sub>F</sub> = 15 A, T <sub>J</sub> = 150°C	V <sub>F</sub>	-	1.6 1.6	1.85 -	V

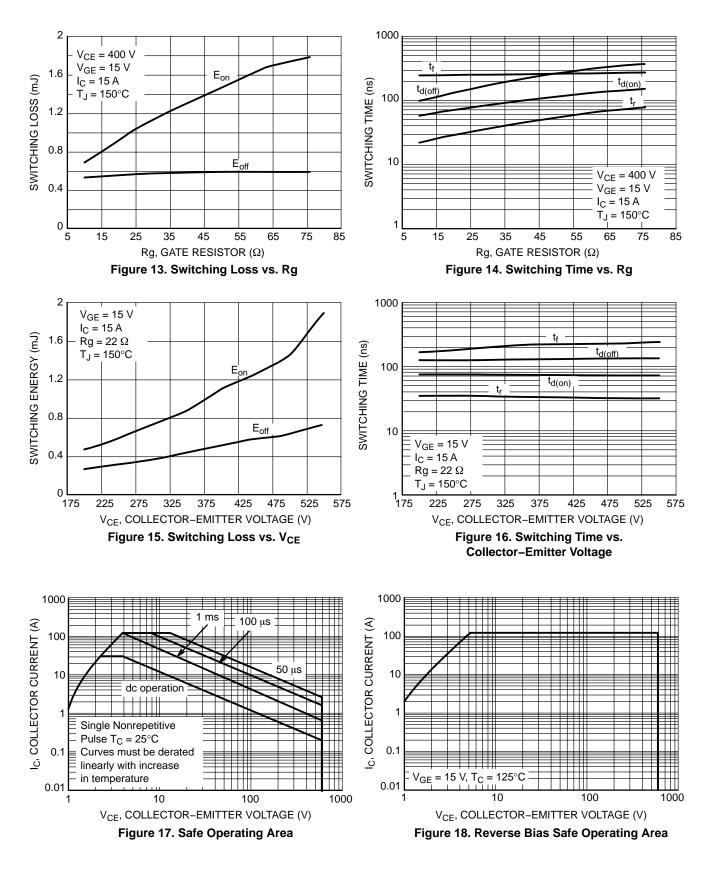
## **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise specified)

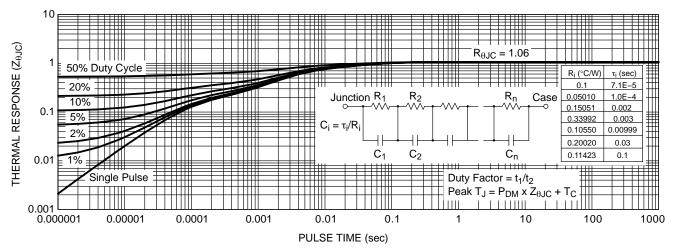
Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
DIODE CHARACTERISTIC						
Reverse recovery time	$T_J = 25^{\circ}C$ I <sub>F</sub> = 15 A, V <sub>R</sub> = 200 V di <sub>F</sub> /dt = 200 A/µs	t <sub>rr</sub>	-	270	-	ns
Reverse recovery charge		Q <sub>rr</sub>	-	350	-	nc
Reverse recovery current		I <sub>rrm</sub>	-	5	-	А
Reverse recovery time	$T_J = 125^{\circ}C$ $I_F = 15 A, V_R = 200 V$ $di_F/dt = 200 A/\mu s$	t <sub>rr</sub>	-	350	-	ns
Reverse recovery charge		Q <sub>rr</sub>	-	1000	-	nc
Reverse recovery current		I <sub>rrm</sub>	-	7.5	-	А

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.











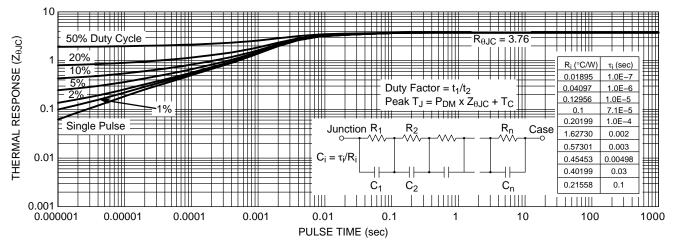


Figure 20. Diode Transient Thermal Impedance

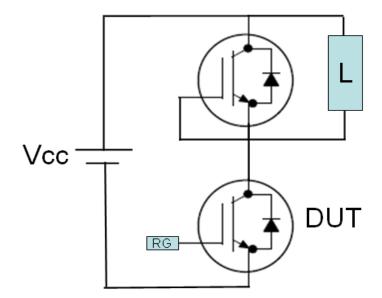
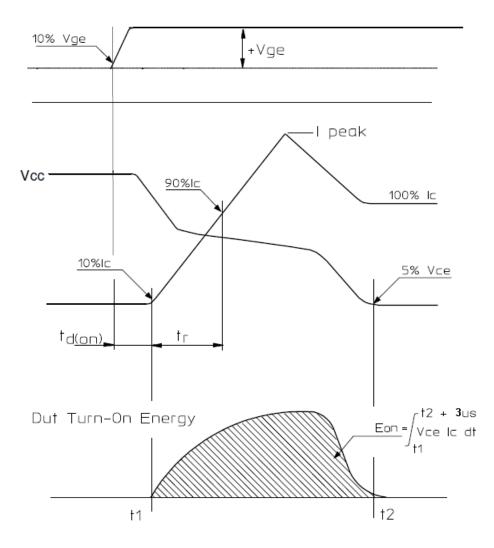
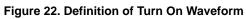


Figure 21. Test Circuit for Switching Characteristics





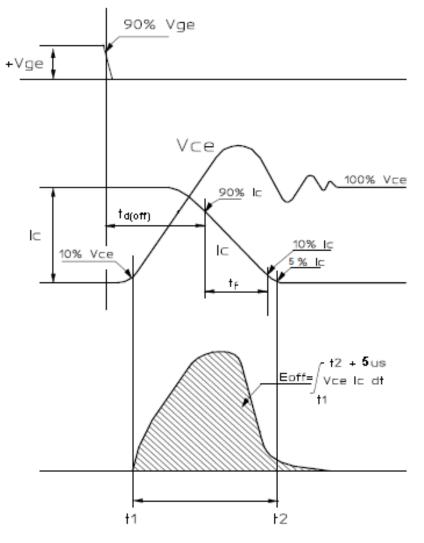


Figure 23. Definition of Turn Off Waveform

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