

Silicon Carbide (SiC) Schottky Diode – EliteSiC, 5 A, 1200 V, D1, Die

PCFFS05120AF

Description

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature dependent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operation frequency, increased power density, reduced EMI, and reduced system size and cost.

Features

- Max Junction Temperature 175°C
- Avalanche Rated 55 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery

Applications

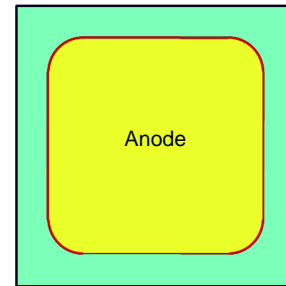
- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits

Die Information

- Wafer Diameter: 6 inch
- Die Size: 1,690 x 1,690 μm (Include Scribe Lane)
- Metallization
 - ♦ Top: Ti / TiN / Al 4 μm
 - ♦ Back: Ti/ NiV / Ag
- Die Thickness: Typ. 200 μm
- Bonding Pad Size
 - ♦ Anode: 1,110 x 1,110 μm
- Recommended Wire Bond (Note 1)
 - ♦ Anode: 12 mil x 1

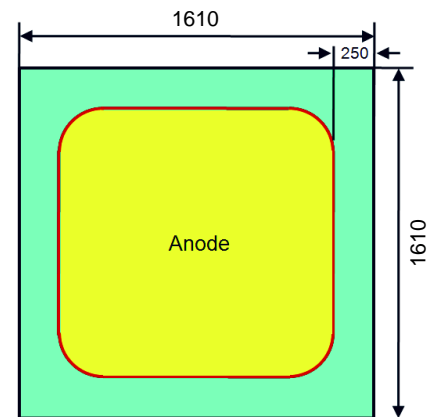
NOTE:

1. Based on TO-247 package of onsemi



DIE LAYOUT

(Dimension: μm , Except Scribe Lane)

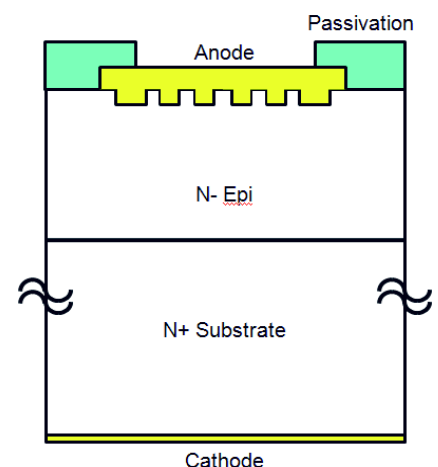


Passivation Area

Passivation Information

- Passivation Material: Polyimide (PSPI)
- Passivation Type: Local Passivation
- Passivation Thickness: 90KA

CROSS SECTION



ORDERING INFORMATION

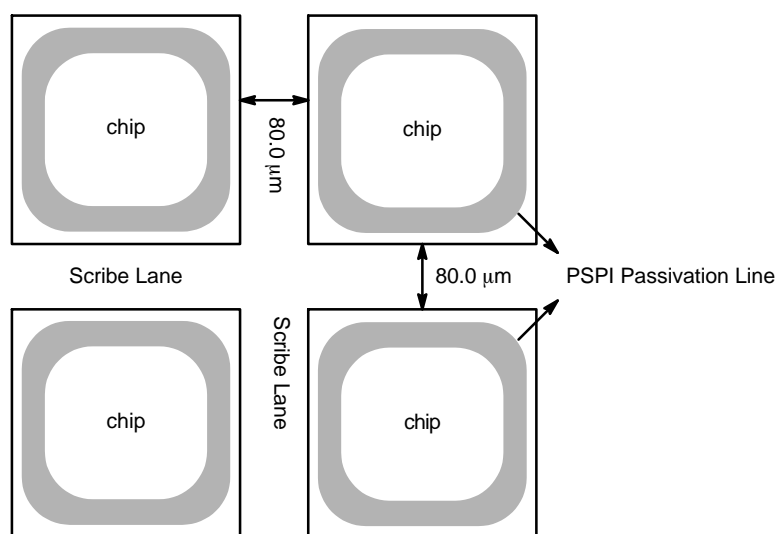
Part Number	Package	Die Size
PCFFS05120AF	N/A	1,690 x 1,690 μm (Include Scribe Lane)

ELECTRICAL CHARACTERISTICS ON WAFER (Note 2) ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V_R	Reverse Blocking Voltage	$I_R = 200\ \mu\text{A}$, $T_C = 25^\circ\text{C}$	1200	–	–	V
V_F	Forward Voltage	$I_F = 5\ \text{A}$, $T_C = 25^\circ\text{C}$	1.20	–	1.75	V
I_R	Reverse Current	$V_R = 1200\ \text{V}$, $T_C = 25^\circ\text{C}$	–	–	200	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Tested 100% on wafer

The Configuration of Chips (Based on 6 Inch Wafer)

Sawn-on-film frame packing based on tested wafer

Figure 1. The Configuration of Chips (Based on 6 Inch Wafer)

PCFFS05120AF

ABSOLUTE MAXIMUM RATINGS ON TO-247 PACKAGE ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V_{RRM}	Peak Repetitive Reverse Voltage	1200	V
E_{AS}	Single Pulse Avalanche Energy (Note 3)	55	mJ
I_F	Continuous Rectified Forward Current @ $T_C < 148^\circ\text{C}$	5	A
$I_{F, Max}$	Non-Repetitive Peak Forward Surge Current	$T_C = 25^\circ\text{C}, 10 \mu\text{s}$	A
		$T_C = 150^\circ\text{C}, 10 \mu\text{s}$	A
$I_{F, SM}$	Non-Repetitive Forward Surge Current	Half-Sine Pulse, $t_p = 8.3 \text{ ms}$	A
$I_{F, RM}$	Repetitive Forward Surge Current	Half-Sine Pulse, $t_p = 8.3 \text{ ms}$	A
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to $+175$	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

3. EAS of 55 mJ is based on starting $T_J = 25^\circ\text{C}$, $L = 0.5 \text{ mH}$, $I_{AS} = 15 \text{ A}$, $V = 150 \text{ V}$.

ELECTRICAL CHARACTERISTICS ON TO-247 PACKAGE ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V_F	Forward Voltage	$I_F = 5 \text{ A}, T_C = 25^\circ\text{C}$	–	1.45	1.75	V
		$I_F = 5 \text{ A}, T_C = 125^\circ\text{C}$	–	1.7	2	
		$I_F = 5 \text{ A}, T_C = 175^\circ\text{C}$	–	2	2.4	
I_R	Reverse Current	$V_R = 1200 \text{ V}, T_C = 25^\circ\text{C}$	–	–	200	μA
		$V_R = 1200 \text{ V}, T_C = 125^\circ\text{C}$	–	–	300	
		$V_R = 1200 \text{ V}, T_C = 175^\circ\text{C}$	–	–	400	
Q_C	Total Capacitive Charge	$V = 800 \text{ V}$	–	37	–	nC
C	Total Capacitance	$V_R = 1 \text{ V}, f = 100 \text{ kHz}$	–	337	–	pF
		$V_R = 400 \text{ V}, f = 100 \text{ kHz}$	–	33	–	
		$V_R = 800 \text{ V}, f = 100 \text{ kHz}$	–	26	–	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

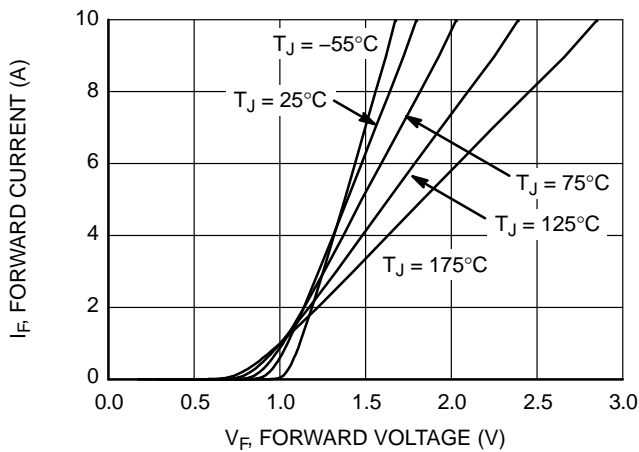


Figure 2. Forward Characteristics

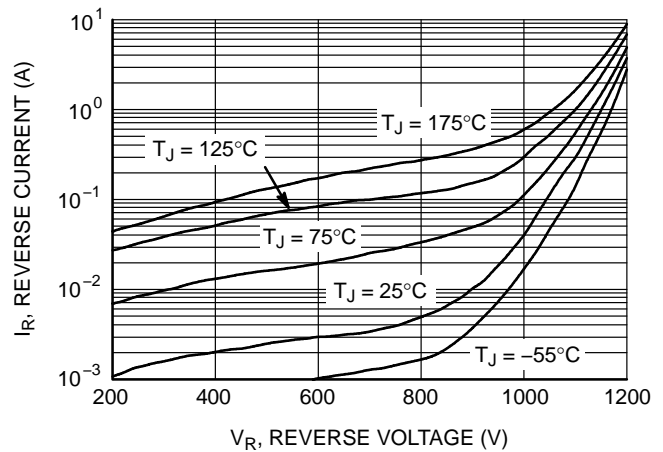


Figure 3. Reverse Characteristics

TYPICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

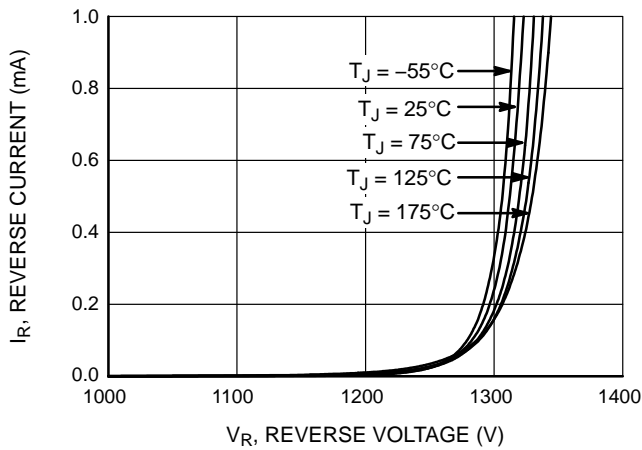


Figure 4. Reverse Characteristics

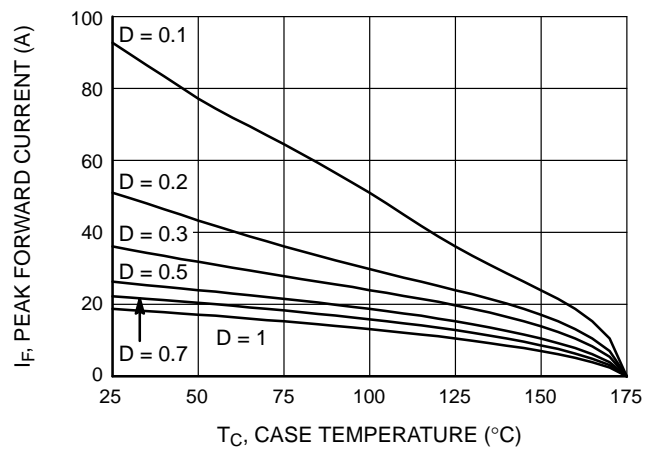


Figure 5. Current Derating

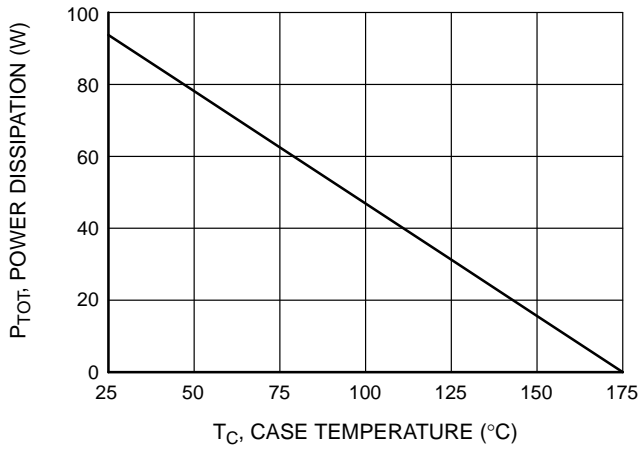


Figure 6. Power Derating

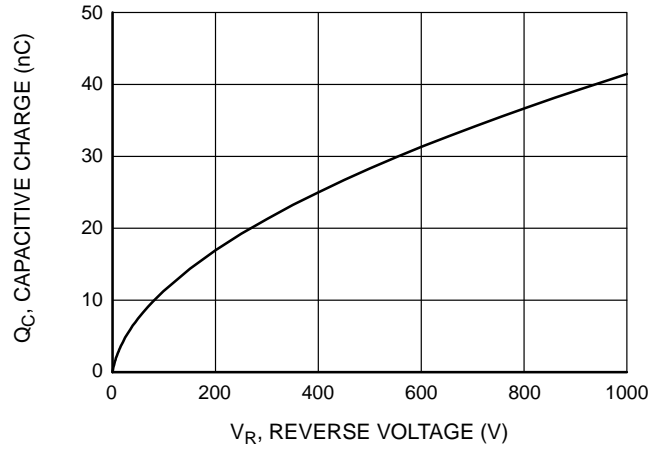


Figure 7. Capacitive Charge vs. Reverse Voltage

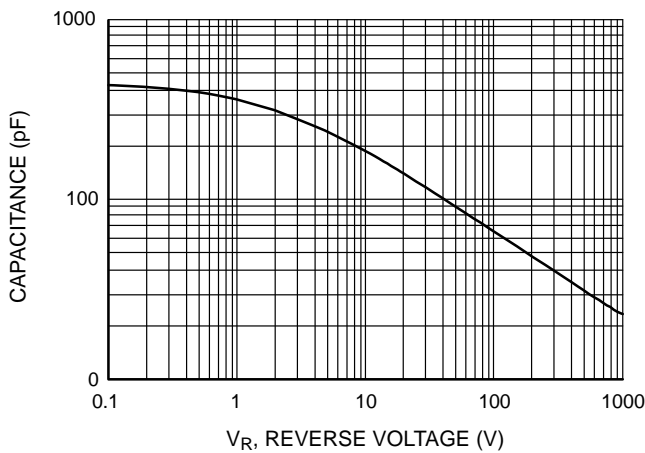


Figure 8. Capacitance vs. Reverse Voltage

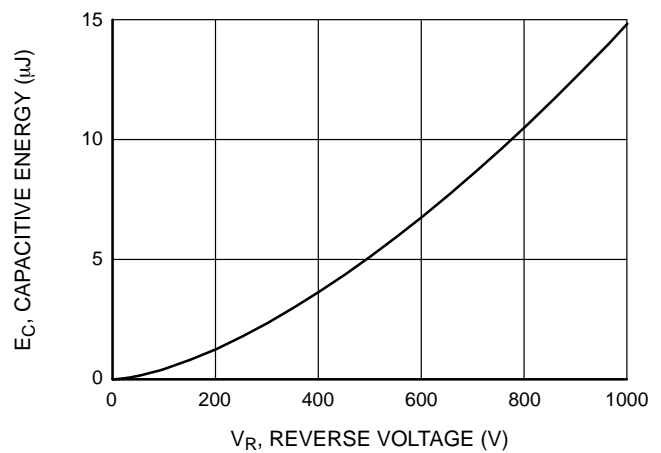


Figure 9. Capacitance Stored Energy

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