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TIP42 / TIP42C PNP Epitaxial Silicon Transistor

Features

- Medium Power Linear Switching Applications
- Complement to TIP41 Series



1.Base 2.Collector 3.Emitter

Ordering Information

Part Number	Top Mark	Package	Packing Method
TIP42	TIP42	TO-220 3L (Single Gauge)	Bulk
TIP42C	TIP42C	TO-220 3L (Single Gauge)	Bulk
TIP42CTU	TIP42C	TO-220 3L (Single Gauge)	Rail

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		Value	Unit	
V _{CBO}	Collector-Base Voltage	TIP42	-40	- V	
	Collector-base voltage	TIP42C	-100		
V _{CEO}	Collector-Emitter Voltage	TIP42	-40	V	
		TIP42C	-100		
V _{EBO}	Emitter-Base Voltage		-5	V	
I _C	Collector Current (DC)		-6	Α	
I _{CP}	Collector Current (Pulse)		-10	Α	
I _B	Base Current		-2	Α	
TJ	Junction Temperature		150	°C	
T _{STG}	Storage Temperature Range		-65 to 150	°C	

Thermal Characteristics

Values are at $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
P _C	Collector Dissipation (T _C = 25°C)	65	
	Collector Dissipation (T _A = 25°C)	2] vv

Electrical Characteristics

Values are at $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Max.	Unit
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage ⁽¹⁾	TIP42	I _C = -30 mA, I _B = 0	-40		V
		TIP42C		-100		
I _{CEO}	Collector Cut-Off Current	TIP42	$V_{CE} = -30 \text{ V}, I_{B} = 0$		-0.7	mA
		TIP42C	$V_{CE} = -60 \text{ V}, I_{B} = 0$		-0.7	
I _{CES}	Collector Cut-Off Current	TIP42	$V_{CE} = -40 \text{ V}, V_{EB} = 0$		-400	μΑ
		TIP42C	$V_{CE} = -100 \text{ V}, V_{EB} = 0$		-400	
I _{EBO}	Emitter Cut-Off Current		$V_{EB} = -5 \text{ V}, I_{C} = 0$		-1	mA
h _{FE}	DC Current Gain ⁽¹⁾		$V_{CE} = -4 \text{ V}, I_{C} = -0.3 \text{ A}$	30		
			$V_{CE} = -4 \text{ V}, I_{C} = -3 \text{ A}$	15	75	
V _{CE} (sat)	Collector-Emitter Saturation Voltage ⁽¹⁾		$I_C = -6 \text{ A}, I_B = -600 \text{ mA}$		-1.5	V
V _{BE} (on)	Base-Emitter On Voltage ⁽¹⁾		$V_{CE} = -4 \text{ V}, I_{C} = -6 \text{ A}$		-2.0	V
f _T	Current Gain Bandwidth Product		$V_{CE} = -10 \text{ V}, I_{C} = -500 \text{ mA},$ f = 1 MHz	3.0		MHz

Note:

1. Pulse test: pw \leq 300 μ s, duty cycle \leq 2%.

Typical Performance Characteristics

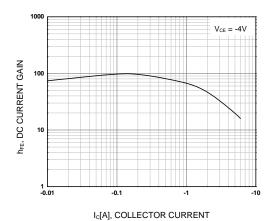
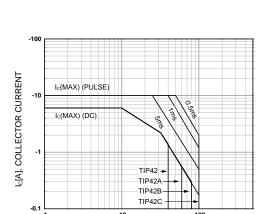


Figure 1. DC Current Gain



 $V_{\text{CE}}[V]$, COLLECTOR-EMITTER VOLTAGE Figure 3. Safe Operating Area

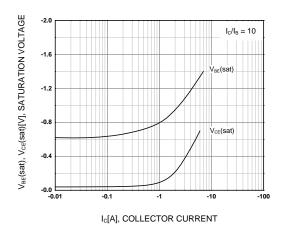


Figure 2. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

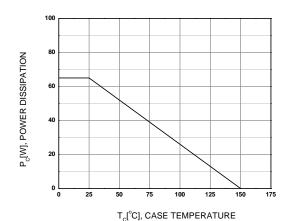
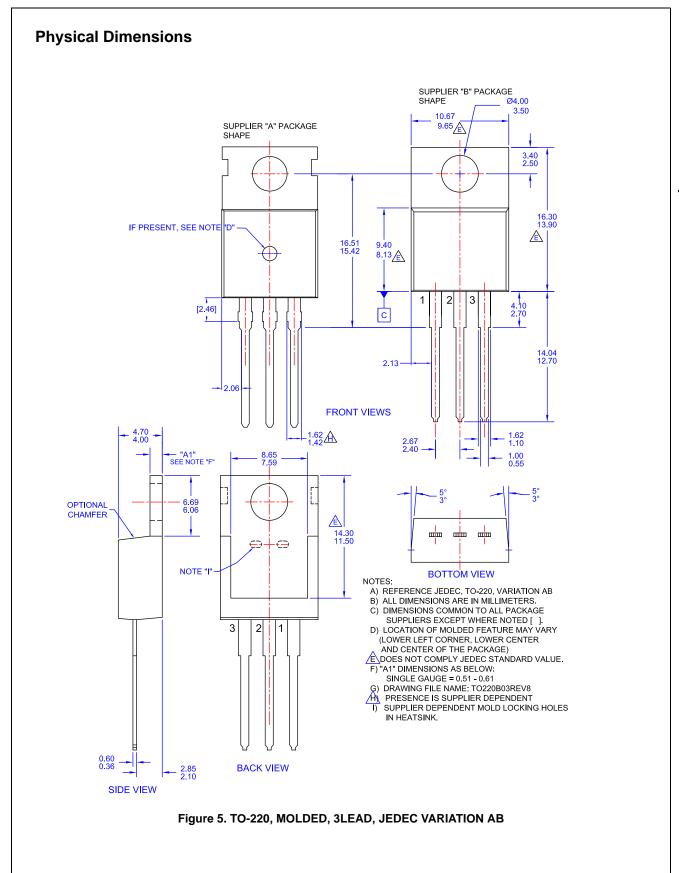


Figure 4. Power Derating



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