CERAMIC DIP 34, 50.8x50.8

CASE 125BK ISSUE O

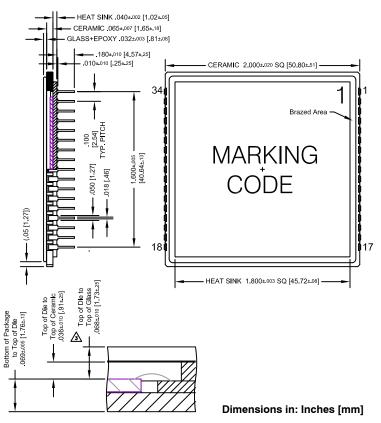
DATE 03 JUL 2014

NOTES:

- 1. Center of Active Image is at (+.046, +.078)mm with respect to Center of Package at (0,0).
- 2. Die is visually aligned within 1 degree of any package cavity edge.
- Cover glass placement not to overhang from outside edges of the package

GLASS: SQ. 1.900±.003 [48.26±.08] APERTURE: SQ. 1.520±.010 [38.60±.25] APERTURE: SQ. 1.520±.010 [38.60±.25] Center of Active Image See Note 1. 1.002±.015 [25.45±.38] Center of Image (.050 [1.27]) 2.010±.020 [51.05±.61]

SHOWN WITH SEALED COVER GLASS



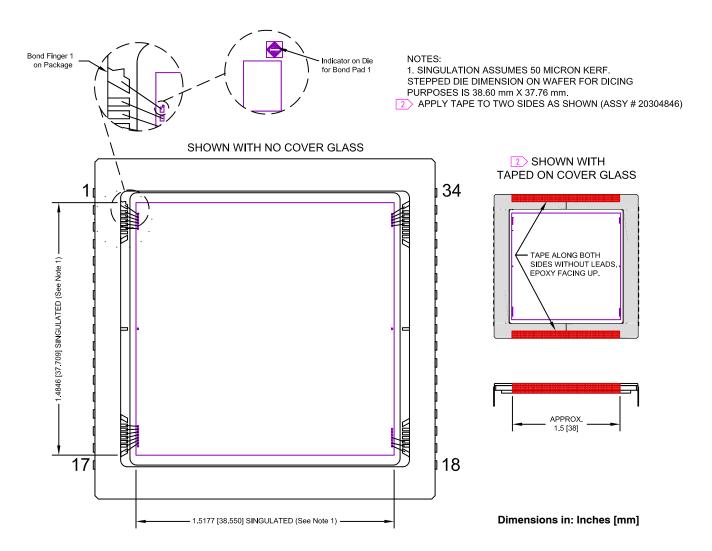
DOCUMENT NUMBER:	98AON87293F	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	CERAMIC DIP 34, 50.8x50.8		PAGE 1 OF 2

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

CERAMIC DIP 34, 50.8x50.8

CASE 125BK ISSUE O

DATE 03 JUL 2014



DOCUMENT NUMBER:	98AON87293F	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	CERAMIC DIP 34, 50.8x50.8		PAGE 2 OF 2

ON Semiconductor and III are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.