

# NTLUF4189NZ

## Power MOSFET and Schottky Diode

30 V, N-Channel with 0.5 A Schottky Barrier Diode, 1.6 x 1.6 x 0.55 mm  $\mu$ Cool™ Package

### Features

- Low Qg and Capacitance to Minimize Switching Losses
- Low Profile UDFN 1.6x1.6 mm for Board Space Saving
- Low VF Schottky Diode
- ESD Protected Gate
- This is a Halide-Free Device
- This is a Pb-Free Device

### Applications

- DC-DC Boost Converter
- Color Display and Camera Flash Regulators
- Optimized for Power Management Applications for Portable Products, such as Cell Phones, PMP, DSC, GPS, and others

### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise stated)

Parameter			Symbol	Value	Units
Drain-to-Source Voltage			V <sub>DSS</sub>	30	V
Gate-to-Source Voltage			V <sub>GS</sub>	±8.0	V
Continuous Drain Current (Note 1)	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	1.5	A
		T <sub>A</sub> = 85°C		1.1	
	t ≤ 5 s	T <sub>A</sub> = 25°C		1.9	
Power Dissipation (Note 1)	Steady State	T <sub>A</sub> = 25°C	P <sub>D</sub>	0.8	W
	t ≤ 5 s	T <sub>A</sub> = 25°C		1.3	
Continuous Drain Current (Note 2)	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	1.2	A
		T <sub>A</sub> = 85°C		0.9	
Power Dissipation (Note 2)		T <sub>A</sub> = 25°C	P <sub>D</sub>	0.5	W
Pulsed Drain Current		t <sub>p</sub> = 10 μs	I <sub>DM</sub>	8.0	A
MOSFET Operating Junction and Storage Temperature			T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C
Schottky Operating Junction & Storage Temperature			T <sub>J</sub> , T <sub>STG</sub>	-55 to 125	°C
Source Current (Body Diode) (Note 2)			I <sub>S</sub>	1.5	A
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			T <sub>L</sub>	260	°C
Gate-to-Source ESD Rating (HBM) per JESD22-A114F			ESD	1000	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
2. Surface-mounted on FR4 board using the minimum recommended pad size of 30 mm<sup>2</sup>, 2 oz. Cu.



ON Semiconductor®

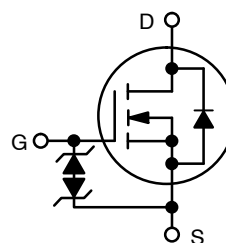
<http://onsemi.com>

### MOSFET

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> MAX	I <sub>D</sub> MAX
30 V	200 mΩ @ 4.5 V	1.5 A
	250 mΩ @ 3.0 V	0.5 A
	350 mΩ @ 2.5 V	0.5 A

### SCHOTTKY DIODE

V <sub>R</sub> MAX	V <sub>F</sub> TYP	I <sub>F</sub> MAX
30 V	0.52 V	0.5 A



N-Channel MOSFET

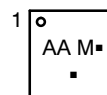


Schottky Diode

### MARKING DIAGRAM

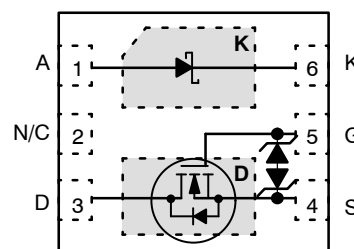


UDFN6  
CASE 517AT  
 $\mu$ COOL™



AA = Specific Device Code  
M = Date Code  
■ = Pb-Free Package

### PIN CONNECTIONS



(Top View)

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

# NTLUF4189NZ

## DEVICE ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
NTLUF4189NZTAG	UDFN6 (Pb-Free)	3000 / Tape & Reel
NTLUF4189NZTBG	UDFN6 (Pb-Free)	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## Schottky Diode Maximum Ratings (T<sub>J</sub> = 25°C unless otherwise stated)

Parameter	Symbol	Value	Units
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	30	V
DC Blocking Voltage	V <sub>R</sub>	30	V
Average Rectified Forward Current	I <sub>F</sub>	0.5	A

## Thermal Resistance Ratings

Parameter	Symbol	Max	Units
Junction-to-Ambient – Steady State (Note 3)	R <sub>θJA</sub>	155	°C/W
Junction-to-Ambient – t ≤ 5 s (Note 3)	R <sub>θJA</sub>	100	
Junction-to-Ambient – Steady State min Pad (Note 4)	R <sub>θJA</sub>	245	

## MOSFET Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
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### OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	30			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA, ref to 25°C		22		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 24 V	T <sub>J</sub> = 25°C		1.0	μA
					10	
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 8.0 V			10	μA

### ON CHARACTERISTICS (Note 5)

Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 250 μA	0.4	1.1	1.5	V
Negative Threshold Temp. Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>			3.0		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1.5 A		145	200	mΩ
		V <sub>GS</sub> = 3.0 V, I <sub>D</sub> = 0.5 A		185	250	
		V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 0.5 A		220	350	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> = 4.0 V, I <sub>D</sub> = 0.15 A		1.1		S

### CHARGES & CAPACITANCES

Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> = 0 V, f = 1 MHz, V <sub>DS</sub> = 15 V		95		pF
Output Capacitance	C <sub>OSS</sub>			15		
Reverse Transfer Capacitance	C <sub>RSS</sub>			10		
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 4.5 V, V <sub>DS</sub> = 15 V; I <sub>D</sub> = 1.5 A		1.4	3.0	nC
Threshold Gate Charge	Q <sub>G(TH)</sub>			0.2		
Gate-to-Source Charge	Q <sub>GS</sub>			0.4		
Gate-to-Drain Charge	Q <sub>GD</sub>			0.4		

- Surface-mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces)
- Surface-mounted on FR4 board using the minimum recommended pad size of 30 mm<sup>2</sup>, 2 oz. Cu.
- Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%
- Switching characteristics are independent of operating junction temperatures

# NTLUF4189NZ

## MOSFET Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
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### SWITCHING CHARACTERISTICS, V<sub>GS</sub> = 4.5 V (Note 6)

Turn-On Delay Time	t <sub>d(ON)</sub>	V <sub>GS</sub> = 4.5 V, V <sub>DD</sub> = 15 V, I <sub>D</sub> = 1 A, R <sub>G</sub> = 6 Ω		7.0		ns
Rise Time	t <sub>r</sub>			4.5		
Turn-Off Delay Time	t <sub>d(OFF)</sub>			10.2		
Fall Time	t <sub>f</sub>			1.2		

### DRAIN-SOURCE DIODE CHARACTERISTICS

Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 1 A	T <sub>J</sub> = 25°C		0.8	1.2	V
			T <sub>J</sub> = 85°C		0.75		
Reverse Recovery Time	t <sub>RR</sub>	V <sub>GS</sub> = 0 V, dI <sub>SD</sub> /dt = 100 A/μs, I <sub>S</sub> = 1 A		10.5			ns
Charge Time	t <sub>a</sub>			8.9			
Discharge Time	t <sub>b</sub>			1.6			
Reverse Recovery Charge	Q <sub>RR</sub>			2.1			nC

### SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Maximum Instantaneous Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA		0.27	0.37	V
		I <sub>F</sub> = 100 mA		0.36	0.46	
		I <sub>F</sub> = 500 mA		0.52	0.62	
Maximum Instantaneous Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 10 V		2.0	10	μA
		V <sub>R</sub> = 30 V		20	200	

### SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 85°C unless otherwise specified)

Maximum Instantaneous Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA		0.2		V
		I <sub>F</sub> = 100 mA		0.3		
		I <sub>F</sub> = 500 mA		0.51		
Maximum Instantaneous Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 10 V		80		μA
		V <sub>R</sub> = 30 V		525		

### SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 125°C unless otherwise specified)

Maximum Instantaneous Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA		0.14		V
		I <sub>F</sub> = 100 mA		0.27		
		I <sub>F</sub> = 500 mA		0.51		
Maximum Instantaneous Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 10 V		600		μA
		V <sub>R</sub> = 30 V		3000		

### SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise specified)

Capacitance	C	V <sub>R</sub> = 5 V, f = 1.0 MHz		6.0		pF
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- Surface-mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces)
- Surface-mounted on FR4 board using the minimum recommended pad size of 30 mm<sup>2</sup>, 2 oz. Cu.
- Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%
- Switching characteristics are independent of operating junction temperatures

TYPICAL MOSFET CHARACTERISTICS

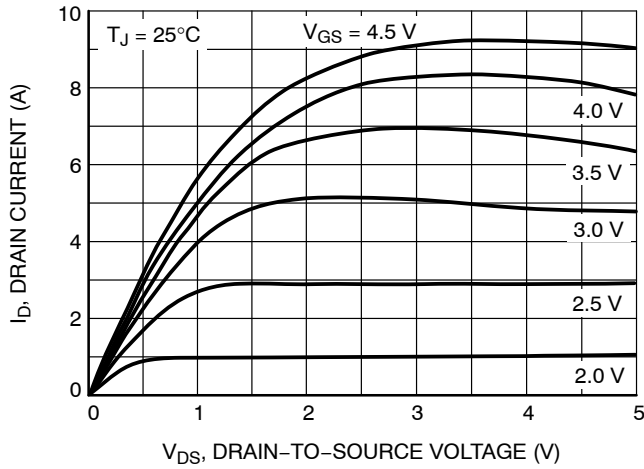


Figure 1. On-Region Characteristics

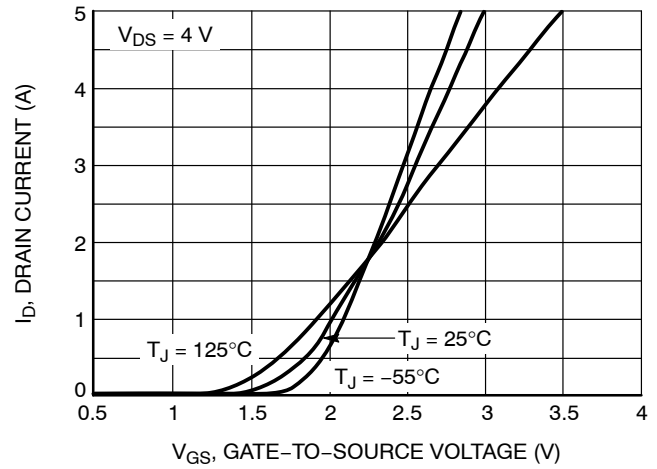


Figure 2. Transfer Characteristics

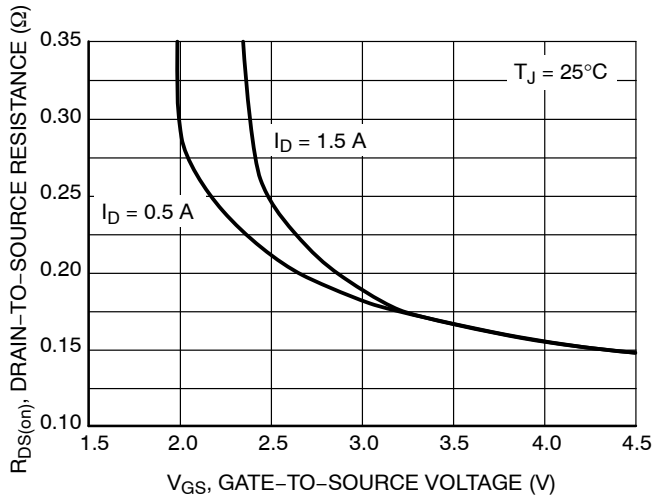


Figure 3. On-Resistance vs. Gate Voltage

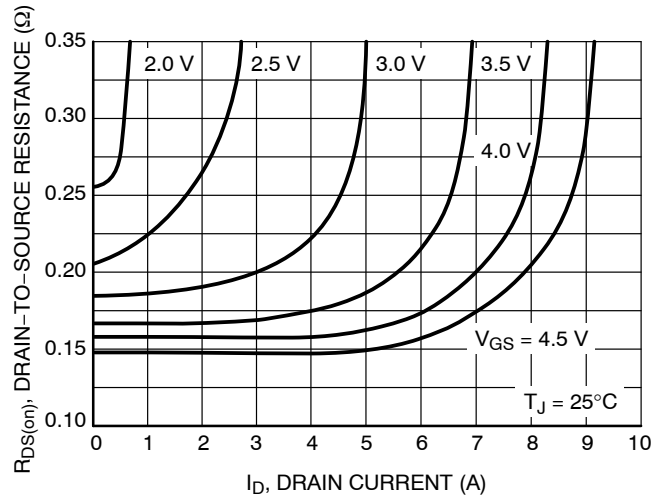


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

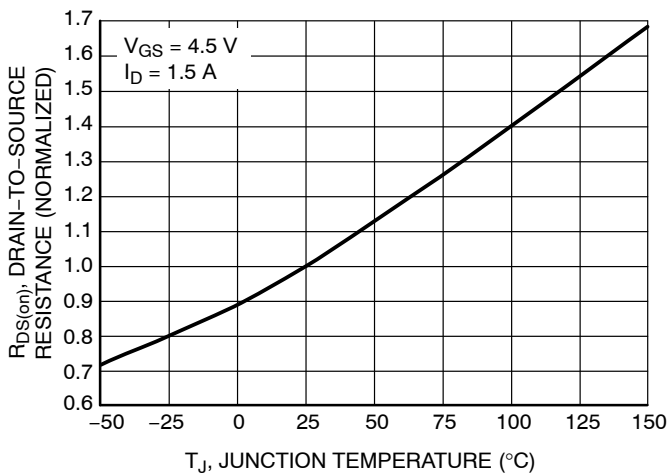


Figure 5. On-Resistance Variation with Temperature

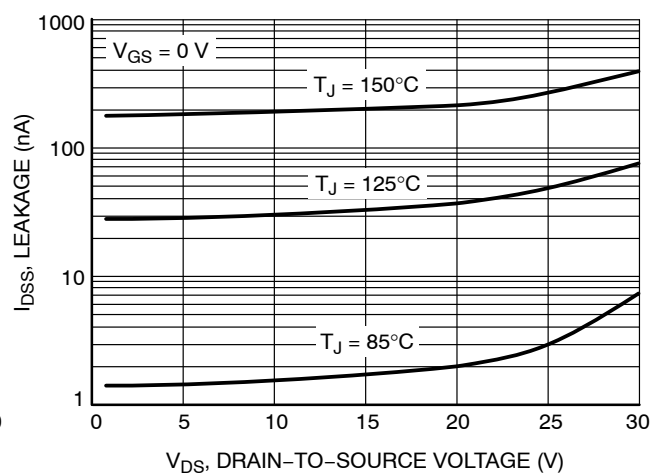


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL MOSFET CHARACTERISTICS

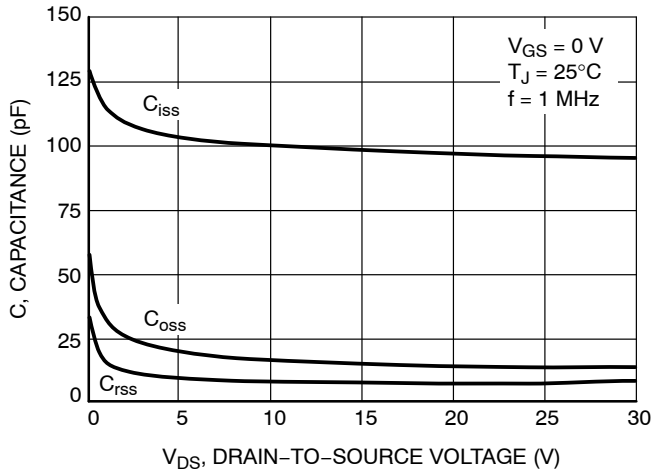


Figure 7. Capacitance Variation

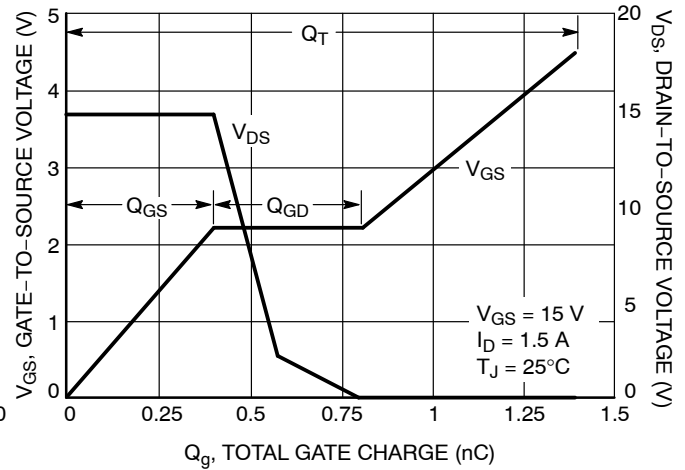


Figure 8. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

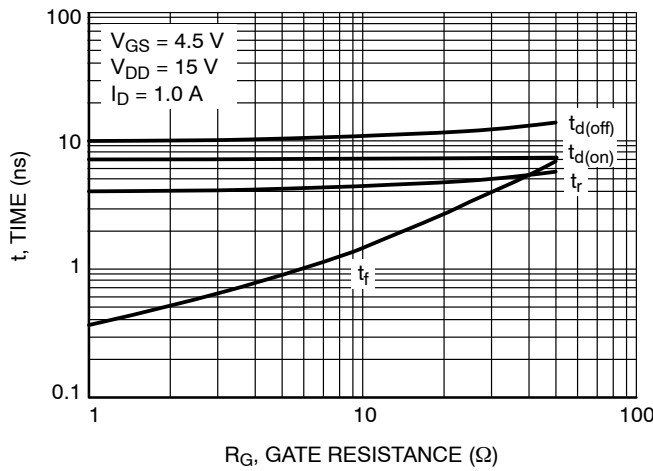


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

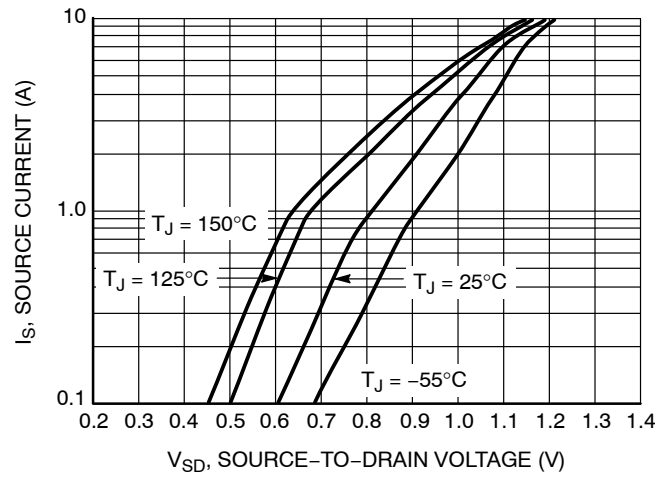


Figure 10. Diode Forward Voltage vs. Current

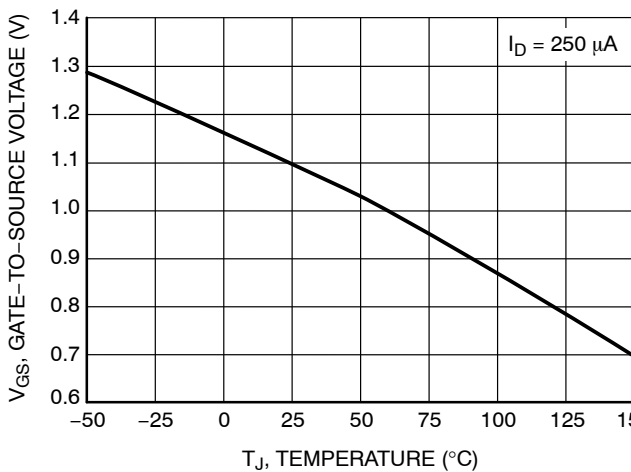


Figure 11. Threshold Voltage

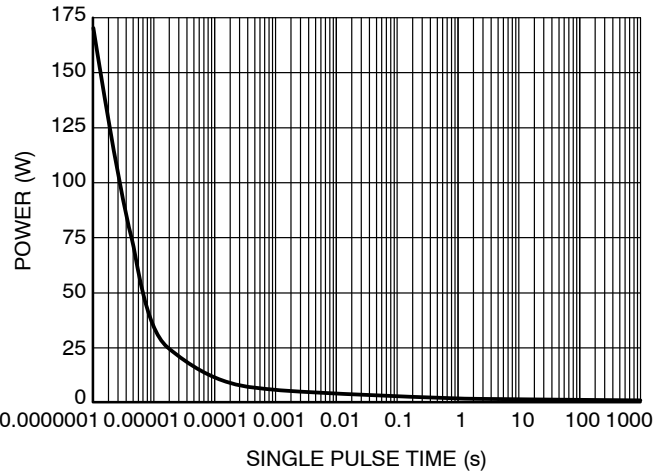


Figure 12. Single Pulse Maximum Power Dissipation

TYPICAL MOSFET CHARACTERISTICS

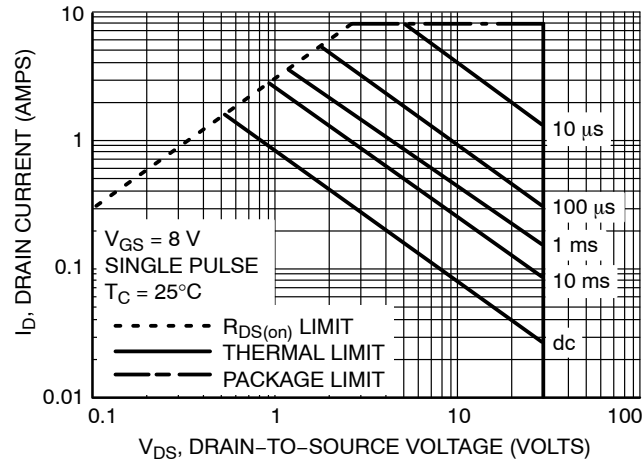


Figure 13. Maximum Rated Forward Biased Safe Operating Area

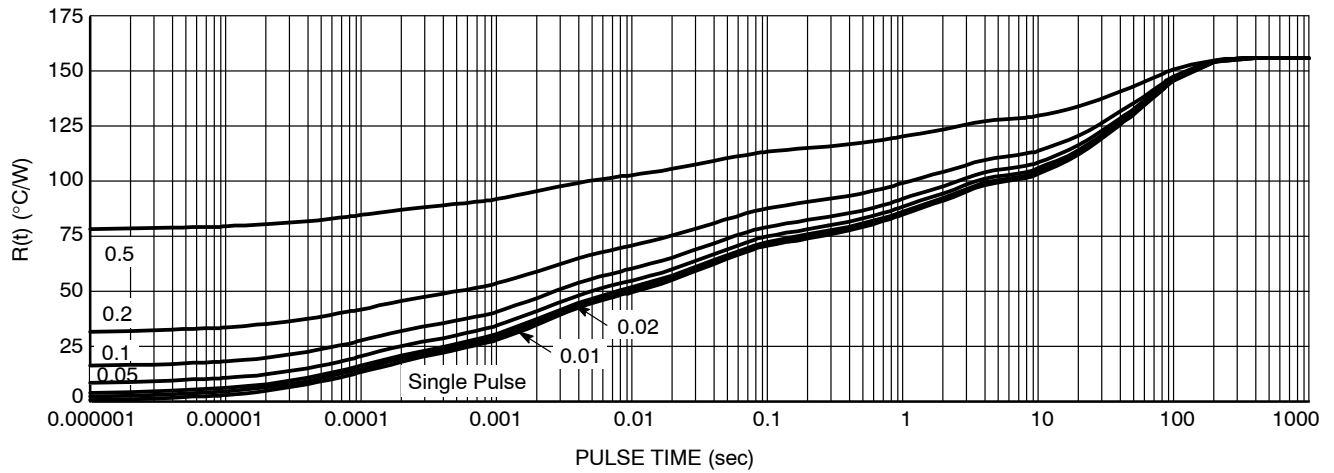


Figure 14. FET Thermal Response

TYPICAL SCHOTTKY CHARACTERISTICS

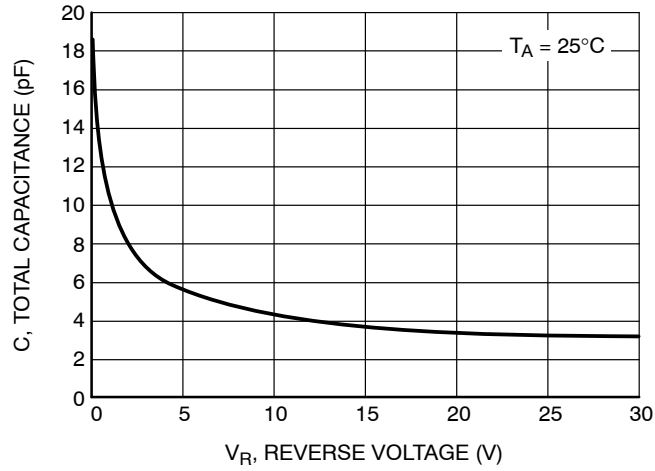


Figure 15. Total Capacitance

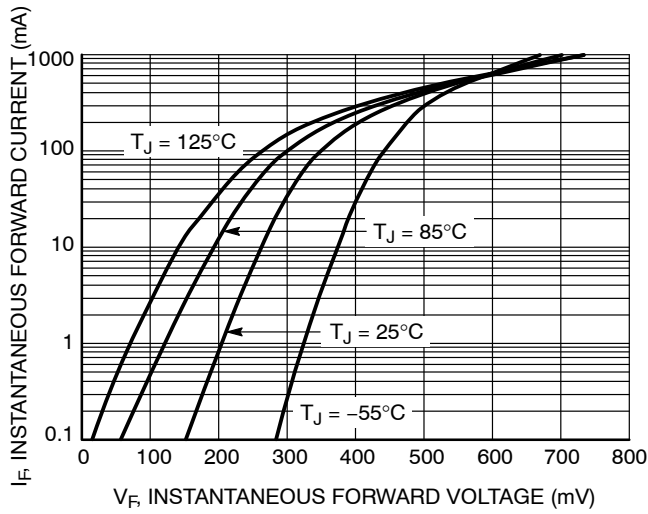


Figure 16. Typical Forward Voltage

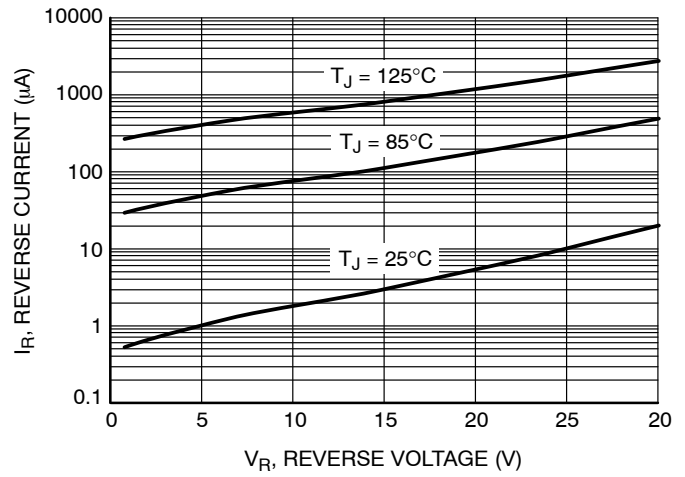
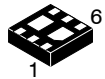


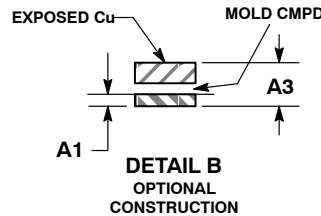
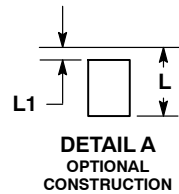
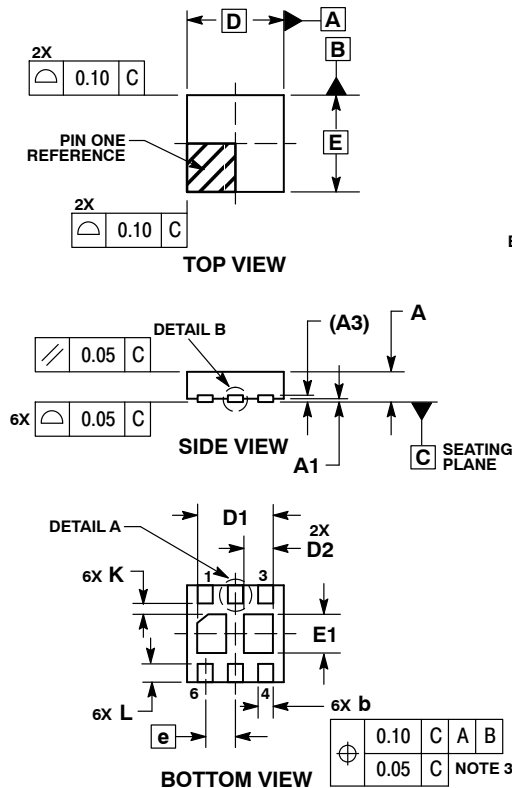
Figure 17. Typical Reverse Current



SCALE 4:1

UDFN6 1.6x1.6, 0.5P  
CASE 517AT  
ISSUE O

DATE 02 SEP 2008

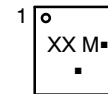


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM TERMINAL.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

MILLIMETERS		
DIM	MIN	MAX
A	0.45	0.55
A1	0.00	0.05
A3	0.13 REF	
b	0.20	0.30
D	1.60 BSC	
E	1.60 BSC	
e	0.50 BSC	
D1	1.14	1.34
D2	0.38	0.58
E1	0.54	0.74
K	0.20	---
L	0.15	0.35
L1	---	0.10

GENERIC  
MARKING DIAGRAM\*



XX = Specific Device Code

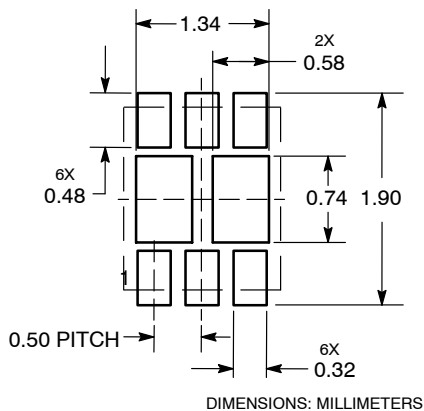
M = Date Code

▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

SOLDERMASK DEFINED  
MOUNTING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	UDFN6, 1.6X1.6, 0.5P	PAGE 1 OF 1

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