onsemi

Octal Bidirectional Transceiver with 3-STATE Inputs/Outputs

74AC245, 74ACT245

Description

The AC/ACT245 contains eight non-inverting bidirectional buffers with 3–STATE outputs and is intended for bus–oriented applications. Current sinking capability is 24 mA at both the A and B ports. The Transmit/Receive (T/\overline{R}) input determines the direction of data flow through the bidirectional transceiver. Transmit (active–HIGH) enables data from A ports to B ports; Receive (active–LOW) enables data from B ports to A ports. The Output Enable input, when HIGH, disables both A and B ports by placing them in a HIGH Z condition.

Features

- I_{CC} and I_{OZ} Reduced by 50%
- Non–Inverting Buffer
- Bidirectional Data Path
- A and B Outputs Source/Sink 24 mA
- ACT245 has TTL-Compatible Inputs
- These are Pb–Free Devices

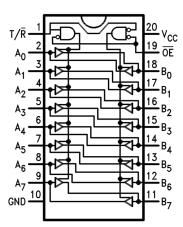
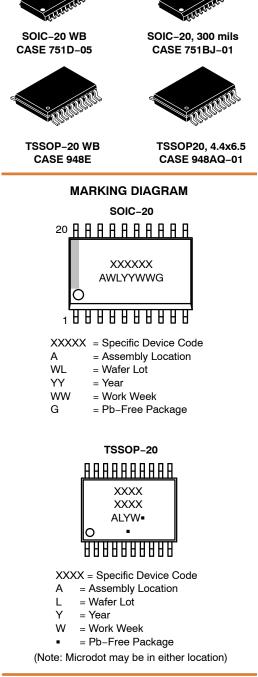


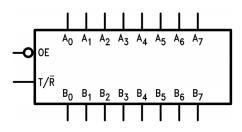
Figure 1. Connection Diagram

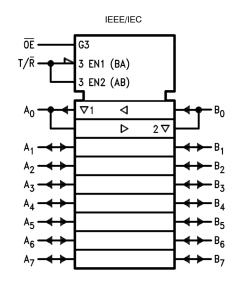


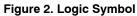
ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

74AC245, 74ACT245







PIN DESCRIPTIONS

Pin Name	Description
OE	Output Enable Input
T/R	Transmit / Receive Input
A ₀ -A ₇	Side A 3–STATE Inputs or 3–STATE Outputs
B ₀ –B ₇	Side B 3–STATE Inputs or 3–STATE Outputs

TRUTH TABLE

Input		
OE	TR	Output
L	L	Bus B Data to Bus A
L	Н	Bus A Data to Bus B
Н	Х	HIGH-Z State

H = HIGH Voltage Level L = LOW Voltage Level

X = Immaterial

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit
V _{CC}	Supply Voltage	-0.5 to +6.5	V
I _{IK}	DC Input Diode Current $V_1 = -0.5 V$	-20	mA
	$V_{I} = V_{CC} + 0.5$	+20	mA
VI	DC Input Voltage	–0.5 to V _{CC} + 0.5	V
Ι _{ΟΚ}	DC Output Diode Current $V_0 = -0.5 V$	-20	mA
	$V_{\rm O} = V_{\rm CC} + 0.5 \ V$	+20	mA
Vo	DC Output Voltage	-0.5 to V _{CC} + 0.5	V
Ι _Ο	DC Output Source or Sink Current	±50	mA
I _{CC} or I _{GND}	DC V _{CC} or Ground Current per Output Pin	±50	mA
T _{STG}	Storage Temperature	–65 to +150	°C
Т _Ј	Junction Temperature	140	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Rating	Unit
V _{CC}	Supply Voltage AC	2.0 to 6.0	V
	ACT	4.5 to 5.5	V
VI	Input Voltage	0 to V _{CC}	V
Vo	Output Voltage	0 to V _{CC}	V
T _A	Operating Temperature	-40 to +85	°C
$\Delta V / \Delta t$	Minimum Input Edge Rate, AC Devices: V _{IN} from 30% to 70% of V _{CC} , V _{CC} @ 3.3 V, 4.5 V, 5.5 V	125	mV/ns
$\Delta V / \Delta t$	Minimum Input Edge Rate, ACT Devices: V _{IN} from 0.8 V to 2.0 V, V _{CC} @ 4.5 V, 5.5 V	125	mV/ns

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

DC ELECTRICAL CHARACTERISTICS FOR AC

				T _A = +	⊦25°C	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	
Symbol	Parameter	V _{cc} (V)	Conditions	Тур.	GL	aranteed Limits	Unit
V _{IH}	Minimum HIGH Level Input Voltage	3.0	$V_{OUT}{=}0.1$ V or $V_{CC}{-}0.1$ V	1.5	2.1	2.1	V
		4.5		2.25	3.15	3.15	
		5.5		2.75	3.85	3.85	
V _{IL}	Maximum LOW Level Input Voltage	3.0	$V_{OUT}{=}0.1$ V or V_{CC} – 0.1 V	1.5	0.9	0.9	V
		4.5		2.25	1.35	1.35	
		5.5		2.75	1.65	1.65	
V _{OH}	Minimum HIGH Level Output Voltage	3.0	I _{OUT} = -50 μA	2.99	2.9	2.9	V
		4.5		4.49	4.4	4.4	
		5.5		5.49	5.4	5.4	
		3.0	$V_{IN} = V_{IL}$ or V_{IH} , $I_{OH} = -12 \text{ mA}$	-	2.56	2.46	
		4.5	$V_{IN} = V_{IL}$ or V_{IH} , $I_{OH} = -24$ mA	-	3.86	3.76	
		5.5	V _{IN} = V _{IL} or V _{IH} , I _{OH} = -24 mA (Note 1)	-	4.86	4.76	
V _{OL}	Maximum LOW Level Output Voltage	3.0	I _{OUT} = 50 μA	0.002	0.1	0.1	V
		4.5		0.001	0.1	0.1	
		5.5		0.001	0.1	0.1	
		3.0	$V_{IN} = V_{IL}$ or V_{IH} , $I_{OL} = 12 \text{ mA}$	-	0.36	0.44	
		4.5	$V_{IN} = V_{IL}$ or V_{IH} , $I_{OL} = 24$ mA	-	0.36	0.44	
		5.5	$V_{IN} = V_{IL}$ or V_{IH} , $I_{OL} = 24 \text{ mA}$ (Note 1)	-	0.36	0.44	
I _{IN} (Note 2)	Maximum Input Leakage Current	5.5	$V_I = V_{CC}$, GND	-	±0.1	±1.0	μΑ
I _{OLD}	Minimum Dynamic Output Current	5.5	V _{OLD} = 1.65 V Max.	-	-	75	mA
I _{OHD}	(Note 3)	5.5	V _{OHD} = 3.85 V Min.	-	-	-75	mA
I _{CC} (Note 2)	Maximum Quiescent Supply Current	5.5	$V_{IN} = V_{CC}$ or GND	-	4.0	40.0	μΑ
I _{OZT}	Maximum I/O Leakage Current	5.5		-	±0.3	±0.3	μΑ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. All outputs loaded; thresholds on input associated with output under test.

2. I_{IN} and I_{CC} @ 3.0 V are guaranteed to be less than or equal to the respective limit @ 5.5 V V_{CC}. 3. Maximum test duration 2.0 ms, one output loaded at a time.

74AC245, 74ACT245

DC ELECTRICAL CHARACTERISTICS FOR ACT

					-25°C	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	
Symbol	Parameter	V _{cc} (V)	Conditions	Typ. G		uaranteed Limits	Unit
VIH	Minimum HIGH Level Input Voltage	4.5	$V_{OUT} = 0.1 \text{ V or } V_{CC} = 0.1 \text{ V}$	1.5	2.0	2.0	V
		5.5		1.5	2.0	2.0	
VIL	Maximum LOW Level Input Voltage	4.5	$V_{OUT} = 0.1 \text{ V or } V_{CC} = 0.1 \text{ V}$	1.5	0.8	0.8	V
		5.5		1.5	0.8	0.8	
V _{OH}	Minimum HIGH Level Output	4.5	I _{OUT} = -50 μA	4.49	4.4	4.4	V
	Voltage	5.5		5.49	5.4	5.4	
		4.5	$V_{IN} = V_{IL} \text{ or } V_{IH},$ $I_{OH} = -24 \text{ mA}$	-	3.86	3.76	
		5.5	$V_{IN} = V_{IL}$ or V_{IH} , $I_{OH} = -24mA$ (Note 4)	-	4.86	4.76	
V _{OL}	Maximum LOW Level Output	4.5	I _{OUT} = 50 μA	0.001	0.1	0.1	V
	Voltage	5.5		0.001	0.1	0.1	
		4.5	$V_{IN} = V_{IL}$ or V_{IH} , $I_{OL} = 24 \text{ mA}$	-	0.36	0.44	
		5.5	$V_{IN} = V_{IL}$ or V_{IH} , $I_{OL} = 24$ mA (Note 4)	-	0.36	0.44	
I _{IN}	Maximum Input Leakage Current	5.5	$V_I = V_{CC}, GND$	-	±0.1	±1.0	μΑ
I _{CCT}	Maximum I _{CC} /Input	5.5	V _I = V _{CC} -2.1V	0.6	-	1.5	mA
I _{OLD}	Minimum Dynamic Output Current	5.5	V _{OLD} = 1.65 V Max.	-	-	75	mA
I _{OHD}	(Note 5)	5.5	V _{OHD} = 3.85 V Min.	-	-	-75	mA
I _{CC}	Maximum Quiescent Supply Current	5.5	$V_{IN} = V_{CC}$ or GND	-	4.0	40.0	μΑ
I _{OZT}	Maximum I/O Leakage Current	5.5		-	±0.3	±3.0	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. All outputs loaded; thresholds on input associated with output under test.

5. Maximum test duration 2.0 ms, one output loaded at a time.

AC ELECTRICAL CHARACTERISTICS FOR AC

		V _{CC} (V)		T _A = +25°C C _L = 50 pF			C to +85°C 50 pF	
Symbol	Parameter	(Note 6)	Min.	Тур.	Max.	Min.	Max.	Unit
t _{PLH}	Propagation Delay, An to B_n or B_n to A_n	3.3	1.5	5.0	8.5	1.0	9.0	ns
		5.0	1.5	3.5	6.5	1.0	7.0	
t _{PHL}	Propagation Delay, A_n to B_n or B_n to A_n	3.3	1.5	5.0	8.5	1.0	9.0	ns
		5.0	1.5	3.5	6.0	1.0	7.0	
t _{PZH}	Output Enable Time	3.3	2.5	7.0	11.5	2.0	12.5	ns
		5.0	1.5	5.0	8.5	1.0	9.0	
t _{PZL}	Output Enable Time	3.3	2.5	7.5	12.0	2.0	13.5	ns
		5.0	1.5	5.5	9.0	1.0	9.5	
t _{PHZ}	Output Disable Time	3.3	2.0	6.5	12.0	1.0	12.5	ns
		5.0	1.5	5.5	9.0	1.0	10.0	
t _{PLZ}	Output Disable Time	3.3	2.0	7.0	11.5	1.5	13.0	ns
		5.0	1.5	5.5	9.0	1.0	10.0	1

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 6. Voltage range 3.3 is $3.3 \text{ V} \pm 0.3 \text{ V}$. Voltage range 5.0 is $5.0 \text{ V} \pm 0.5 \text{ V}$.

74AC245, 74ACT245

AC ELECTRICAL CHARACTERISTICS FOR ACT

		V _{cc} (V)	$ \begin{array}{c c} T_{A} = +25^{\circ}C & T_{A} = -40^{\circ}C \text{ to } +85^{\circ}C \\ C_{L} = 50 \text{ pF} & C_{L} = 50 \text{ pF} \end{array} $					
Symbol	Parameter	(Note 7)	Min.	Тур.	Max.	Min.	Max.	Unit
t _{PLH}	Propagation Delay, A_n to B_n or B_n to A_n	5.0	1.5	4.0	7.5	1.5	8.0	ns
t _{PHL}	Propagation Delay, A_n to B_n or B_n to A_n	5.0	1.5	4.0	8.0	1.0	9.0	ns
t _{PZH}	Output Enable Time	5.0	1.5	5.0	10.0	1.5	11.0	ns
t _{PZL}	Output Enable Time	5.0	1.5	5.5	10.0	1.5	12.0	ns
t _{PHZ}	Output Disable Time	5.0	1.5	5.5	10.0	1.0	11.0	ns
t _{PLZ}	Output Disable Time	5.0	2.0	5.0	10.0	1.5	11.0	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 7. Voltage Range 5.0 is 5.0 V ±0.5 V.

CAPATICANCE

Symbol	Parameter	Conditions	Тур.	Unit
C _{IN}	Input Capacitance	V _{CC} = OPEN	4.5	pF
C _{I/O}	Input/Output Capacitance	$V_{CC} = 5.0 V$	15.0	pF
C _{PD}	Power Dissipation Capacitance	$V_{CC} = 5.0 V$	45.0	рF

ORDERING INFORMATION

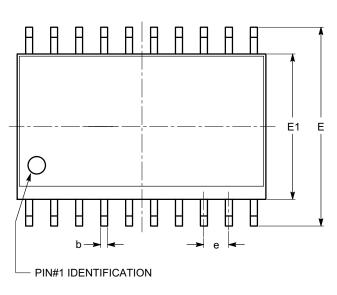
Device	Marking	Package	Shipping [†]
74AC245MTC	AC 245	TSSOP-20	75 Units / Tube
74AC245MTCX	AC 245	TSSOP-20	2500 / Tape & Reel
74AC245SCX	AC245	SOIC-20	1000 / Tape & Reel
74ACT245MTCX	ACT 245	TSSOP-20 2500	
74ACT245SC	ACT245	SOIC-20	38 Units / Tube
74ACT245SCX	ACT245	SOIC-20	1000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

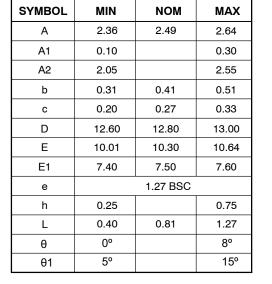


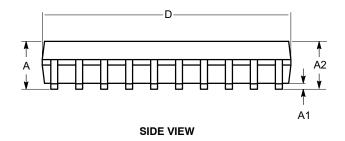
SOIC-20, 300 mils CASE 751BJ ISSUE O

DATE 19 DEC 2008



TOP VIEW

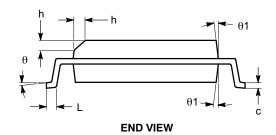




Notes:

(1) All dimensions are in millimeters. Angles in degrees.

(2) Complies with JEDEC MS-013.



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SOIC-20 WB

DATE 22 APR 2015

- NOTES:
 DIMENSIONS ARE IN MILLIMETERS.
 INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD
- DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSION. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE. DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF B DIMENSION AT MAXIMUM MATERIAL DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIMETERS				
DIM	MIN	MAX			
Α	2.35	2.65			
A1	0.10	0.25			
b	0.35	0.35 0.49			
C	0.23	0.32			
D	12.65	12.95			
E	7.40	7.60			
е	1.27	BSC			
н	10.05	10.55			
h	0.25	0.75			
L	0.50	0.90			
θ	0 °	7 °			

GENERIC **MARKING DIAGRAM***

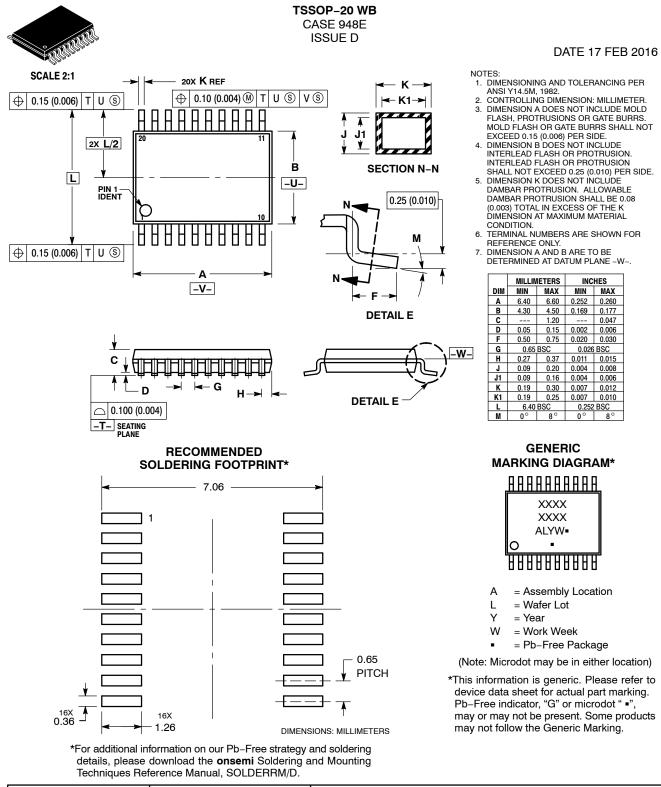
20	A	<u> </u>	a
	С	XXXXXXXXXXXX XXXXXXXXXXXX AWLYYWWG	
1 1	H	88888888	J
A W Y	′L Y	 (XX = Specific Device (= Assembly Locati Wafer Lot Year Work Week 	
Ŵ	W	/ = Work Week	

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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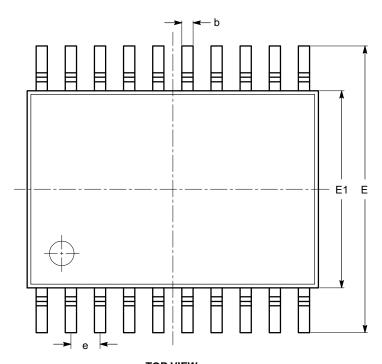
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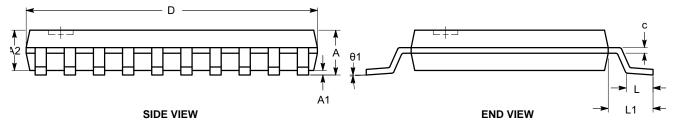
TSSOP20, 4.4x6.5 CASE 948AQ ISSUE A

DATE 19 MAR 2009



SYMBOL	MIN	NOM	MAX
STMBOL			
А			1.20
A1	0.05		0.15
A2	0.80		1.05
b	0.19		0.30
С	0.09		0.20
D	6.40	6.50	6.60
Е	6.30	6.40	6.50
E1	4.30	4.40	4.50
е		0.65 BSC	
L	0.45	0.60	0.75
L1		1.00 REF	
θ	0°		8°





Notes:

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