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FCH25N60N

N 沟道 SupreMOS[®] MOSFET 600 V, 25 A, 126 mΩ

特性

- R_{DS}(on) = 108 mΩ (Typ.)@ V_{GS} = 10 V, I_D = 12.5 A
- 超低栅极电荷 (典型值 Q_a = 57 nC)
- 低有效输出电容 (典型值 Coss(eff.)= 262 pF)
- 100% 经过雪崩测试
- 符合 RoHS 标准

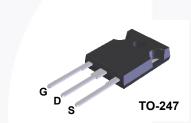
应用

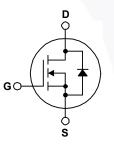
- 太阳能逆变器
- AC-DC 电源



说明

SupreMOS[®] MOSFET 是飞兆半导体的下一代高压超级结(SJ) 技术,该技术采用区别于传统 SJ MOSFET 产品的深沟槽填充工 艺。这项先进技术和精密的工艺控制提供了最低的 Rsp onresistance(导通电阻规格),卓越的开关性能和耐用性。 SupreMOS MOSFET 产品非常适合高频开关电源转换器应用, 如功率因数校正 (PFC)、服务器 / 电信电源、平板电视电源、ATX 电源及工业电源应用。





MOSFET 最大额定值 TC=25°C 除非另有说明。

符号		参数	FCH25N60N	单位	
V _{DSS}	漏极一源极电压	600	V		
V _{GSS}	栅极一源极电压	±30	V		
ID	漏极电流	- 连续 (T _C = 25°C)	25		
		- 连续 (T _C = 100°C)	16	— A	
I _{DM}	漏极电流	- 脉冲 (说即	明1) 75	A	
E _{AS}	单脉冲雪崩能量 (说明 2)		明 2) 861	mJ	
I _{AR}	雪崩电流 (说明 1)		明1) 8.3	Α	
E _{AR}	重复雪崩能量	明1) 2.2	mJ		
dv/dt	MOSFET dv/dt	100	V/ns		
	峰值二极管恢复 dv/dt	明3) 20	V/ns		
P _D	功耗	(T _C = 25°C)	216	W	
		- 降低至 25°C 以上	1.72	W/°C	
T _J , T _{STG}	工作和存储温度范围		-55 至 +150	°C	
ΤL	用于焊接的最大引线温度,距离外壳 1/8",持续 5 秒		300	°C	

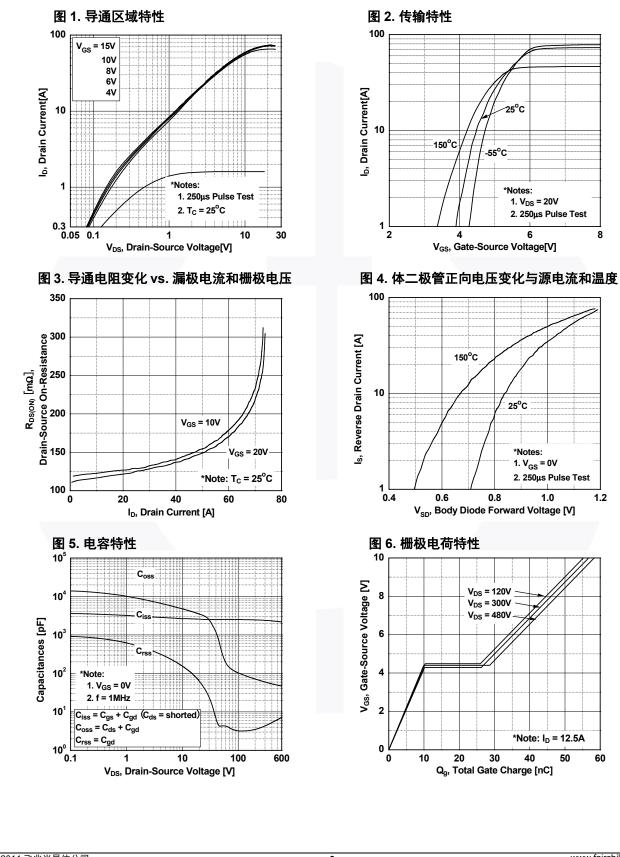
热性能

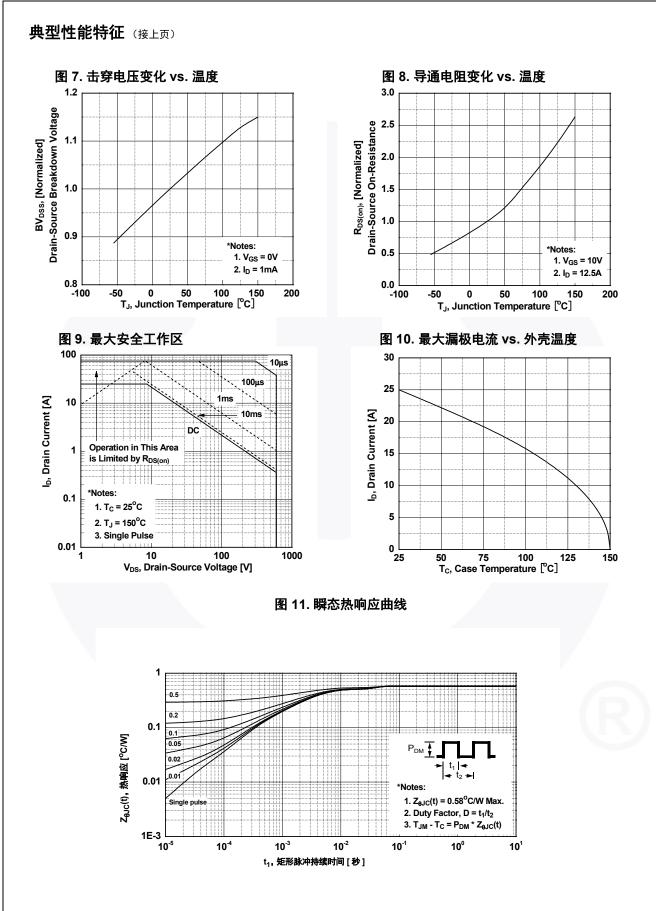
符号	参数	FCH25N60N	单位	
$R_{\theta JC}$	结至外壳热阻最大值	0.58	°C/W	
$R_{ extsf{ heta}JA}$	结至环境热阻最大值	40	C/W	

88 11 2	扁号	顶标	封装	包装方法	卷尺寸		带宽	ž	数量
FCH25N60N FCH25N60N		TO-247	塑料管	不适用		不适用	30 单元		
电气特性	TC = 25°C	除非另有说明。							
符号		参数		测试条件		最小值	典型值	最大值	单位
关断特性									
BV _{DSS}	漏极一源极击穿电压		lo =	I _D = 1 mA, V _{GS} = 0 V,T _J = 25°C		600	-	-	V
ABV _{DSS}	击穿电压						0.74		-
$/\Delta T_J$			_	I _D = 1 mA,温度参考 25°C			0.74	-	V/°C
DSS	雯栅极由	压 湿极由		$_{\rm S}$ = 480 V, V _{GS} = 0 V		-	-	10	μA
088	受1001次已	零栅极电压漏极电流		_S = 480 V, T _J = 125°C		-	-	100	μΛ
GSS	栅极 - 体注	扇电流	V _G	$_{\rm S}$ = ±30 V, V _{DS} = 0 V		-	-	±100	nA
导通特性									
V _{GS(th)}	栅极阈值	由	Va	_S = V _{DS} , I _D = 250 μA		2.0	_	4.0	V
R _{DS(on)}		^{屯压} 极静态导通电阻		$s = 10 \text{ V}, I_D = 12.5 \text{ A}$	-	-	0.108	0.126	Ω
- DS(011)	//雨似土//小	饭静心守 遗屯阻	• 6	5			01100	020	
动态特性									
C _{iss}	输入电容		V	V _{DS} = 100 V, V _{GS} = 0 V, f = 1 MHz		-	2520	3352	pF
C _{oss}	输出电容					-	103	137	pF
C _{rss}	反向传输	电容				-	3.2	5	pF
C _{oss}	输出电容 有效输出电容			V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz			55	-	pF
C _{oss(eff.)}			V _D	V _{DS} = 0 V 至 480 V, V _{GS} = 0 V		-	262	-	pF
Q _{g(tot)}		服电荷总量	V _D	_S = 380 V, I _D = 12.5 A			57	74	nC
Q _{gs}		极栅极电荷	V _G	V _{GS} = 10 V (说明 4)		-	10	-	nC
Q _{gd}		极 " 米勒 " 电荷				-	18	-	nC
ESR	等效串联	电阻 (G-S)	t =	1 MHz		-	1	-	Ω
开关特性									
t _{d(on)}	导通延迟						21	52	ns
tr	开通上升		V _{DI}	V _{DD} = 380 V, I _D = 12.5 A, V _{GS} = 10 V, R _G = 4.7 Ω (说明 4)			22	54	ns
t _{d(off)}	关断延迟		V _G			-	68	146	ns
<u>t</u> f	关断下降					-	5	20	ns
屚极 - 源极	一极答特	杜							
I _S			流			-	-	25	Α
I _{SM}	漏极 - 源极二极管最大正向连续电流 漏极 - 源极二极管最大正向脉冲电流					-	-	75	Α
	漏极 - 源极二极管嵌入正问称冲电流			V _{GS} = 0 V, I _{SD} = 12.5 A		-	-	1.2	V
V _{SD}	反向恢复		$V_{GS} = 0 V, I_{SD} = 12.5 A,$		-	370	-	ns	
V _{SD} t _{rr}		电荷		/dt = 100 A/µs		-	7	-	μC

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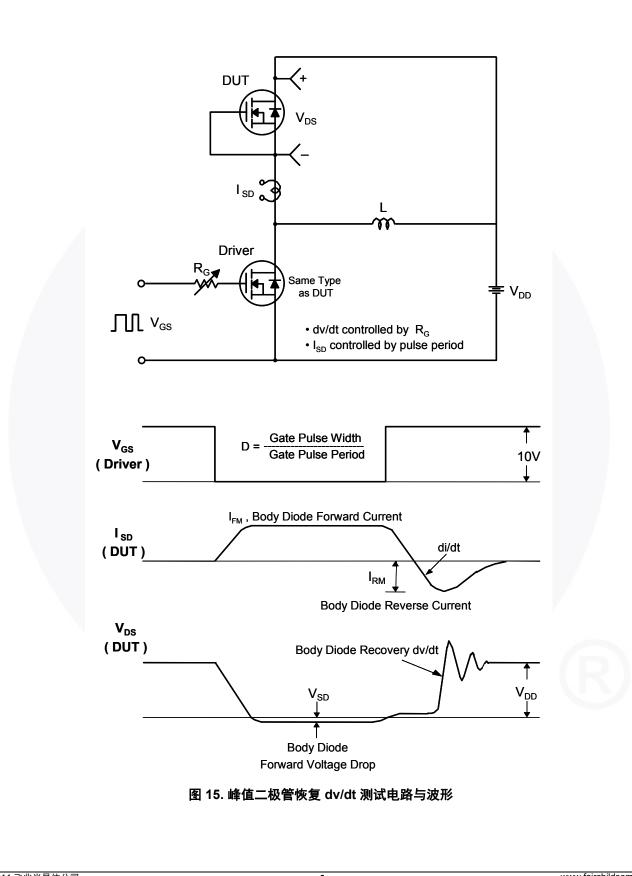
典型性能特征

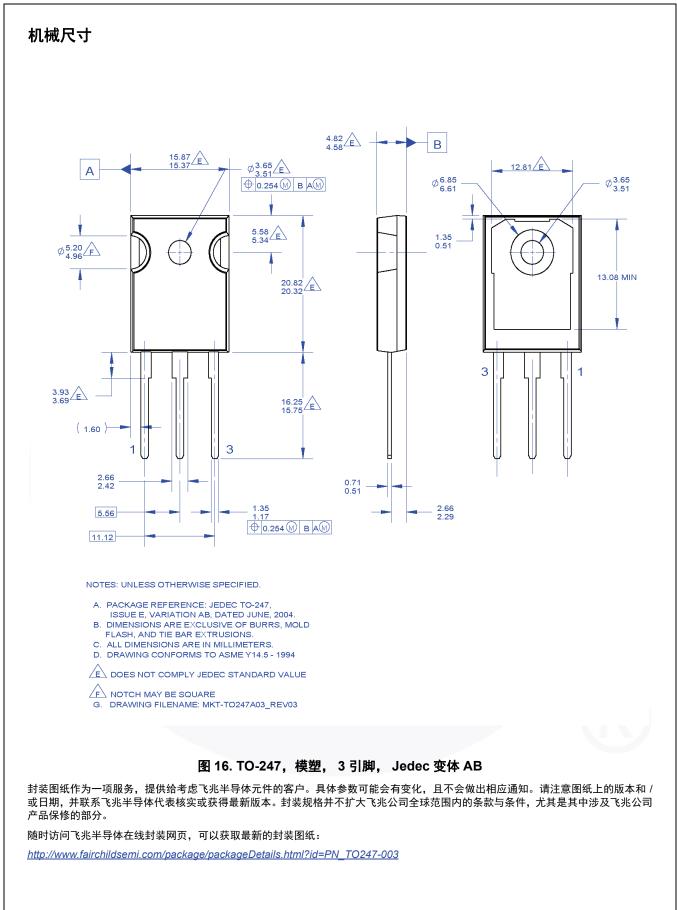




 V_{GS} ≲ק Q_g FV_{DS} Q_{gd} Q_{gs} GS • DUT l_G = 常量 Ŧ Charge 图 12. 栅极电荷测试电路与波形 R VDS VDS 90% ο V_{DD} V_{GS} R_{G} 10% V_{GS} DUT V_{GS} ∏ a 图 13. 阻性开关测试电路与波形 L $E_{AS} = \frac{1}{2} L I_{AS}^2$ VDS $\mathsf{BV}_{\mathsf{DSS}}$ ID a I_{AS} R_{G} ∔v₀ I_D (t) V_{GS} [$V_{DS}(t)$ V_{DD} DUT Time t_p 图 14. 非箝位感性开关测试电路与波形

FCH25N60N — N 沟道 SupreMOS[®] MOSFET







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R	MicroFET™	SMART START™
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