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FDB024N04AL7

N-Channel PowerTrench[®] MOSFET

40 V, 219 A, 2.4 mΩ

特性

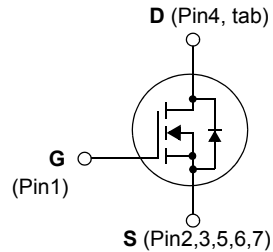
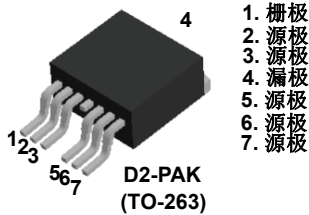
- $R_{DS(on)} = 2.0 \text{ m}\Omega$ (典型值) @ $V_{GS} = 10\text{V}$, $I_D = 80 \text{ A}$
- 快速开关速度
- 低栅极电荷
- 高性能沟道技术可实现极低的 $R_{DS(on)}$
- 高功率和高电流处理能力
- 符合 RoHS 标准

描述

此 N 沟道 MOSFET 采用飞兆半导体先进的 PowerTrench 工艺生产，这一先进工艺是专为最大限度地降低导通电阻并保持卓越开关性能而定制的。

应用

- 用于 ATX/ 服务器 / 电信 PSU 的同步整流
- 电池保护电路
- 电机驱动和不间断电源



MOSFET 最大额定值 $T_C = 25^\circ\text{C}$ 除非另有说明。

符号	参数	额定值	单位
V_{DSS}	漏极-源极电压	40	V
V_{GSS}	栅极-源极电压	± 20	V
I_D	漏极电流	- 连续 ($T_C = 25^\circ\text{C}$, 硅限制)	219*
		- 连续 ($T_C = 100^\circ\text{C}$, 硅限制)	155*
		- 连续 ($T_C = 25^\circ\text{C}$, 封装限制)	100
I_{DM}	漏极电流	- 脉冲 (注 1)	876
E_{AS}	单脉冲雪崩能量	(注 2)	864
dv/dt	二极管恢复 dv/dt 峰值	(注 3)	6.0
P_D	功耗	($T_C = 25^\circ\text{C}$)	214
		- 高于 25°C 的功耗系数	1.43
T_J, T_{STG}	工作和存储温度范围	-55 至 +175	$^\circ\text{C}$
T_L	用于焊接的最高引脚温度, 距离外壳 1/8", 持续 5 秒	300	$^\circ\text{C}$

* 连续电流是基于最高可允许的结温计算所得。封装限制电流为 120 A。

热性能

符号	参数	额定值	单位
$R_{\theta JC}$	结点 - 壳体的热阻	0.7	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	结至环境热阻	62.5	

封装标识与订购信息

器件标识	器件	封装	卷尺寸	带宽	数量
FDB024N04A	FDB024N04AL7	D2-PAK-7L	330mm	24mm	800

电气特性 $T_C = 25^\circ\text{C}$ 除非另有说明

符号	参数	测试条件	最小值	典型值	最大值	单位
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关断特性

BV_{DSS}	漏极-源极击穿电压	$I_D = 250 \mu\text{A}, V_{GS} = 0 \text{V}, T_C = 25^\circ\text{C}$	40	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	击穿电压温度系数	$I_D = 250 \mu\text{A}$, 参考 25°C 数值	-	30	-	$\text{mV}/^\circ\text{C}$
I_{DSS}	零栅极电压漏极电流	$V_{DS} = 32 \text{V}, V_{GS} = 0 \text{V}$	-	-	10	μA
		$V_{DS} = 32 \text{V}, T_C = 150^\circ\text{C}$	-	-	500	
I_{GSS}	栅极-体漏电流	$V_{GS} = \pm 20 \text{V}, V_{DS} = 0 \text{V}$	-	-	± 100	nA

导通特性

$V_{GS(th)}$	栅极阈值电压	$V_{GS} = V_{DS}, I_D = 250 \mu\text{A}$	1.0	-	3.0	V
$R_{DS(on)}$	漏极至源极静态导通电阻	$V_{GS} = 10 \text{V}, I_D = 80 \text{A}$	-	2.0	2.4	$\text{m}\Omega$
g_{FS}	正向跨导	$V_{DS} = 10 \text{V}, I_D = 80 \text{A}$ (注4)	-	368	-	S

动态特性

C_{iss}	输入电容	$V_{DS} = 25 \text{V}, V_{GS} = 0 \text{V}$ $f = 1 \text{MHz}$	-	5490	7300	pF
C_{oss}	输出电容		-	1220	1620	pF
C_{riss}	反向传输电容		-	155	233	pF
$Q_{g(tot)}$	10 V 的栅极电荷总量	$V_{DS} = 32 \text{V}, I_D = 80 \text{A}$ $V_{GS} = 10 \text{V}$ (注4,5)	-	84	109	nC
Q_{gs}	栅极-源极栅极电荷		-	19	-	nC
Q_{gs2}	阈值电压-“米勒”平台电荷		-	9.5	-	nC
Q_{gd}	栅极-漏极“米勒”电荷		-	12	-	nC

开关特性

$t_{d(on)}$	导通延迟时间	$V_{DD} = 20 \text{V}, I_D = 80 \text{A}$ $R_{GEN} = 4.7 \Omega, V_{GS} = 10 \text{V}$ (注4,5)	-	17	44	ns
t_r	导通上升时间		-	8	26	ns
$t_{d(off)}$	关断延迟时间		-	71	152	ns
t_f	关断下降时间		-	17	44	ns
ESR	等效串联电阻 (G-S)		-	1.1	-	Ω

漏极-源极二极管特性

I_S	漏极-源极二极管最大正向连续电流	-	-	219	A	
I_{SM}	漏极-源极二极管最大正向脉冲电流	-	-	876	A	
V_{SD}	漏极-源极二极管正向电压	$V_{GS} = 0 \text{V}, I_{SD} = 80 \text{A}$	-	-	1.3	V
t_{rr}	反向恢复时间	$V_{GS} = 0 \text{V}, I_{SD} = 80 \text{A}$	-	54	-	ns
Q_{rr}	反向恢复电荷	$di_F/dt = 100 \text{A}/\mu\text{s}$ (注4)	-	49	-	nC

注:

- 重复额定值: 脉冲宽度受限于最大结温
- $L = 3 \text{mH}, I_{AS} = 24 \text{A}, V_{DD} = 40 \text{V}, R_G = 25 \Omega$, 开始于 $T_J = 25^\circ\text{C}$
- $I_{SD} \leq 80 \text{A}, di/dt \leq 200 \text{A}/\mu\text{s}, V_{DD} \leq BV_{DSS}$, 开始于 $T_J = 25^\circ\text{C}$
- 脉冲测试: 测试脉宽 $\leq 300 \mu\text{s}$, 占空比 $\leq 2\%$
- 典型特性本质上独立于工作温度

典型性能特征

图 1. 导通区域特性

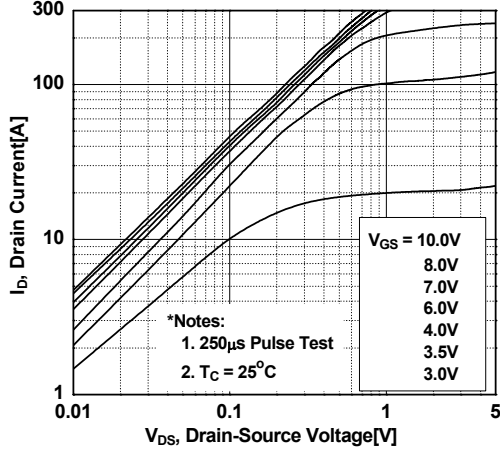


图 2. 传输特性

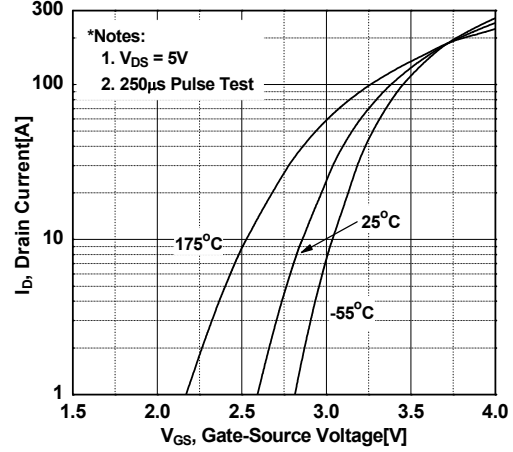


图 3. 导通电阻变化与漏极电流和栅极电压的关系

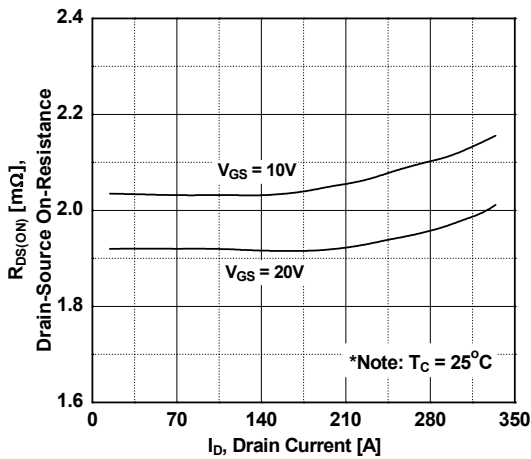


图 4. 体二极管正向电压变化与源极电流和温度的关系

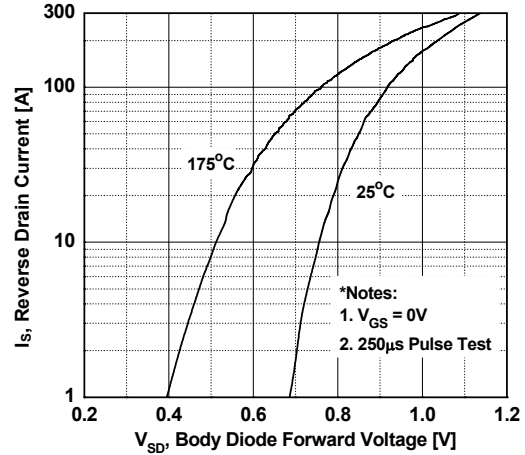


图 5. 电容特性

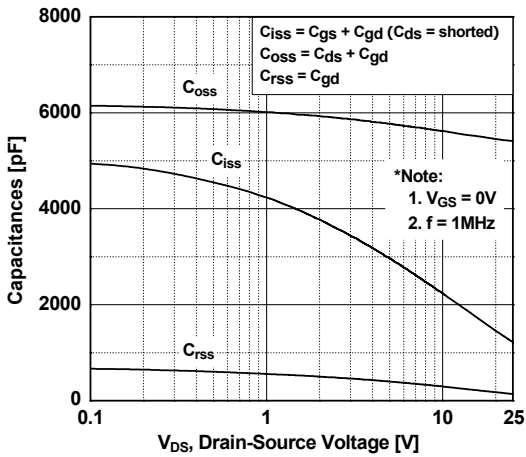
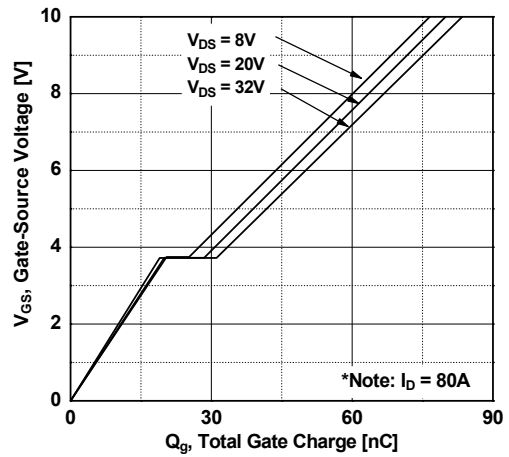


图 6. 栅极电荷特性



典型性能特性 (接上页)

图 7. 击穿电压变化与温度的关系

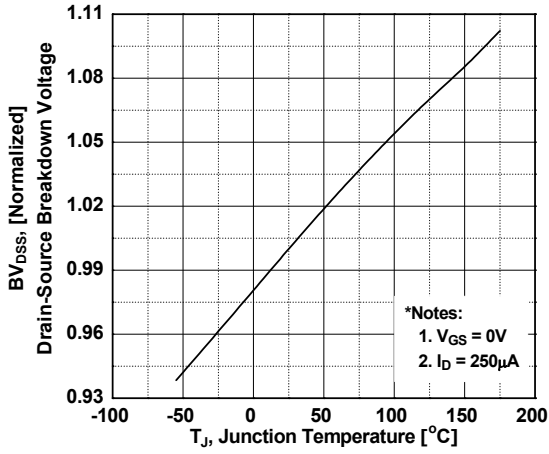


图 8. 导通电阻变化与温度的关系

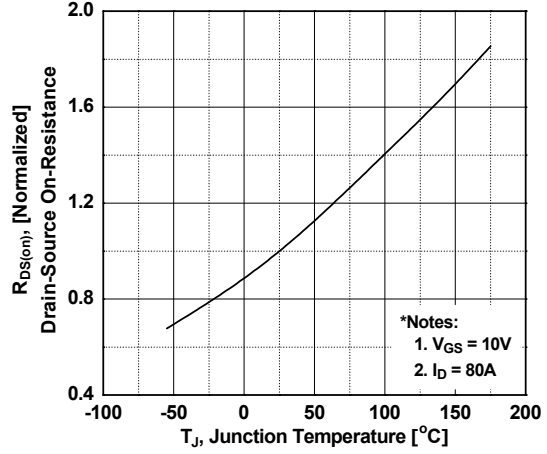


图 9. 最大安全工作区

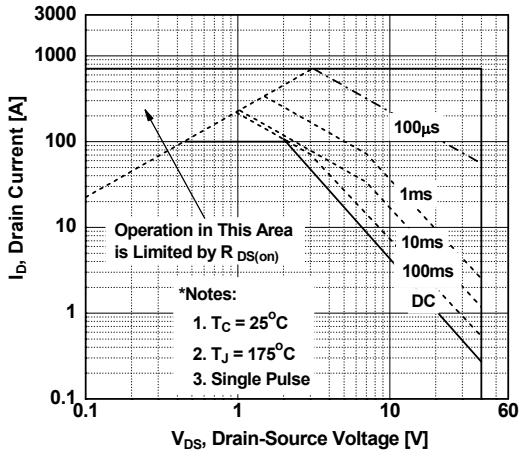


图 10. 最大漏极电流与壳温的关系

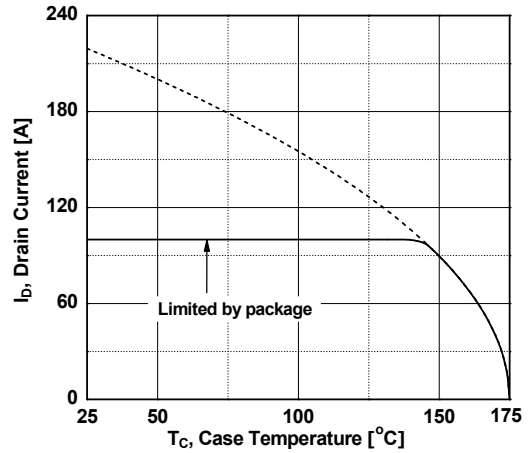
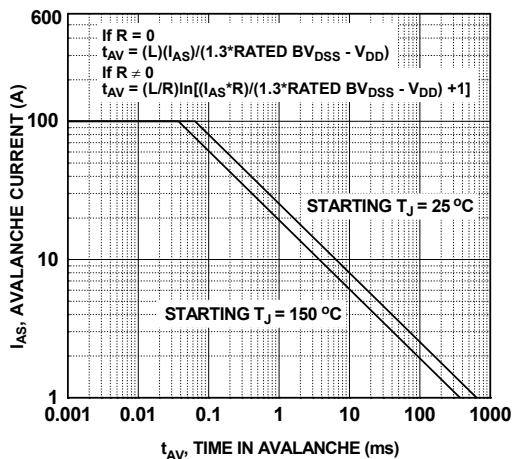


图 11. 非箝位电感开关能力



典型性能特性 (接上页)

图 12. 瞬态热响应曲线

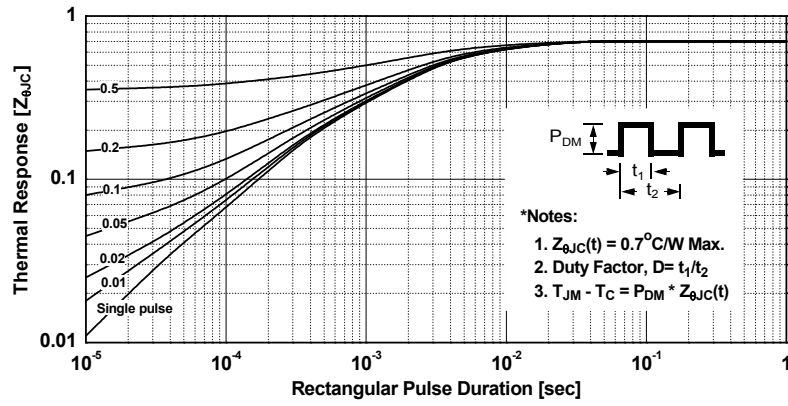


图 13. 栅极电荷测试电路与波形

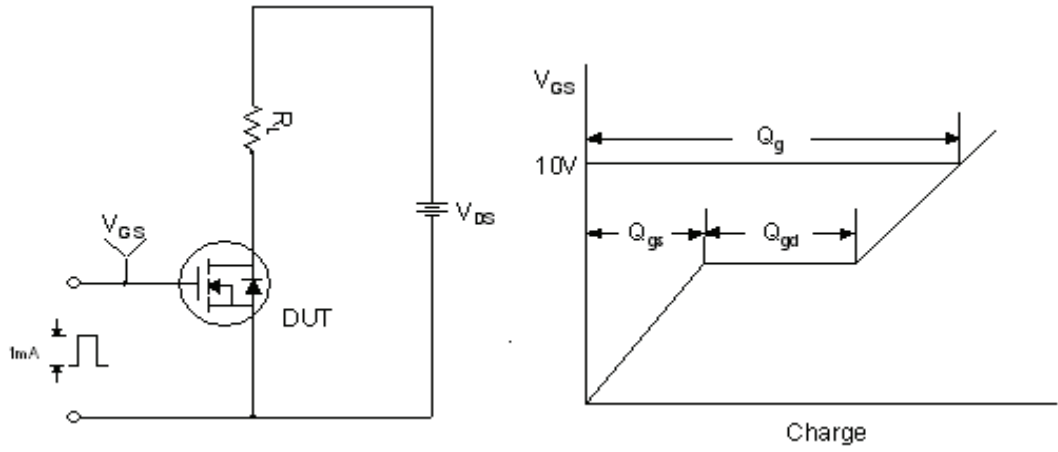


图 14. 阻性开关测试电路与波形

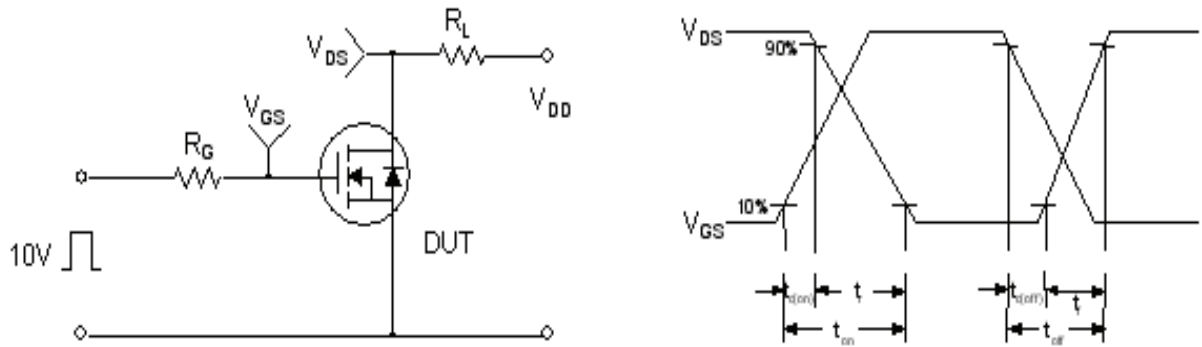


图 15. 非箝位感性开关测试电路与波形

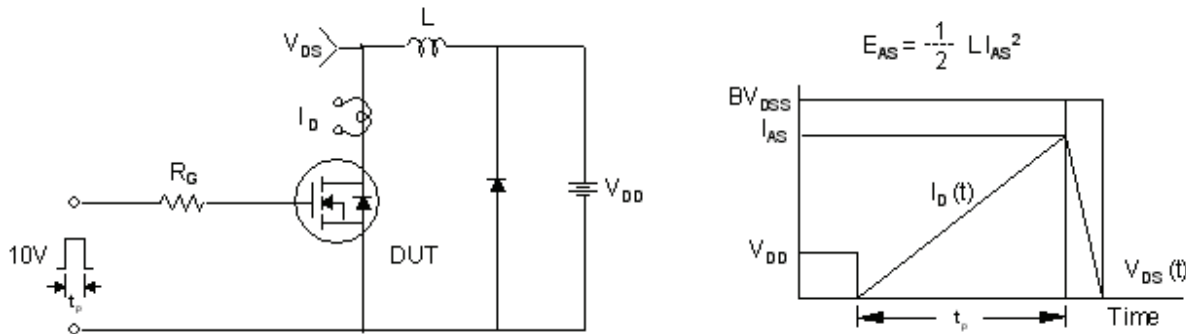
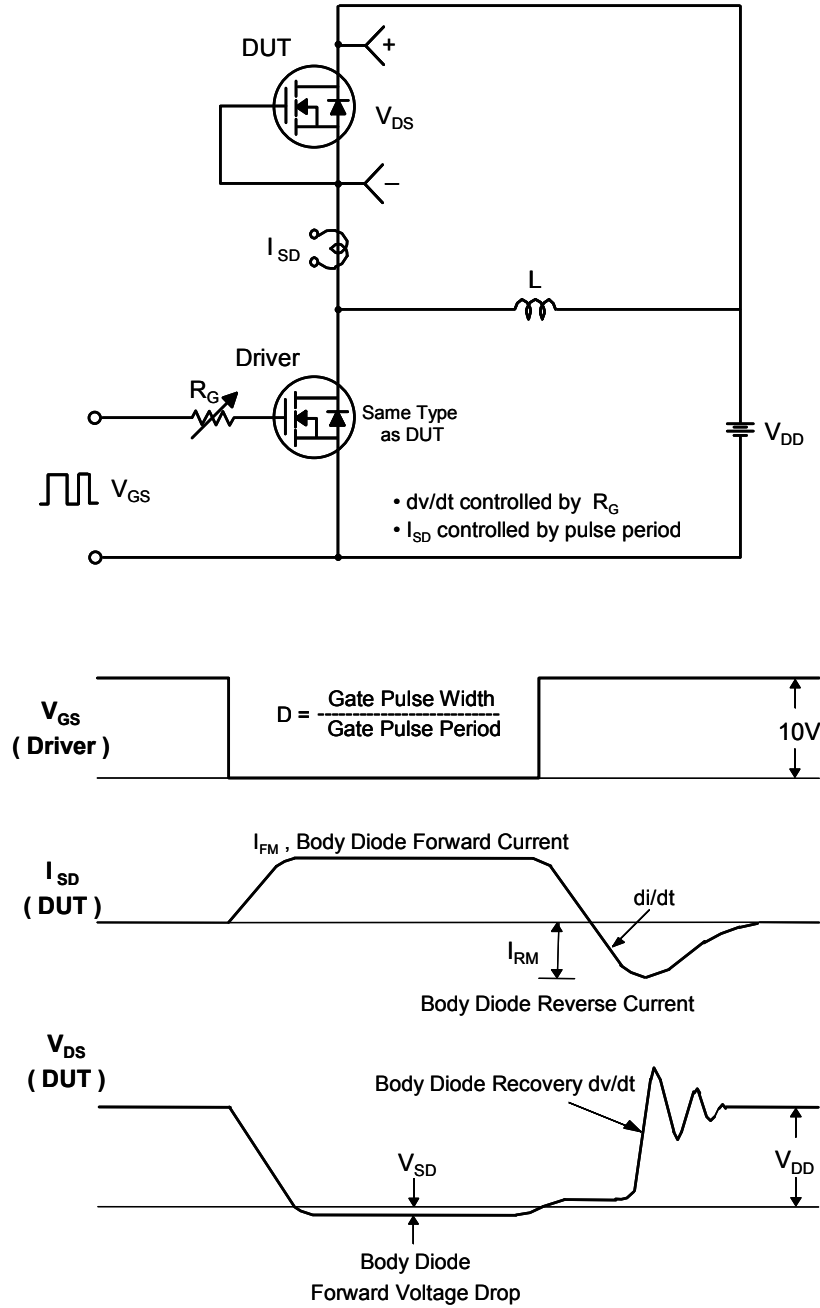
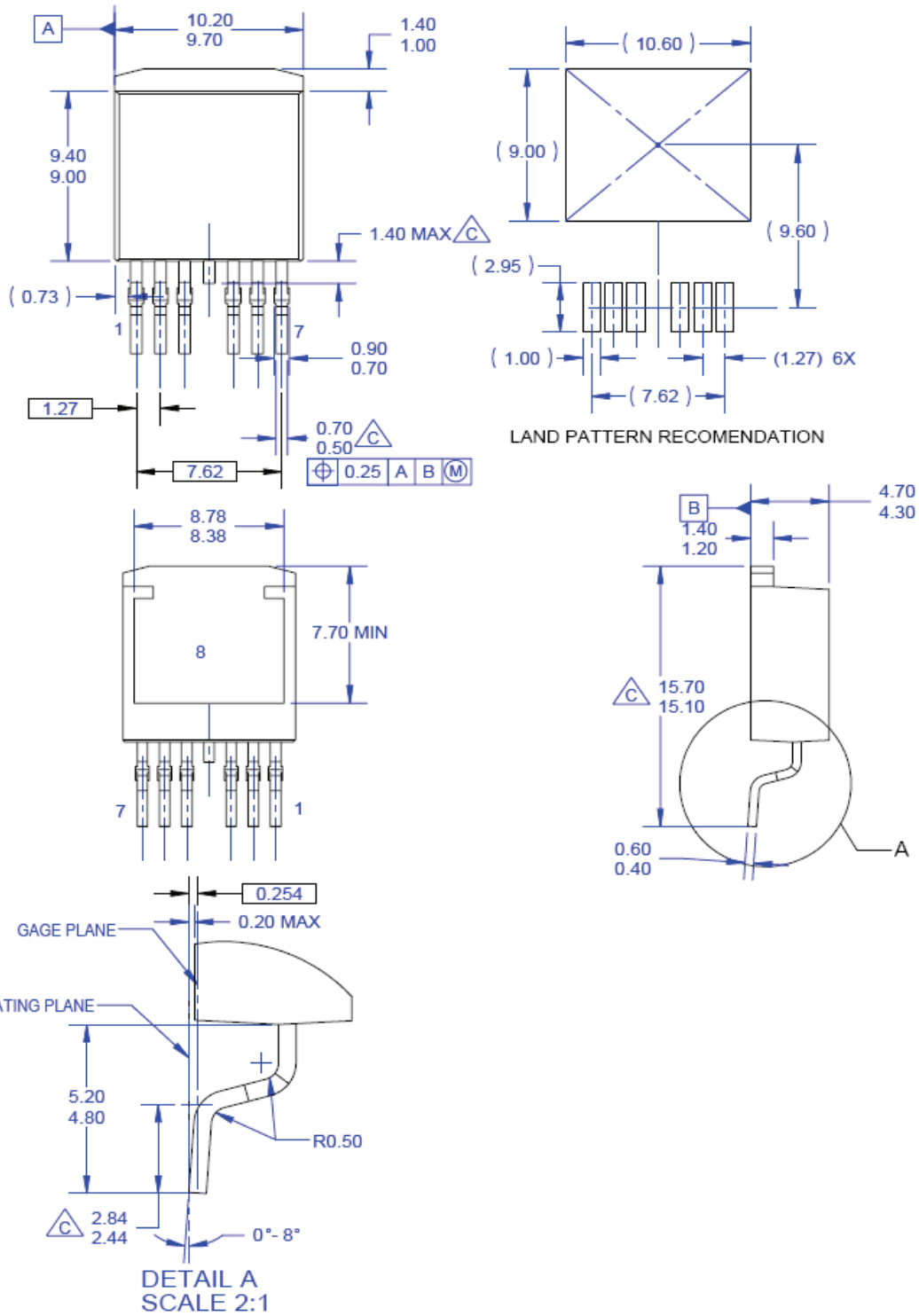


图 16. 二极管恢复 dv/dt 峰值测试电路与波形



机械尺寸

D²PAK-7L




FDB024N04AL7 N 沟道 PowerTrench[®] MOSFET

尺寸单位为毫米



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