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March 2015

FDD8778/FDU8778

N-Channel PowerTrench® MOSFET

25V, **35A**, **14m**Ω

Features

- Max $r_{DS(on)}$ = 14.0m Ω at V_{GS} = 10V, I_D = 35A
- Max $r_{DS(on)}$ = 21.0m Ω at V_{GS} = 4.5V, I_D = 33A
- Low gate charge: $Q_{g(TOT)} = 12.6nC(Typ)$, $V_{GS} = 10V$
- Low gate resistance
- RoHS compliant

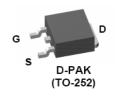


General Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low $r_{\text{DS(on)}}$ and fast switching speed.

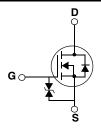
Application

- DC-DC for Desktop Computers and Servers
- VRM for Intermediate Bus Architecture









MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V_{DS}	Drain to Source Voltage		25	V
V_{GS}	Gate to Source Voltage		±20	V
	Drain Current -Continuous (Package Limited)		35	
I_D	-Continuous (Die Limited)		40	Α
	-Pulsed	(Note 1)	145	
E _{AS}	Single Pulse Avalanche Energy	(Note 2)	24	mJ
P_{D}	Power Dissipation		39	W
T _J , T _{STG}	Operating and Storage Temperature		-55 to 175	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case TO-252,TO-251	3.8	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient TO-252,TO-251	100	°C/W
R _{e,IA}	Thermal Resistance, Junction to Ambient TO-252,1in ² copper pad area	52	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD8778	FDD8778	TO-252AA	13"	16mm	2500 units
FDU8778	FDU8778	TO-251AA	N/A(Tube)	N/A	75 units
FDU8778	FDU8778_F071	TO-251AA	N/A(Tube)	N/A	75 units

Electrical	Charac	teristics	$T_{\rm J} = 25^{\circ}$	C unless otherwise noted
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Symbol	Parameter	lest Conditions	win	тур	IVIAX	Units
Off Chara	acteristics					
BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0V$	25			V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, referenced to 25°C		17.2		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 20V,$ $V_{GS} = 0V$ $T_{J} = 150^{\circ}C$			1 250	μА
I _{GSS}	Gate to Source Leakage Current	V _{GS} = ±20V			±10	μΑ

On Characteristics

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	1.2	1.5	2.5	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I_D = 250 μ A, referenced to 25°C		-5.3		mV/°C
r _{DS(on)}	Drain to Source On Resistance	V _{GS} = 10V, I _D = 35A		11.6	14.0	
		V _{GS} = 4.5V, I _D = 33A		15.7	21.0	mΩ
	Drain to course on registance	V _{GS} = 10V, I _D = 35A T _J = 175°C		18.2	23.8	11122

Dynamic Characteristics

C _{iss}	Input Capacitance	101/11/	635	845	pF
Coss	Output Capacitance	V _{DS} = 13V, V _{GS} = 0V, f = 1MHz	160	215	pF
C _{rss}	Reverse Transfer Capacitance	1 - 11411 12	108	162	pF
R_g	Gate Resistance	f = 1MHz	1.3		Ω

Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time		6	12	ns
t _r	Rise Time	$V_{DD} = 13V$, $I_{D} = 35A$ $V_{GS} = 10V$, $R_{GS} = 27\Omega$	22	35	ns
t _{d(off)}	Turn-Off Delay Time	V _{GS} = 10V, K _{GS} = 2712	43	69	ns
t _f	Fall Time		32	51	ns
$Q_{g(TOT)}$	Total Gate Charge at 10V	V _{GS} = 0V to 10V	12.6	18	nC
$Q_{g(5)}$	Total Gate Charge at 5V	$V_{GS} = 0V \text{ to } 5V$ $V_{DD} = 13V$ $I_{D} = 35A$	6.7	9.4	nC
Q_{gs}	Gate to Source Gate Charge	$I_D = 35A$ $I_0 = 1.0 \text{mA}$	2.1		nC
Q _{gd}	Gate to Drain "Miller" Charge	- ig 1.511# (3.2		nC

Drain-Source Diode Characteristics

V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0V, I _S = 35A	1.03	1.25	\/
	Source to Drain Diode 1 of ward voltage	V _{GS} = 0V, I _S = 15A	0.89	1.2	v
t _{rr}	Reverse Recovery Time	I _F = 35A, di/dt = 100A/μs	25	38	ns
Q _{rr}	Reverse Recovery Charge	$I_F = 35A$, di/dt = 100A/ μ s	17	26	nC

Notes:
1: Pulse time < 300µs, Duty cycle = 2%.
2: Starting T_J = 25°C, L = 0.1mH, I_{AS} = 22A, V_{DD} = 23V, V_{GS} = 10V.

Typical Characteristics T_J = 25°C unless otherwise noted

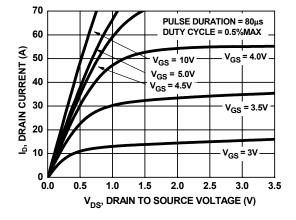


Figure 1. On Region Characteristics

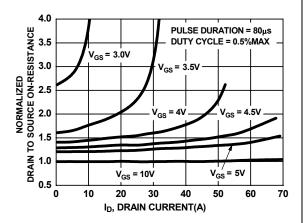


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

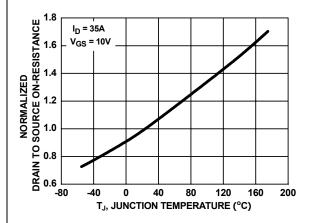


Figure 3. Normalized On Resistance vs Junction Temperature

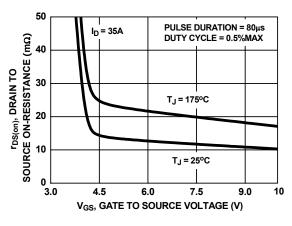


Figure 4. On-Resistance vs Gate to Source Voltage

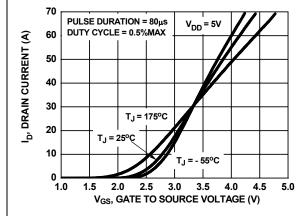


Figure 5. Transfer Characteristics

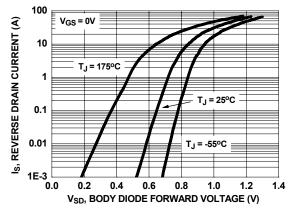


Figure 6. Source to Drain Diode Forward Voltage vs Source Current



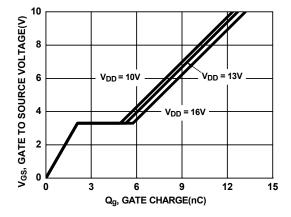


Figure 7. Gate Charge Characteristics

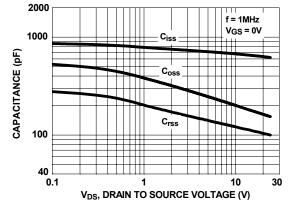


Figure 8. Capacitance vs Drain to Source Voltage

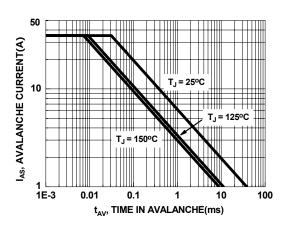


Figure 9. Unclamped Inductive Switching Capability

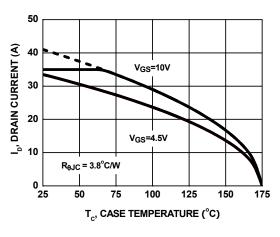


Figure 10. Maximum Continuous Drain Current vs Case Temperature

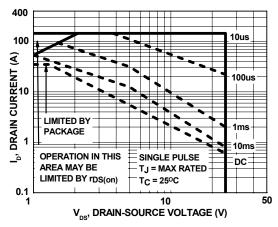


Figure 11. Forward Bias Safe Operating Area

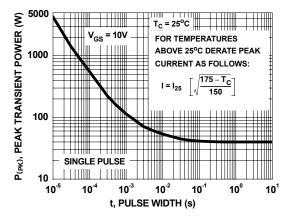


Figure 12. Single Pulse Maximum Power Dissipation

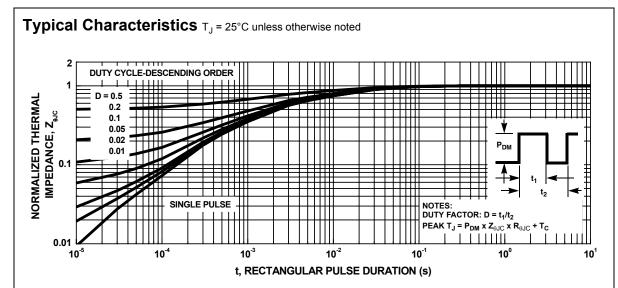
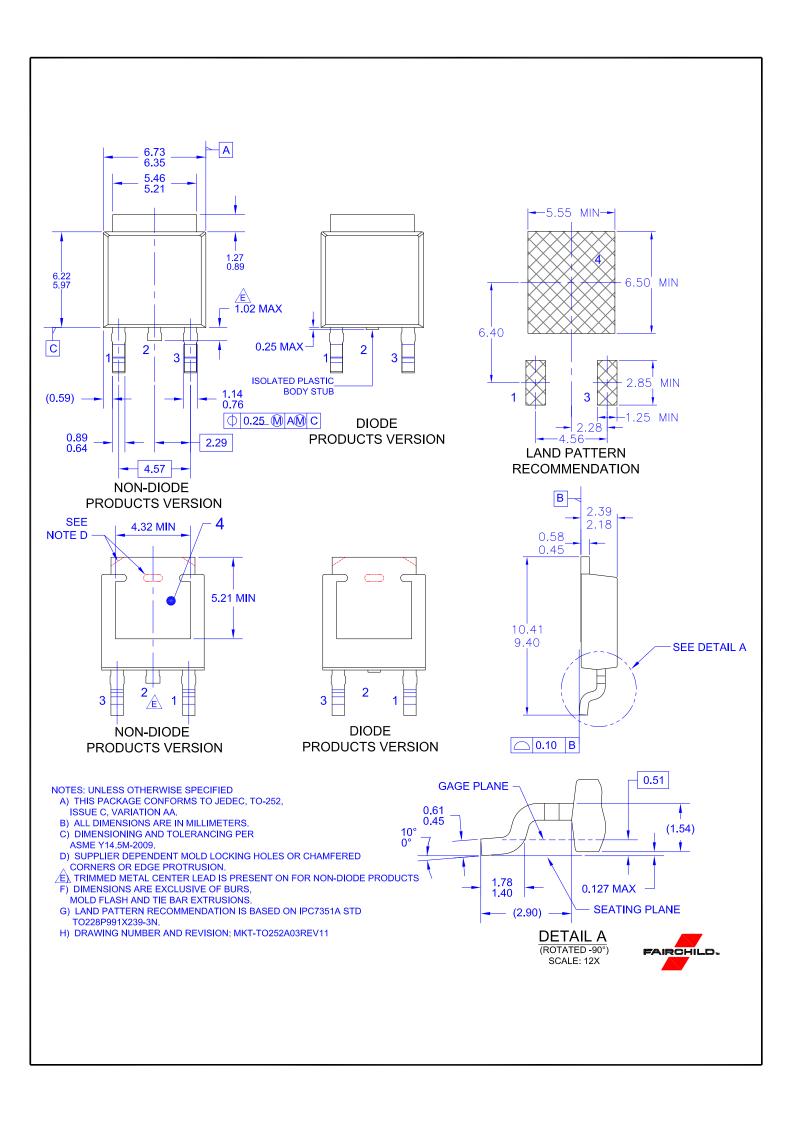


Figure 13. Transient Thermal Response Curve



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