

MOSFET – N 沟道, POWERTRENCH®

150 V, 25 A, 34 mΩ

FDMC86260ET150

概述

该 N 沟道 MOSFET 采用安森美 (onsemi) 进的 POWERTRENCH 工艺生产, 这一先进工艺是专为最大限度地降低通态电阻并保持卓越开关性能而定制的。

特性

- T_J 额定值扩展: 175°C
- 最大 $R_{DS(on)}$ = 34 mΩ at $V_{GS} = 10\text{ V}$, $I_D = 5.4\text{ A}$
- 最大 $R_{DS(on)}$ = 44 mΩ at $V_{GS} = 6\text{ V}$, $I_D = 4.8\text{ A}$
- 高性能沟道技术可实现极低的 $R_{DS(on)}$
- 100% 经过 UIL 测试
- 终端为无铅产品
- 符合 RoHS 标准

应用

- DC-DC 转换

MOSFET 最大额定值 ($T_A = 25^\circ\text{C}$ unless otherwise noted)

符号	参数	额定值	单位
V_{DS}	漏极-源极电压	150	V
V_{GS}	栅极-源极电压	±20	V
I_D	漏极电流		A
	连续, $T_C = 25^\circ\text{C}$ (注 5)	25	
	连续, $T_C = 100^\circ\text{C}$ (注 5)	18	
	连续, $T_A = 25^\circ\text{C}$ (注 1a)	5.4	
	脉冲 (注 4)	116	
E_{AS}	单脉冲雪崩能量 (注 3)	121	mJ
P_D	功耗		W
	$T_C = 25^\circ\text{C}$	65	
	$T_A = 25^\circ\text{C}$ (注 1a)	2.8	
T_J, T_{STG}	工作和存储结温范围	-55 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

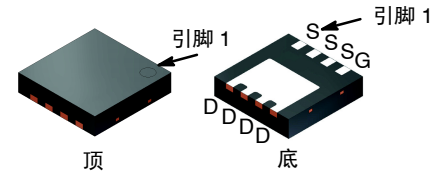
(参考译文)

如果电压超过最大额定值表中列出的值范围, 器件可能会损坏。如果超过任何这些限值, 将无法保证器件功能, 可能会导致器件损坏, 影响可靠性。

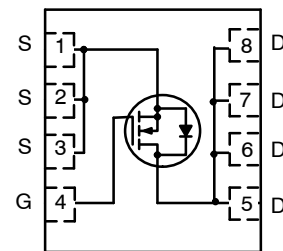
热性能

符号	参数	额定值	单位
$R_{\theta JC}$	结至外壳热阻 (注 1)	2.3	°C/W
$R_{\theta JA}$	结至环境热阻 (注 1a)	53	°C/W

V_{DS}	$R_{DS(on)}$ MAX	I_D MAX
150 V	34 mΩ @ 10 V	25 A
	44 mΩ @ 6 V	

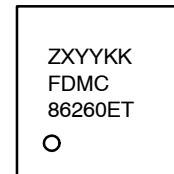


WDFN8 3.3 × 3.3, 0.65P
(Power 33)
CASE 483AW



N-CHANNEL MOSFET

MARKING DIAGRAM



Z = Assembly Plant Code
 XYY = 3-Digit Date Code Format
 KK = 2-Alphanumeric Lot Run Traceability Code
 FDMC86260ET = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping†
FDMC86260ET150	PQFN8 (Pb-Free, Halide Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, [BRD8011/D](#).

FDMC86260ET150

电气特性 (T_J = 25°C 除非另有说明)

符号	参数	测试条件	最小值	典型值	最大值	单位
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关断特性

BV _{DSS}	漏极-源极击穿电压	I _D = 250 μA, V _{GS} = 0 V	150	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	击穿电压温度系数	I _D = 250 μA, 参考温度为 25°C	-	110	-	mV/°C
I _{DSS}	零栅极电压漏极电流	V _{DS} = 120 V, V _{GS} = 0 V	-	-	1	μA
I _{GSS}	栅极-源极漏电流	V _{GS} = ±20 V, V _{DS} = 0 V	-	-	±100	nA

导通特性

V _{GS(th)}	栅极-源极阈值电压	V _{GS} = V _{DS} , I _D = 250 μA	2	2.7	4	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	栅极-源极阈值电压温度系数	I _D = 250 μA, 参考温度为 25°C	-	-9	-	mV/°C
R _{DS(on)}	漏极至源极静态导通电阻	V _{GS} = 10 V, I _D = 5.4 A	-	27	34	mΩ
		V _{GS} = 6 V, I _D = 4.8 A	-	31	44	
		V _{GS} = 10 V, I _D = 5.4 A, T _J = 125°C	-	55	69	
g _{FS}	正向跨导	V _{DD} = 10 V, I _D = 5.4 A	-	19	-	S

动态特性

C _{iss}	输入电容	V _{DS} = 75 V, V _{GS} = 0 V, f = 1 MHz	-	1000	1330	pF
C _{oss}	输出电容		-	105	140	pF
C _{rss}	反向传输电容		-	4.8	10	pF
R _g	栅极阻抗		0.1	0.6	1.8	Ω

开关特性

t _{d(on)}	导通延迟时间	V _{DD} = 75 V, I _D = 5.4 A, V _{GS} = 10 V, R _{GEN} = 6 Ω	-	9.5	19	ns
t _r	上升时间		-	2	10	ns
t _{d(off)}	关断延迟时间		-	17	30	ns
t _f	下降时间		-	3.3	10	ns
Q _{g(TOT)}	总栅极电荷	V _{GS} = 0 V 至 10 V, V _{DD} = 75 V, I _D = 5.4 A	-	15	21	nC
		V _{GS} = 0 V 至 6 V, V _{DD} = 75 V, I _D = 5.4 A	-	9.7	14	nC
Q _{gs}	总栅极电荷	V _{DD} = 75 V, I _D = 5.4 A	-	4.0	-	nC
Q _{gd}	栅极-漏极“米勒”电荷	V _{DD} = 75 V, I _D = 5.4 A	-	3.1	-	nC

FDMC86260ET150

电气特性 (T_J = 25°C 除非另有说明) (continued)

符号	参数	测试条件	最小值	典型值	最大值	单位
漏极-源极二极管特性						
V _{SD}	源极-漏极二极管正向电压	V _{GS} = 0 V, I _S = 5.4 A (注 2)	-	0.77	1.3	V
		V _{GS} = 0 V, I _S = 1.9 A (注 2)	-	0.72	1.2	
t _{rr}	反向恢复时间	I _F = 5.4 A, di/dt = 100 A/μs	-	64	102	ns
Q _{rr}	反向恢复电荷		-	85	137	nC

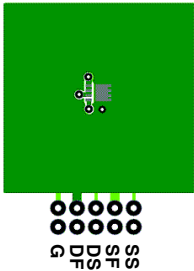
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

(参考译文)

除非另有说明,“电气特性”表格中列出的是所列测试条件下的产品性能参数。如果在不同条件下运行,产品性能可能与“电气特性”表格中所列性能参数不一致。

注意:

1 R_{θJA} 取决于安装在一平方英寸衬垫, 2oz 铜焊盘以及 FR-4 材质尺寸 1.5 x 1.5 in. 的衬垫上的器件。R_{θCA} 由用户的电路板设计确定。



a) 53 安装在 2 oz 最小 1 in² 铜焊盘上时的 °C/W



b) 125 安装在 2 oz 最小铜焊盘上时的 °C/W

2 脉冲测试: 脉冲宽度: < 300 ms, 占空比: < 2.0%。

3 E_{AS} 为 121 mJ, 依据起始 T_J = 25°C, L = 3 mH, I_{AS} = 9 A, V_{DD} = 150 V, V_{GS} = 10 V。100% 经过测试 (L = 0.1 mH, I_{AS} = 22 A)。

4 有关脉冲编号的更多详情, 请参考图 11 中的 SOA 图形。

5 计算得到的连续电流仅限于最大结温, 实际连续电流将受限于散热以及电气机械应用的电路板设计。

典型特性

($T_J = 25^\circ\text{C}$ 除非另有说明)

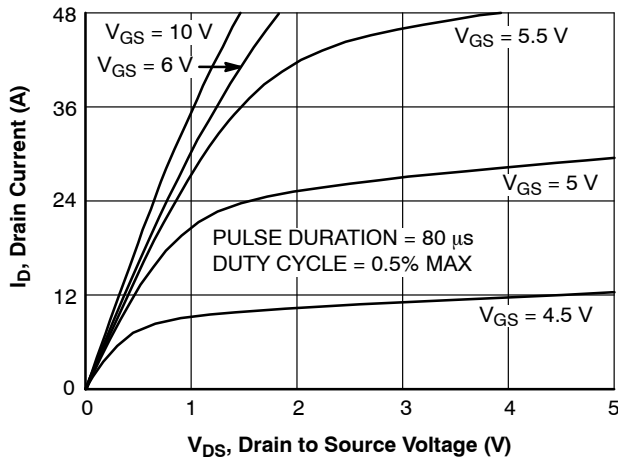


图 1. 通态区域特性

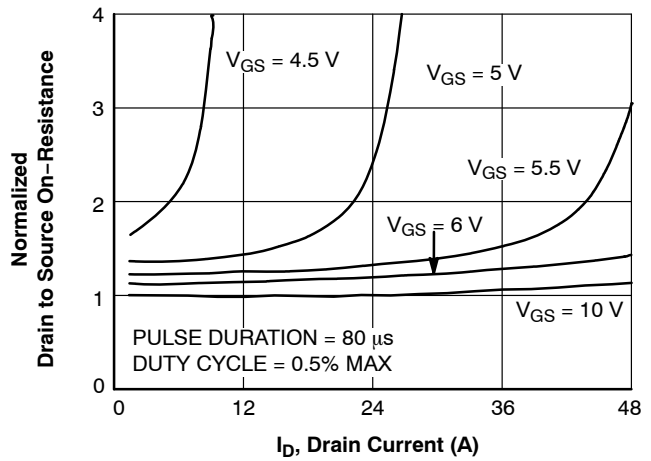


图 2. 标准化导通电阻与漏极电流和栅极电压的关系

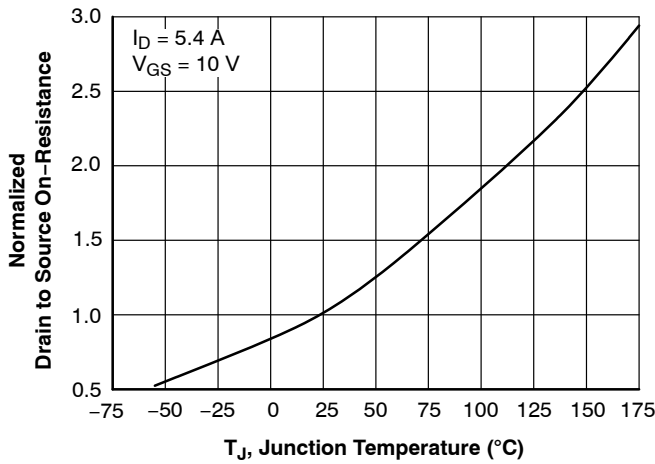


图 3. 标准化导通电阻与结温的关系

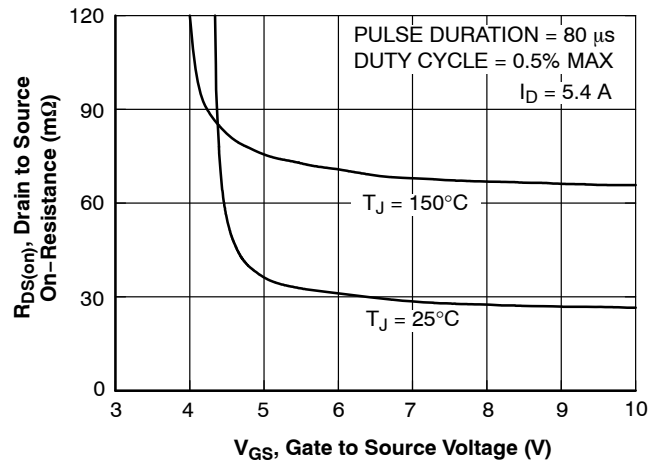


图 4. 导通电阻与栅极 - 源极电压的关系

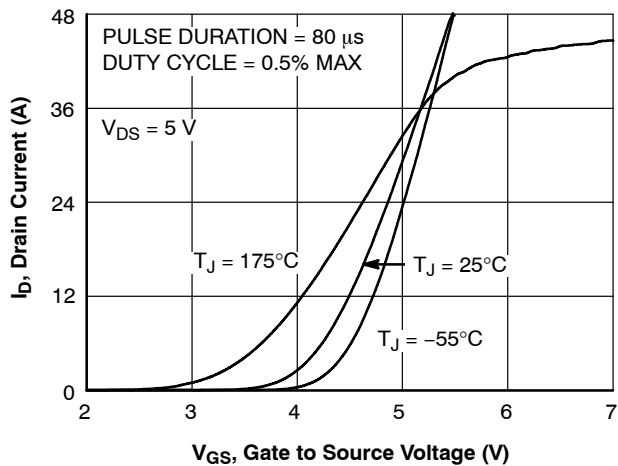


图 5. 转换特性

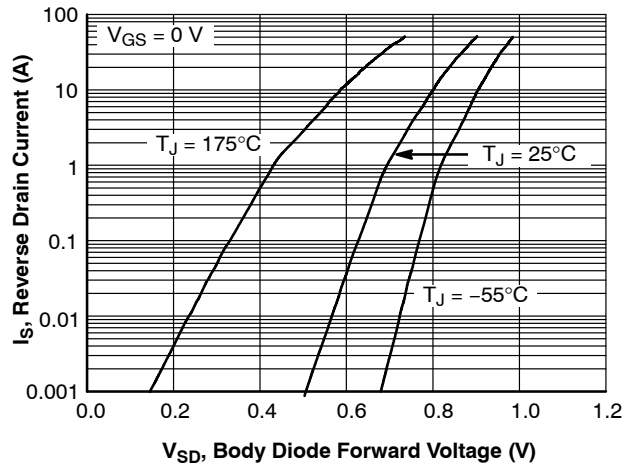


图 6. 源极 - 漏极二极管正向电压与源电流的关系

典型特性 (continued)

($T_J = 25^\circ\text{C}$ 除非另有说明)

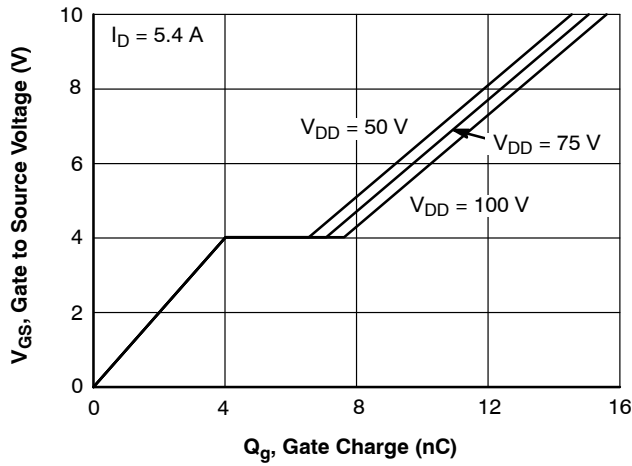


图 7. 栅极电荷特性

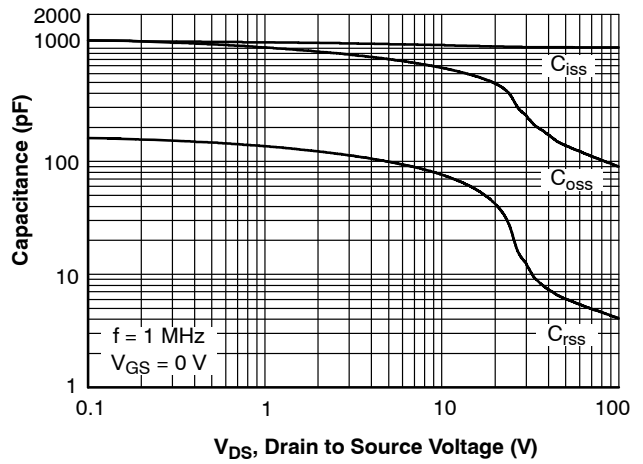


图 8. 电容与漏极 - 源极电压的关系

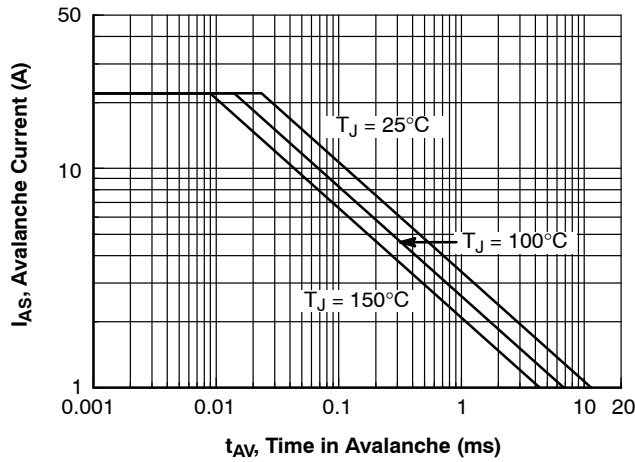


图 9. 非箝位电感开关能力

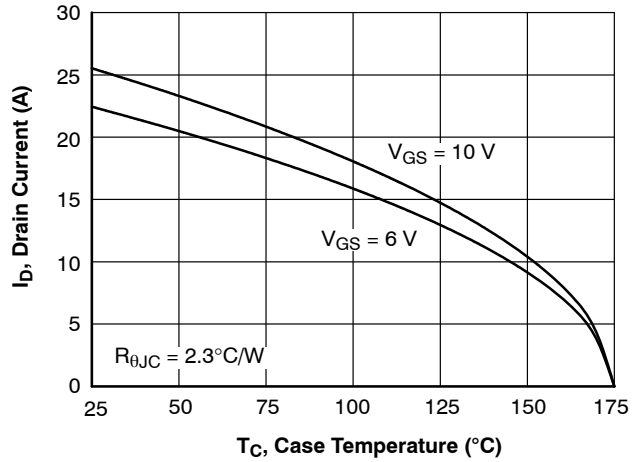


图 10. 最大连续漏极电流与壳温的关系

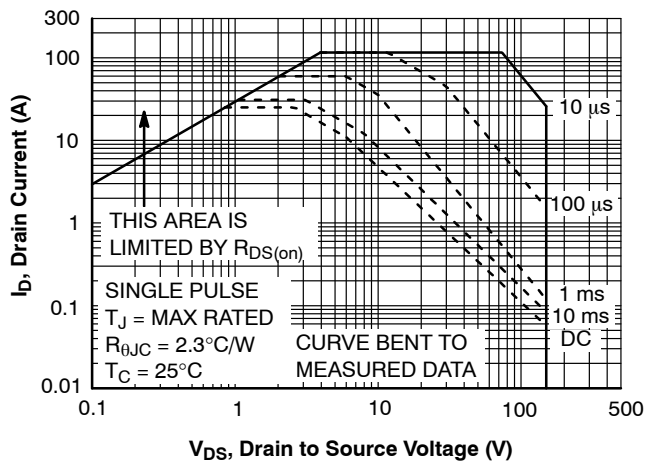


图 11. 正向偏压安全工作区

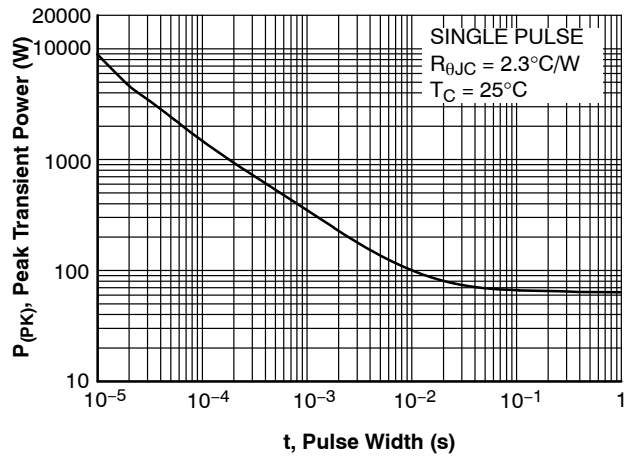


图 12. 单个脉冲最大功耗

FDMC86260ET150

典型特性 (continued)
 (T_J = 25°C 除非另有说明)

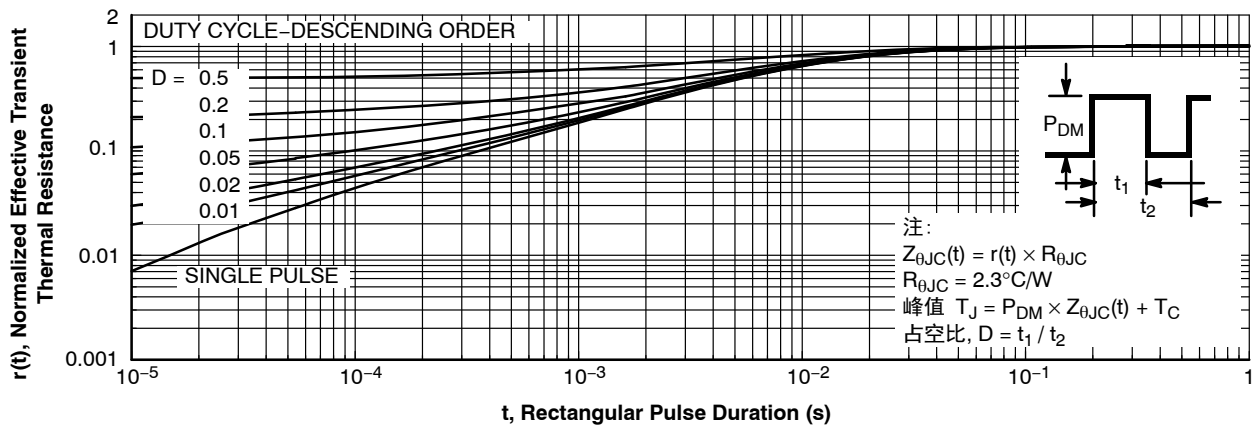
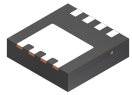


图 13. 结至外壳瞬态热响应曲线

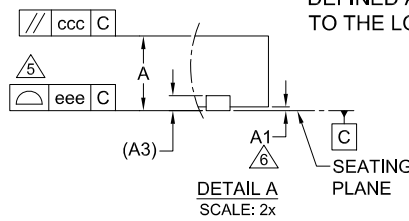
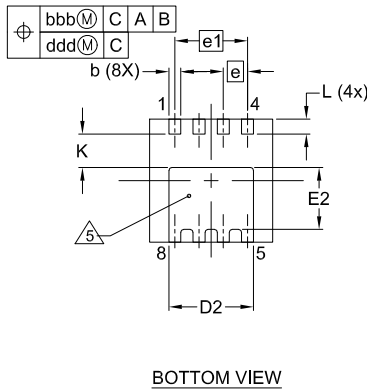
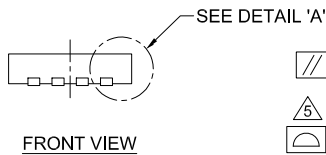
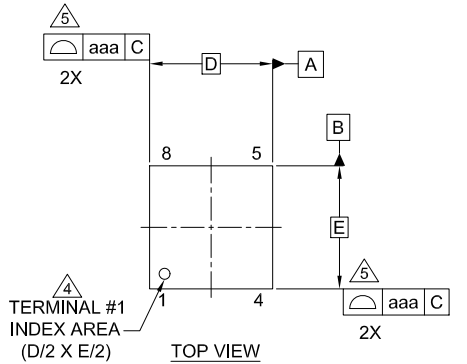
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MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

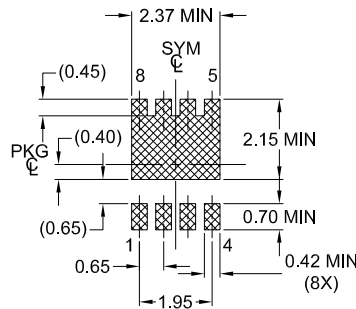


WDFN8 3.30x3.30x0.75, 0.65P
CASE 483AW
ISSUE B

DATE 22 MAR 2024



LAND PATTERN RECOMMENDATION



NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
2. ALL DIMENSIONS ARE IN MILLIMETERS.
3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
4. THE TERMINAL #1 IDENTIFIER AND TERMINAL NUMBERING CONVENTION SHALL CONFORM TO JEP95 SEC. 3 SPP-12. DETAILS OF TERMINAL #1 IDENTIFIER ARE OPTIONAL, BUT MUST BE LOCATED WITHIN THE ZONE INDICATED. THE TERMINAL #1 IDENTIFIER MAY BE EITHER A MOLD, EMBEDDED METAL OR MARKED FEATURE.
5. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.
6. SEATING PLANE IS DEFINED BY THE TERMINALS. 'A1' IS DEFINED AS THE DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	--	--	0.05
A3	0.20 REF		
b	0.27	0.32	0.37
D	3.30 BSC		
D2	2.17	2.27	2.37
E	3.30 BSC		
E2	1.56	1.66	1.76
e	0.65 BSC		
e1	1.95 BSC		
K	0.90	--	--
L	0.30	0.40	0.50
aaa	0.10		
bbb	0.10		
ccc	0.10		
ddd	0.05		
eee	0.05		

*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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