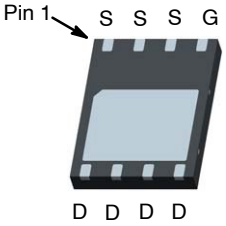
MOSFET – N-Channel, UltraFET Trench

100 V, 22 A, 23 mΩ

FDMS3672

V_{DS}	$R_{DS(ON)}$ MAX	I_D MAX
100 V	23 mΩ @ 10 V	22 A
	29 mΩ @ 6 V	



Bottom

WDFN8 5x6, 1.27P
Power 56
CASE 506DP

General Description

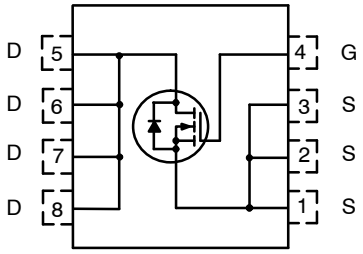
UltraFET devices combine characteristics that enable benchmark efficiency in power conversion applications. Optimized for $R_{DS(on)}$, low ESR, low total and Miller gate charge, these devices are ideal for high frequency DC to DC converters.

Features

- Max $R_{DS(on)}$ = 23 mΩ at V_{GS} = 10 V, I_D = 7.4 A
- Max $R_{DS(on)}$ = 29 mΩ at V_{GS} = 6 V, I_D = 6.6 A
- Typ Qg = 31 nC at V_{GS} = 10 V
- Low Miller Charge
- Optimized Efficiency at High Frequencies
- This Device is Pb−Free, Halide Free and RoHS Compliant

Applications

- DC−DC Conversion



N-CHANNEL MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C unless otherwise noted.)

Symbol	Parameter		Value	Unit
V_{DS}	Drain−Source Voltage		100	V
V_{GS}	Gate−Source Voltage		±20	V
I_D	Drain Current − Continuous (Package Limited) − Continuous (Silicon Limited) − Continuous (Note 1a) − Pulsed	 T_C = 25°C T_C = 25°C T_A = 25°C 	 22 41 7.4 30	A
P_D	Power Dissipation Power Dissipation (Note 1a)	T_C = 25°C T_A = 25°C	78 2.5	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range		−55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS (T_A = 25°C unless otherwise noted.)

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.6	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1a)	53	°C/W

MARKING DIAGRAM

&Z	= Assembly Location
&2	= Date Code
&K	= Lot Run Traceability Code
FDMS3672	= Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping†
FDMS3672	WDFN8 (Pb−Free, Halide Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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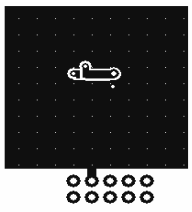
ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
BV_{DSS}	Drain to Source Breakdown Voltage	I_D = 250 μA, V_{GS} = 0 V	100	–	–	V
ΔBV_{DSS} /ΔT_J	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, referenced to 25°C	–	104	–	mV/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 80 V V, V_{GS} = 0 V	–	–	1	μA
		V_{DS} = 80 V, V_{GS} = 0 V, T_J = 55°C	–	–	10	
I_{GSS}	Gate to Source Leakage Current	V_{GS} = ±20 V, V_{DS} = 0 V	–	–	±100	nA
ON CHARACTERISTICS						
$V_{GS(th)}$	Gate to Source Threshold Voltage	V_{GS} = V_{DS}, I_D = 250 μA	2.0	3.1	4.0	V
$\Delta V_{GS(th)}$ /ΔT_J	Gate to Source Threshold Voltage Temperature Coefficient	I_D = 250 μA, referenced to 25°C	–	−11	–	mV/°C
$R_{DS(on)}$	Static Drain to Source On Resistance	V_{GS} = 10 V, I_D = 7.4 A	–	19	23	mΩ
		V_{GS} = 6 V, I_D = 6.6 A	–	24	29	
		V_{GS} = 10 V, I_D = 7.4 A, T_J = 125°C	–	33	40	
g_{FS}	Forward Transconductance	V_{DS} = 10 V, I_D = 7.4 A	–	20	–	S
DYNAMIC CHARACTERISTICS						
C_{iss}	Input Capacitance	V_{DS} = 50 V, V_{GS} = 0 V, f = 1 MHz	–	2015	2680	pF
C_{oss}	Output Capacitance		–	210	280	pF
C_{rss}	Reverse Transfer Capacitance		–	90	135	pF
R_g	Gate Resistance	f = 1 MHz	–	1.3	–	Ω
SWITCHING CHARACTERISTICS						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 50 V, I_D = 7.4 A, V_{GS} = 10 V, R_{GEN} = 6 Ω	–	23	37	ns
t_r	Rise Time		–	11	20	ns
$t_{d(off)}$	Turn-Off Delay Time		–	36	58	ns
t_f	Fall Time		–	8	16	ns
Q_g	Total Gate Charge at 10 V	V_{GS} = 0 V to 10 V, V_{DD} = 50 V, I_D = 7.4 A	–	31	44	nC
Q_g	Total Gate Charge at 4.5 V	V_{GS} = 0 V to 4.5 V, V_{DD} = 50 V, I_D = 7 A	–	–	–	nC
Q_{gs}	Gate to Source Charge	V_{DD} = 50 V, I_D = 7.4 A	–	9.5	–	nC
Q_{gd}	Gate to Drain “Miller” Charge	V_{DD} = 50 V, I_D = 7.4 A	–	8	–	nC
DRAIN-SOURCE DIODE CHARACTERISTICS						
V_{SD}	Source to Drain Diode Forward Voltage	V_{GS} = 0 V, I_S = 7.4 A (Note 2)	–	0.8	1.2	V
t_{rr}	Reverse Recovery Time	I_F = 7.4 A, di/dt = 100 A/μs	–	52	78	ns
Q_{rr}	Reverse Recovery Charge		–	101	152	nC

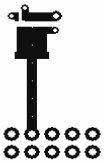
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

NOTES:

1. $R_{\theta JA}$ is determined with the device mounted on a 1 in^2 pad 2 oz copper pad on a 1.5 × 1.5 in. board of FR−4 material. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user’s board design.



a) 50°C/W when mounted on a 1 in^2 pad of 2 oz copper.



b) 125°C/W when mounted on a minimum pad of 2 oz copper.

2. Pulse Test: Pulse Width < 300 μs, Duty cycle < 2.0%.

TYPICAL CHARACTERISTICS

(T_J = 25°C unless otherwise noted)

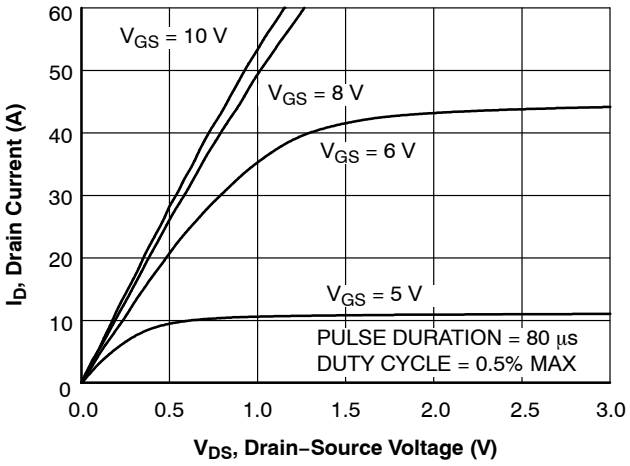


Figure 1. On−Region Characteristics

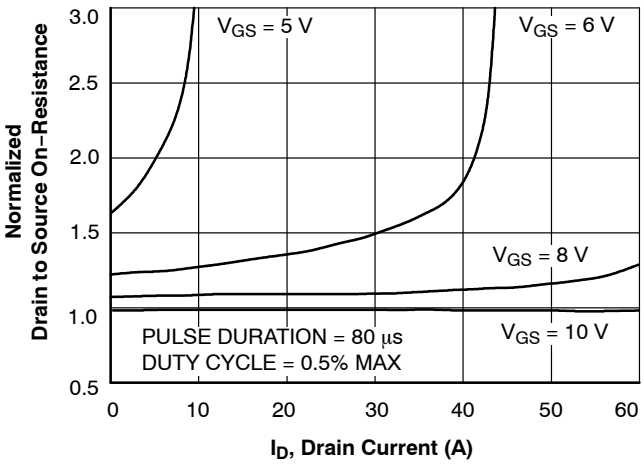


Figure 2. Normalized On−Resistance vs. Drain Current and Gate Voltage

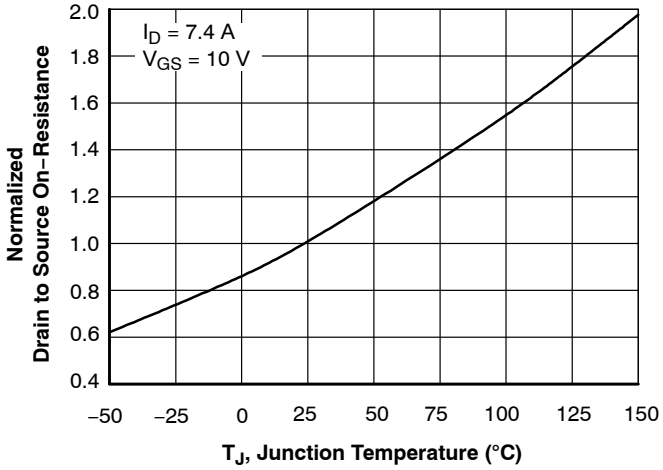


Figure 3. Normalized On−Resistance vs. Junction Temperature

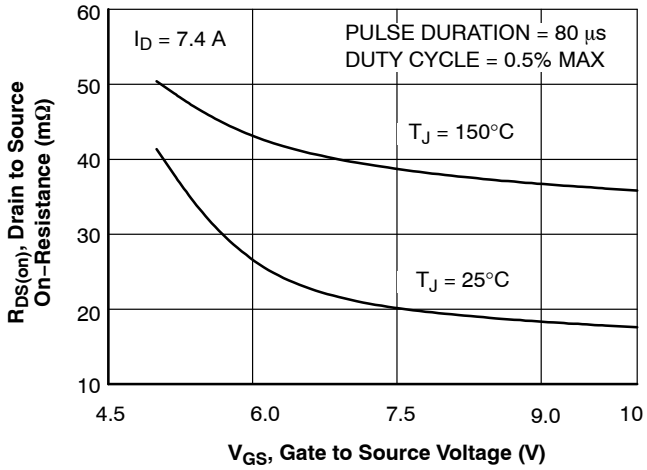


Figure 4. On−Resistance vs. Gate to Source Voltage

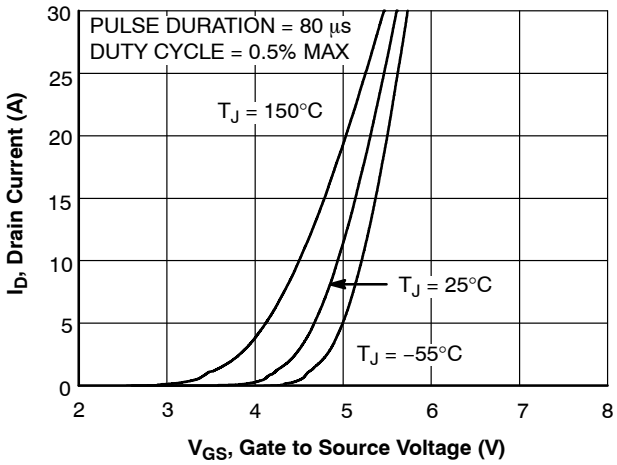


Figure 5. Transfer Characteristics

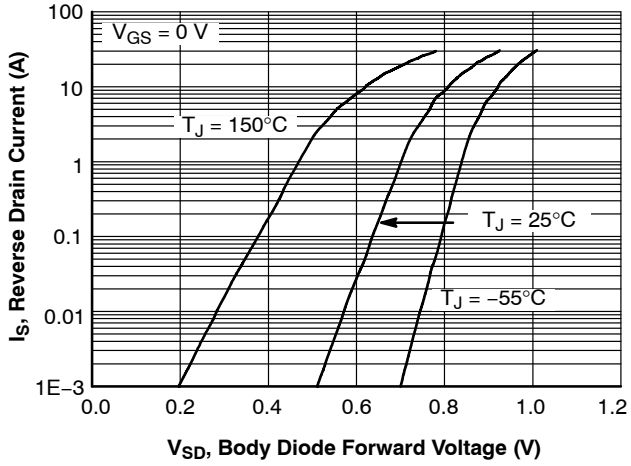


Figure 6. Source to Drain Diode Forward Voltage vs. Source Current

TYPICAL CHARACTERISTICS (CONTINUED)

(T_J = 25°C unless otherwise noted)

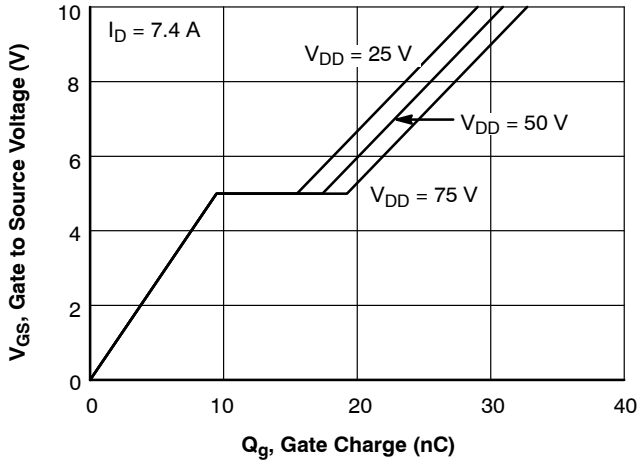


Figure 7. Gate Charge Characteristics

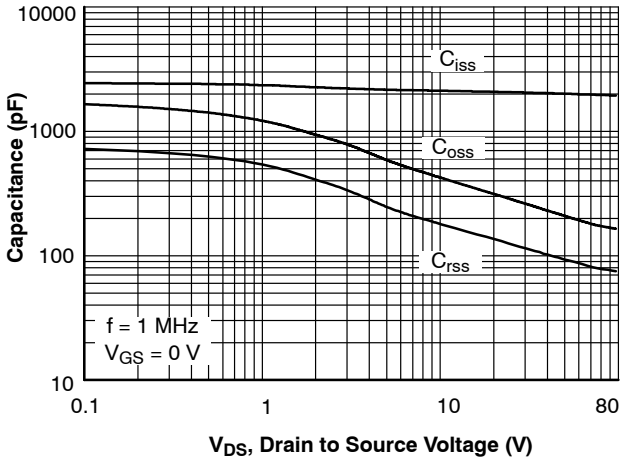


Figure 8. Capacitance vs. Drain to Source Voltage

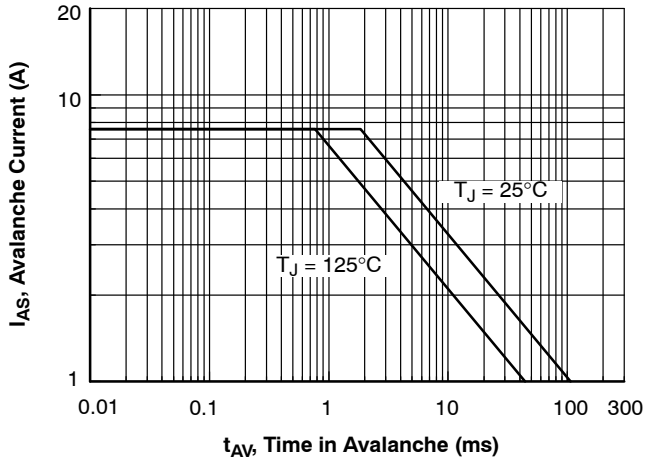


Figure 9. Unclamped Inductive Switching Capability

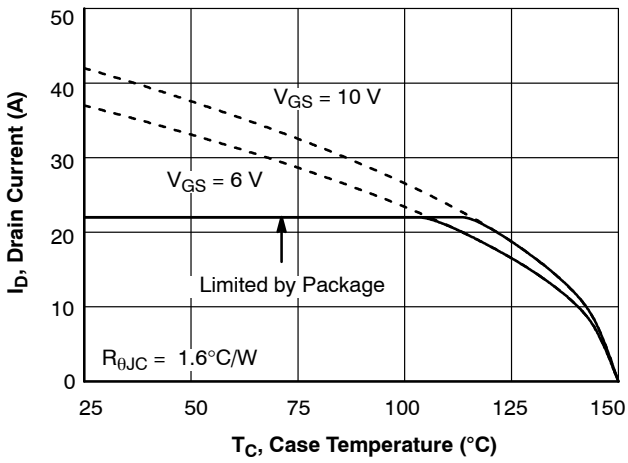


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

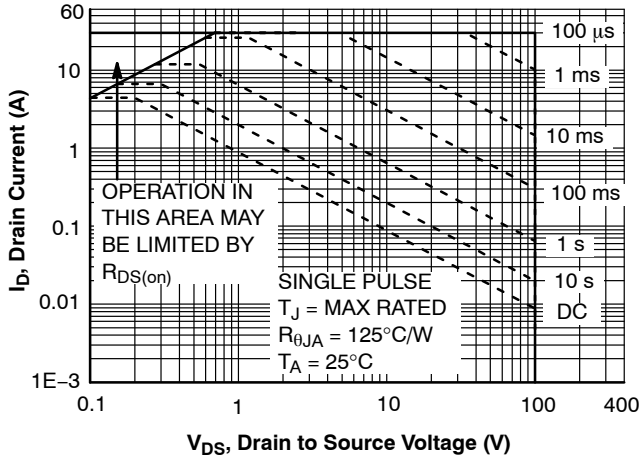


Figure 11. Forward Bias Safe Operating Area

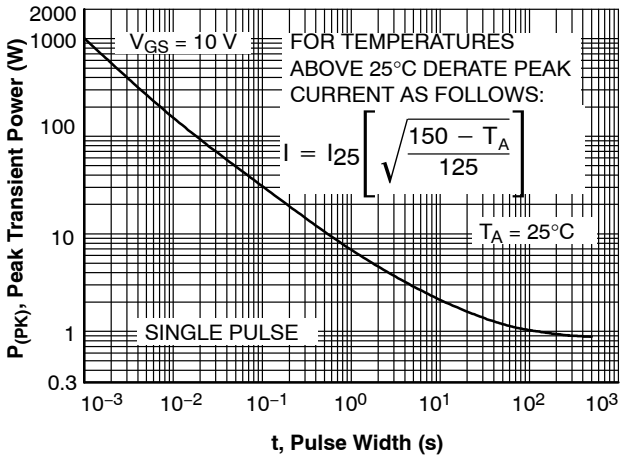


Figure 12. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS (CONTINUED)

(T_J = 25°C unless otherwise noted)

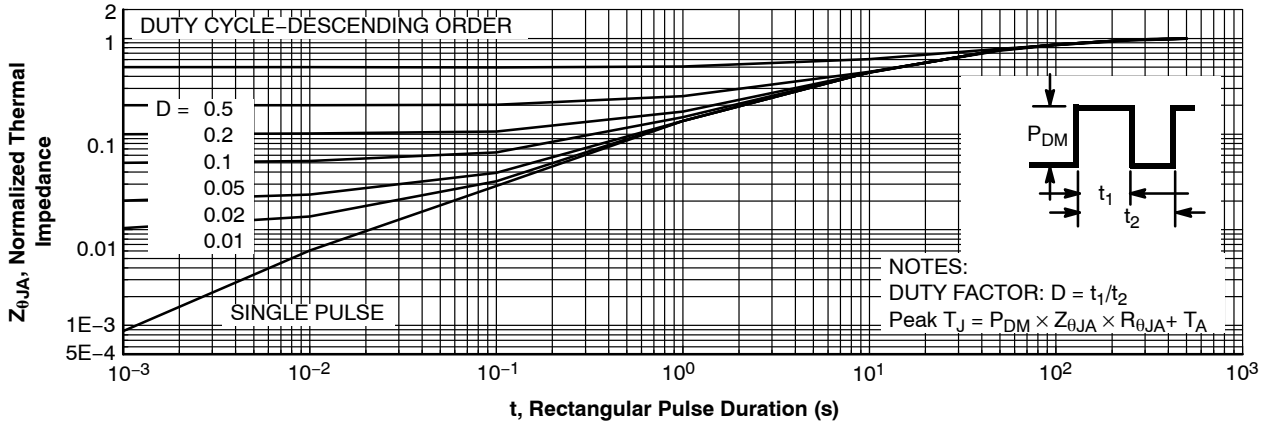


Figure 13. Transient Thermal Response Curve

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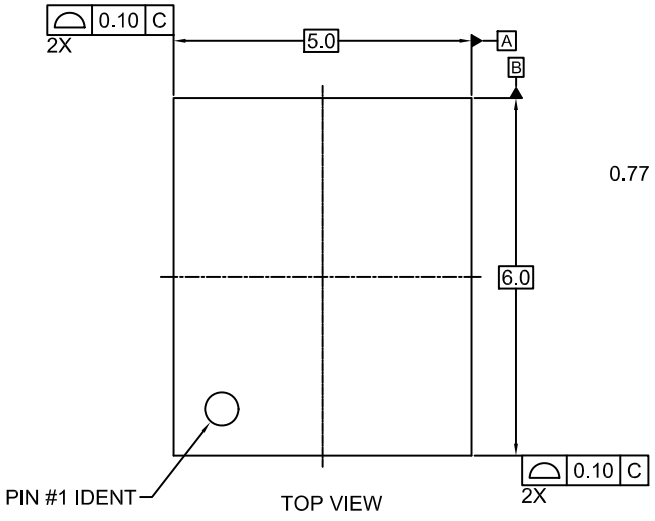
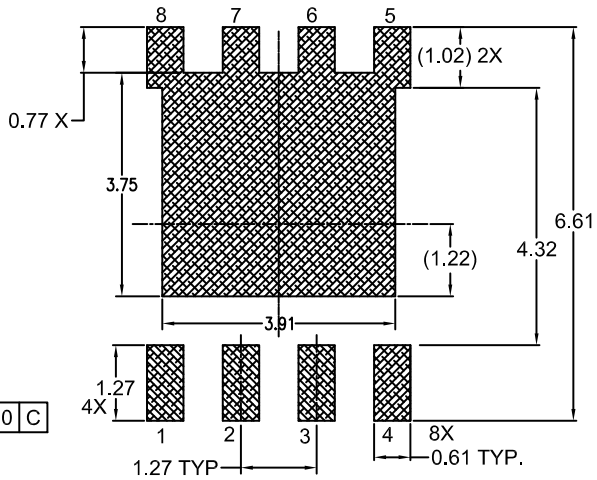
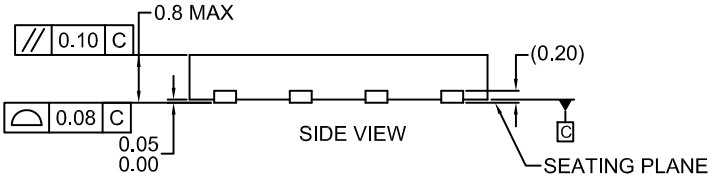
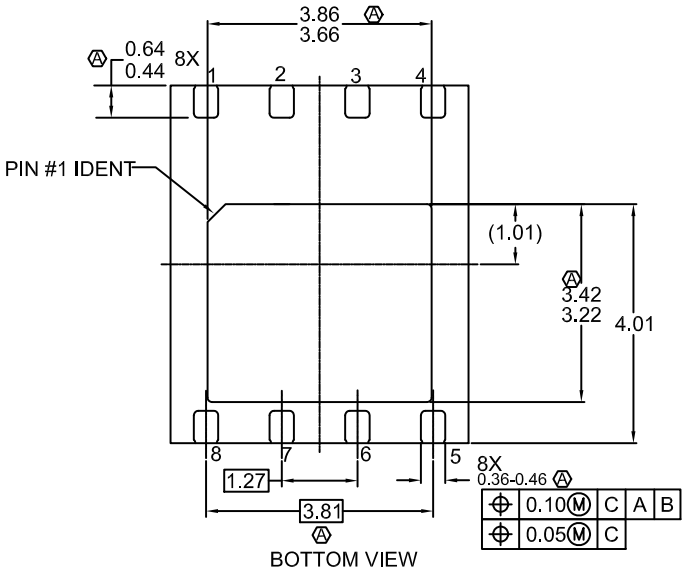




RECOMMENDED LAND PATTERN





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