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2015年3月

FDMS86202ET120

N 沟道屏蔽栅极 PowerTrench[®] MOSFET 120 V, 102 A, 7.2 mΩ

特性

- 扩展额定 T」至 175°C
- 屏蔽栅极 MOSFET 技术
- 最大 $r_{DS(on)}$ = 7.2 m Ω (V_{GS} = 10 V, I_D = 13.5 A)
- 最大 $r_{DS(on)}$ = 10.3 $m\Omega$ (V_{GS} = 6 V, I_D = 11.5 A)
- 低 r_{DS(on)} 和高效的先进硅封装
- MSL1 耐用封装设计
- 100% 经过 UIL 测试
- 符合 RoHS 标准

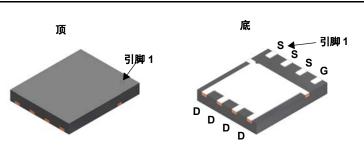


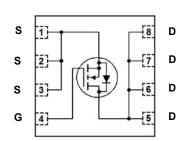
概述

本 N 沟道 MOSFET 采用飞兆半导体先进的 PowerTrench® 工艺制造而成,其中集成了栅极屏蔽技术。该工艺经优化以减小导通电阻,却仍保持卓越的开关性能。

应用

■ DC-DC 转换





Power 56

MOSFET 最大额定值 T_A = 25 °C 除非另有说明

符号	参数		额定值	单位	
V_{DS}	漏极一源极电压			120	٧
V_{GS}	栅极一源极电压			±20	V
	漏极电流 - 连续	T _C = 25 °C	(注5)	102	
	- 连续	T _C = 100°C	(注 5)	72	A
I _D	- 连续	T _A = 25°C	(注 1a)	13.5	_ ^
	- 脉冲		(注4)	538	
E _{AS}	单脉冲雪崩能量		(注 3)	600	mJ
P _D	功耗	T _C = 25 °C		187	W
	功耗	T _A = 25 °C	(注 1a)	3.3	VV
T _J , T _{STG}	工作和存储结温范围			-55 至 +175	°C

热性能

$R_{\theta JC}$	结一壳体的热阻	0.8	°C/W
$R_{\theta JA}$	结至环境热阻最大值 (注 1a)	45	C/VV

封装标识与定购信息

器件标识	器件	封装	卷尺寸	带宽	数量
FDMS86202ET	FDMS86202ET120	Power 56	13 "	12 mm	3000 units

电气特性 T」= 25°C,除非另有说明

参数

关断特性						
BV_{DSS}	漏极一源极击穿电压	$I_D = 250 \mu A, V_{GS} = 0 V$	120			V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	击穿电压温度系数	I _D = 250 μA (相对 25 °C)		103		mV/°C
I _{DSS}	零栅极电压漏极电流	V _{DS} = 96 V, V _{GS} = 0 V			1	μΑ
I_{GSS}	栅极一源极漏电流	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA

测试条件

导通特性

符号

V _{GS(th)}	栅极一源极阈值电压	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$	2.0	3.1	4.0	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	栅极一源极阈值电压温度系数	I _D = 250 μA (相对 25 °C)		-10		mV/°C
		V _{GS} = 10 V, I _D = 13.5 A		6.0	7.2	
r _{DS(on)}	漏极至源极静态导通电阻	V _{GS} = 6 V, I _D = 11.5 A		8.1	10.3	mΩ
		V_{GS} = 10 V, I_D = 13.5 A, T_J = 125 °C		10.9	13.2	
9 _{FS}	正向跨导	V _{DS} = 5 V, I _D = 13.5 A		44		S

动态特性

C _{iss}	输入电容	V _{DS} = 60 V, V _{GS} = 0 V, f = 1 MHz		3275	4585	pF
Coss	输出电容			460	644	pF
C _{rss}	反向传输电容			17	30	pF
R _g	栅极阻抗		0.1	0.9	2.7	Ω

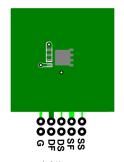
开关特性

t _{d(on)}	导通延迟时间		21	33	ns
t _r	上升时间	V _{DD} = 60 V, I _D = 13.5 A,	8.75	17.5	ns
t _{d(off)}	关断延迟时间	V_{GS} = 10 V, R_{GEN} = 6 Ω	27.2	44	ns
t _f	下降时间		6.1	12.2	ns
Q_q	总栅极电荷	V _{GS} = 0 V 到 10 V	45	64	nC
Q_g	总栅极电荷	V _{GS} = 0 V 到 6 V V _{DD} = 60 V,	29	41	nC
Q_{gs}	栅极一源极电荷	I _D = 13.5 A	14.3		nC
Q_{qd}	栅极一漏极"米勒"电荷		9.5		nC

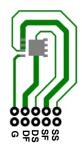
漏极 - 源极二极管特性

V_{SD}	源极一漏极二极管正向电压	V _{GS} = 0 V, I _S = 2.1 A (注 2)	0.69	1.2	V
		V _{GS} = 0 V, I _S = 13.5 A (注 2)	0.76	1.3	
t _{rr}	反向恢复时间	I _F = 13.5 A, di/dt = 100 A/μs	79	127	ns
Q _{rr}	反向恢复电荷	ης – 13.5 A, αναι – 100 Ανμε	140	224	nC

注意: 1. $R_{\theta JA}$ 取决于安装在 FR-4 材质 1.5 x 1.5 英寸电路板上 1 英寸 2 2 盎司铜焊盘上的器件。 $R_{\theta CA}$ 取决于使用者的电路板设计。



a) 45 °C/W (安装于 1 英寸² 的 2 盎司铜焊盘)



b) 115 °C/W (安装于最小 2 盘 司铜焊盘上)

最小值 典型值 最大值 单位

- 2. 脉冲测试: 脉宽 < 300 μ, 占空比 < 2.0%。
- 3. 600 mJ 的 E_{AS} 取决于起始 E_{AS} 取决于起始 E_{AS} $E_{$
- 4. 脉冲 ld 请参见图 11 SOA 曲线。
- 5. 直流理论值仅受限于最大结温, 直流实际值则同时受限于热和机电电路板的设计。

2

典型特性 T」= 25 ℃ 除非另有说明

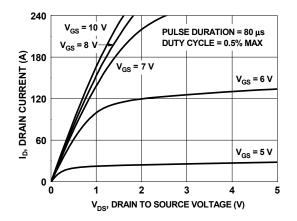


图 1. 导通区域特性

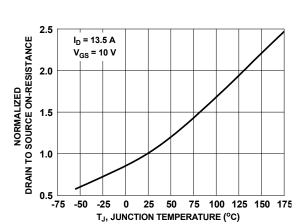


图 3. 标准化导通电阻 vs 结温

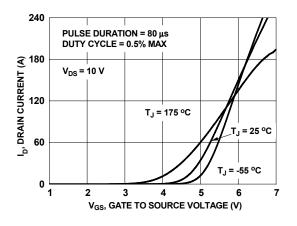


图 5. 转换特性

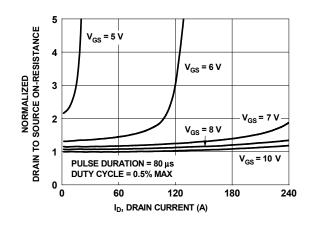


图 2. 标准化导通电阻 vs 漏极电流和栅极电压

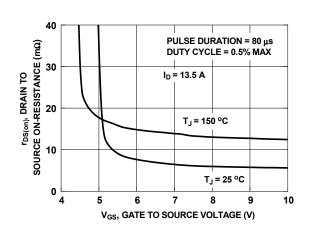


图 4. 导通电阻 vs 栅极一源极电压

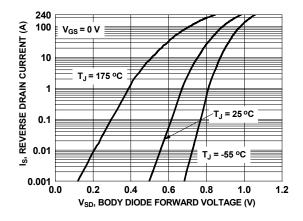
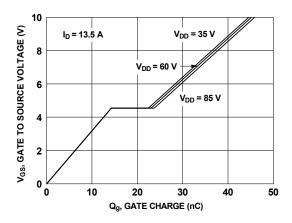


图 6. 源极一漏极二极管正向电压 vs 源极电流

典型特性 T」= 25 ℃ 除非另有说明



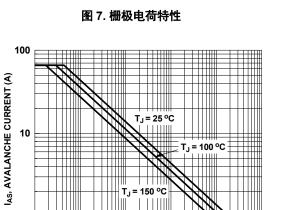


图 9. 非钳位感应开关能力

 \mathbf{t}_{AV} , TIME IN AVALANCHE (ms)

100

1000

10

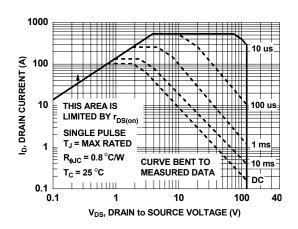


图 11. 正向偏置安全工作区

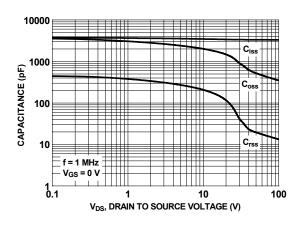


图 8. 电容 vs 漏极一源极电压

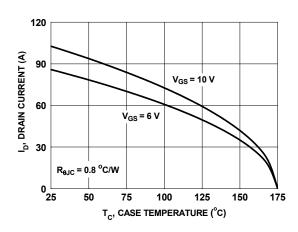


图 10. 最大连续漏极电流 vs 壳体温度

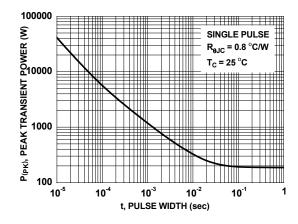


图 12. 单脉冲最大功耗

1 └─ 0.01

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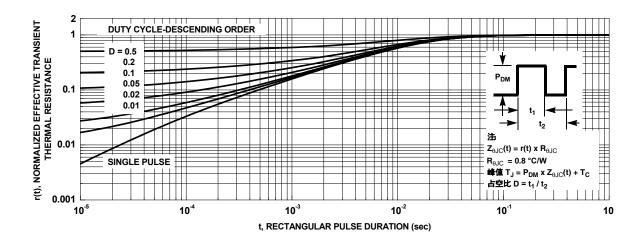
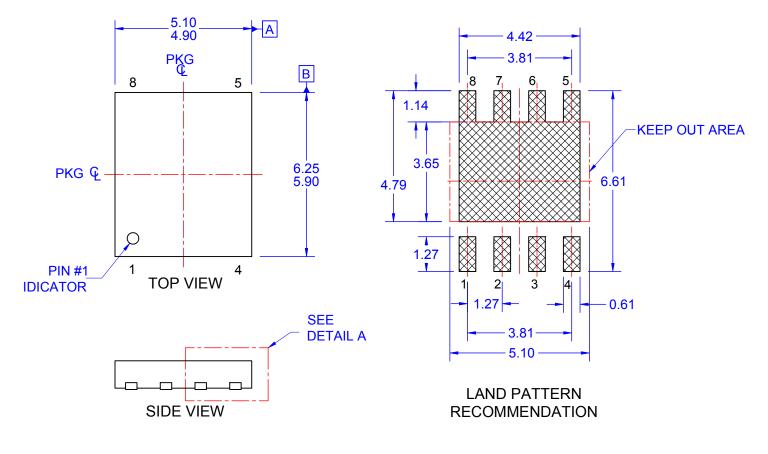
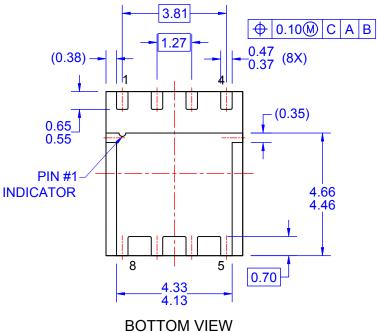
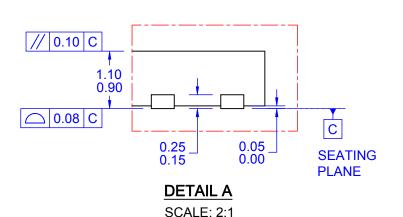


图 13. 结 - 环境之间瞬态热响应曲线







NOTES: UNLESS OTHERWISE SPECIFIED

- A) PACKAGE STANDARD REFERENCE: JEDEC MO-240, ISSUE A, VAR. AA,
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH. MOLD FLASH OR BURRS DOES NOT EXCEED 0.10MM.
- D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009.
- E) IT IS RECOMMENDED TO HAVE NO TRACES OR VIAS WITHIN THE KEEP OUT AREA.
- F) DRAWING FILE NAME: PQFN08JREV3.



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