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FDP054N10 N 沟道 PowerTrench[®]MOSFET 100 V, 144 A, 5.5 mΩ

特性

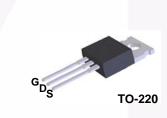
- $R_{DS(on)} = 4.6 \text{ m}\Omega \text{ (Typ.)}@V_{GS} = 10 \text{ V}, I_D = 75 \text{ A}$
- 快速开关速度
- 低栅极电荷
- 高性能沟道技术可实现极低的 R_{DS(on)}
- 高功率和高电流处理能力
- ・ 符合 RoHS 标准

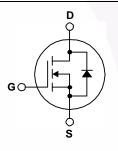
说明

此 N 沟道 MOSFET 采用飞兆半导体先进的 PowerTrench[®] 工艺 生产,这一先进工艺是专为最大限度地降低通态电阻并保持卓越 开关性能而定制的。

应用

- 用于 ATX/Server/Telecom PSU 的同步整流
- 电池保护电路
- 电机驱动和不间断电源
- 微型太阳能逆变器





MOSFET 最大额定值 Tc=25°C 除非另有说明。

符号	参数			FDP054N10	单位
V _{DSS}	漏极一源极电压		100	V	
V _{GSS}	栅极一源极电压			±20	V
		一连续(T _C = 25 ^o C, 硅限制)		144	
ID	漏极电流	一连续 (T _C = 100 ^o C,硅限制)		102	Α
		一连续 (T _C = 25 ^o C,封装限制)		120	
I _{DM}	漏极电流	一脉冲	(注1)	576	Α
E _{AS}	单脉冲雪崩能量		(注2)	1153	mJ
dv/dt	峰值二极管雪崩能量		(注3)	6	V/ns
P _D		$(T_{\rm C} = 25^{\rm o}{\rm C})$		263	W
	功耗	一超过 25 [°] C 时降额		1.75	W/ºC
T _J , T _{STG}	工作和存储温度范围			-55 至 +175	°C
ΤL	用于焊接的最大引脚温度,距离外壳 1/8",持续 5 秒			300	°C

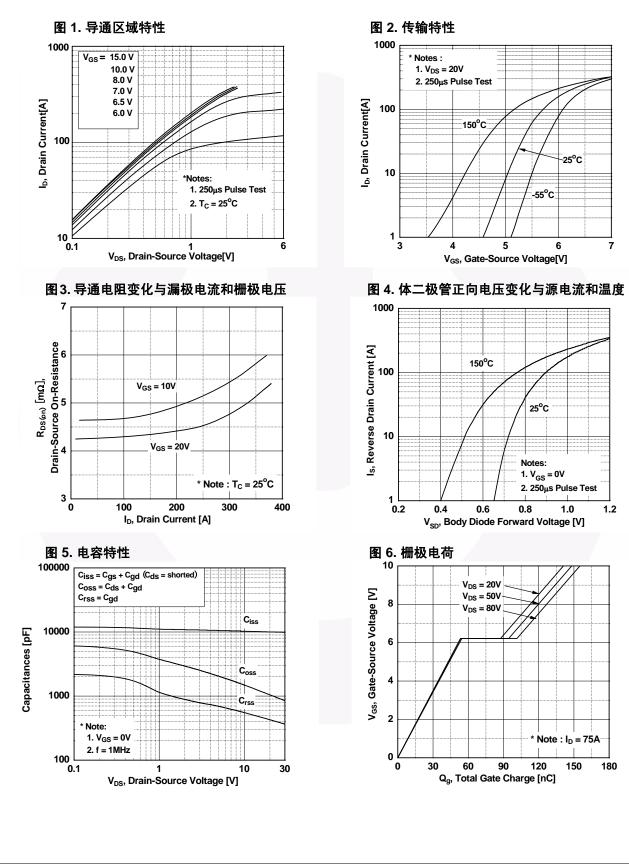
热性能

符号	参数	FDP054N10	单位
$R_{\theta JC}$	结至外壳热阻最大值	0.57	°C/W
R _{θJA}	结至环境热阻最大值	62.5	°C/W

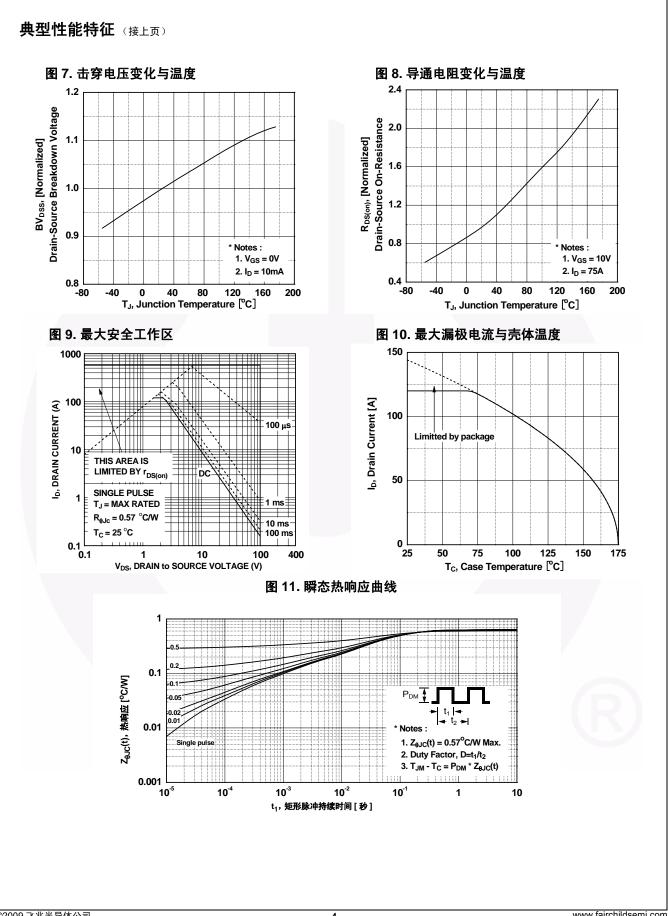
2013 年 12 月

器件组	扁号	顶标	封装	包装方法	卷尺寸		带宽	数	量
		TO-220			不适用		50 单元		
- /는 나는 사네									
	T _C = 25°C	除非另有说明。			• <i>b</i> L		-H- 771 /		
符号		参数		测试象	₹1 1	最小值	典型值	最大值	单位
关断特性									
BV _{DSS}	漏极一源	极击穿电压	l	$I_D = 250 \ \mu A, V_{GS} = 0 \ V, T_C = 25^{\circ}C$		100	-	-	V
ΔBV _{DSS} / ΔT _J	击穿电压温度系数		1	I _D = 250 µA,参考 25 ^o C			0.01	-	V/ºC
	雷坦机山	零栅极电压漏极电流		V _{DS} = 100 V, V _{GS} = 0 V		-	-	1	μA
DSS	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	压 滴似电流	Y	V _{DS} = 100 V, V _{GS} =	= 0 V, T _C = 150 ^o C	-	-	500	μΑ
GSS	栅极一体	漏电流	Y	V _{GS} = ±20 V, V _{DS} =	0 V	-	-	±100	nA
导通特性									
V _{GS(th)}	栅极阈值	电压	,	V _{GS} = V _{DS} , I _D = 25	0 μΑ	2.5	3.5	4.5	V
R _{DS(on)}		极静态导通电阻		V _{GS} = 10 V, I _D = 75		-	4.6	5.5	mΩ
9 _{FS}	正向跨导		,	V _{GS} = 10 V, I _D = 75	A	-	192	-	S
动态特性									
C _{iss}	输入电容					-	9985	13280	pF
C _{oss}	输出电容			$V_{DS} = 25 V, V_{GS} =$	0 V,	-	935	1245	pF
C _{rss}	反向传输	电容		f = 1 MHz		-	390	585	pF
Q _{g(tot)}		极电荷总量	,	V _{DS} = 80 V, I _D = 75	Α,	-	156	203	nC
Q _{gs}	栅极一源	极栅极电荷		V _{GS} = 10 V	,	-	53	-	nC
Q _{gd}	栅极一漏	极 " 密勒 " 电荷			(说明 4)	-	48	-	nC
干关特性									
d(on)	导通延迟	时间				-	44	98	ns
r	开通上升			$V_{DD} = 50 \text{ V}, \text{ I}_{D} = 75 \text{ V}$		-	92	194	ns
d(off)	关断延迟			V _{GS} = 10 V, R _G = 4	.7 \(\D)	-	80	170	ns
^l f	关断下降I	时间			(说明4)	-	39	88	ns
虽极 一酒权	、 人二极管特·	性	I						
s		Ⅰ 极二极管最大正向连续	申流			-	-	144	Α
SM		极二极管最大正向脉冲				-	-	576	Α
V _{SD}		漏极一源极二极管正向电压 V _{GS} = 0 V, I _{SD} = 75 A		-	-	1.3	V		
rr	反向恢复		,	$V_{GS} = 0 V, I_{SD} = 75 A,$ dI _F /dt = 100 A/µs		-	57		ns
	反向恢复					-	121	-	nC

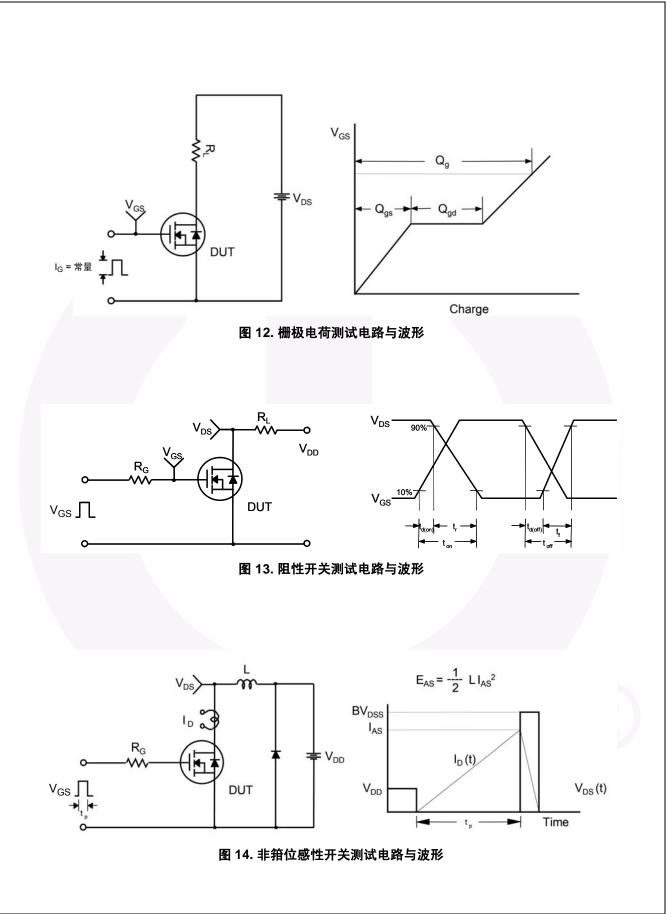
典型性能特征



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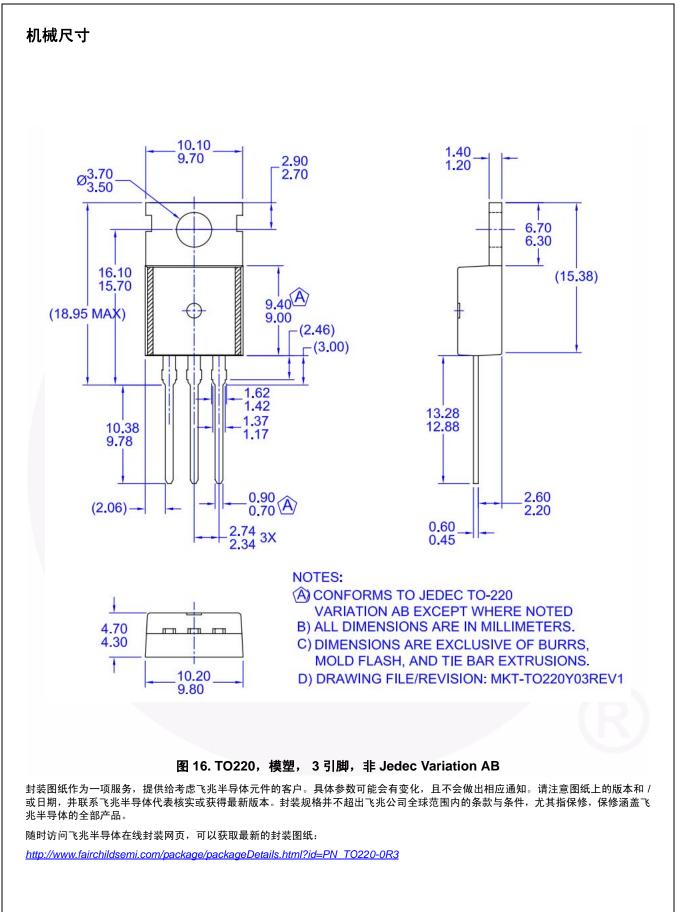


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FDP054N10 — N 沟道 PowerTrench[®] MOSFET

DUT + v_{DS} ۱_{SD} م a L Driver R_G, Same Type as DUT L F ∨_{DD} ∏∏ V_{GS} • dv/dt controlled by R_{G} • I_{SD} controlled by pulse period ſ Gate Pulse Width V_{GS} D = Gate Pulse Period 10V (Driver) \mathbf{I}_{FM} , Body Diode Forward Current I _{SD} di/dt (DUT) I_{RM} Body Diode Reverse Current V_{DS} (DUT) Body Diode Recovery dv/dt V_{SD} V_{PD} Body Diode Forward Voltage Drop 图 15. 二极管恢复 dv/dt 峰值测试电路与波形



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FDP054N10 — N 沟道 PowerTrench[®] MOSFET



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