# onsemi

## MOSFET – N-Channel, UniFET™

## 650 V, 15 A, 440 m $\Omega$

# FDPF15N65

## Description

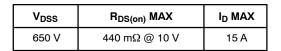
UniFET<sup>™</sup> MOSFET is **onsemi**'s high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.

## Features

- $R_{DS(on)} = 360 \text{ m}\Omega \text{ (Typ.)} @ V_{GS} = 10 \text{ V}, I_D = 7.5 \text{ A}$
- Low Gate Charge (Typ. 48.5 nC)
- Low C<sub>rss</sub> (Typ. 23.6 pF)
- 100% Avalanche Tested

## Applications

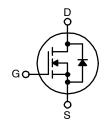
- LCD / LED / PDP TV and Monitor
- Uninterruptible Power Supply



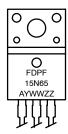


TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT

#### **N-CHANNEL MOSFET**



MARKING DIAGRAM



### ORDERING INFORMATION

See detailed ordering and shipping information on page 8 of this data sheet.

#### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = $25^{\circ}$ C unless otherwise noted)

Symbol	Parameter Drain-Source Voltage		Rating	Unit V
V <sub>DSS</sub>			650	
Ι <sub>D</sub>	Drain Current	- Continuous ( $T_C = 25^{\circ}C$ ) - Continuous ( $T_C = 100^{\circ}C$ )	15* 9.5*	A A
I <sub>DM</sub>	Drain Current	– Pulsed (Note 1)	60*	А
V <sub>GSS</sub>	Gate-Source Voltage		±30	V
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		637	mJ
I <sub>AR</sub>	Avalanche Current (Note 1)		15	А
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		25.0	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5	V/ns
P <sub>D</sub>	Power Dissipation	(T <sub>C</sub> = 25°C) – Derate Above 25°C	38.5 0.3	W W/°C
$T_{J_1} T_{STG}$	Operating and Storage Temperature Range		-55 to +150	°C
ΤL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality Stresses exceeding those listed in the Maximum Hatings table may damage to should not be assumed, damage may occur and reliability may be affected. \*Drain current limited by maximum junction temperature. 1. Repetitive rating: pulse-width limited by maximum junction temperature. 2. L = 5.23 mH, I<sub>AS</sub> = 15 A, V<sub>DD</sub> = 50 V, R<sub>G</sub> = 25  $\Omega$ , starting T<sub>J</sub> = 25°C. 3. I<sub>SD</sub> ≤ 15 A, di/dt ≤ 200 A/µs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, starting T<sub>J</sub> = 25°C.

## **THERMAL CHARACTERISTICS**

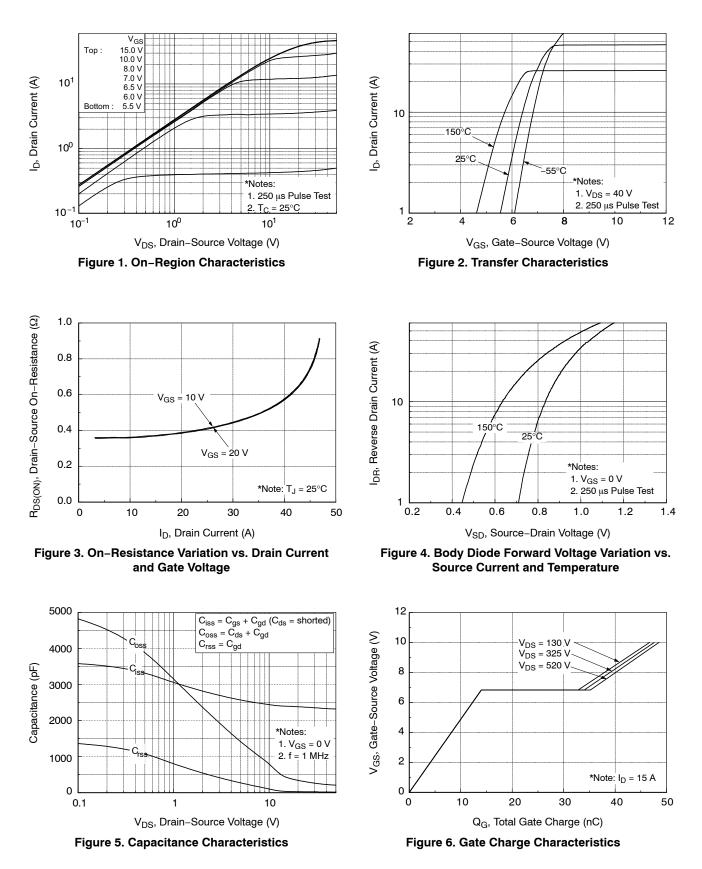
Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	3.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W

## **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
OFF CHAR	ACTERISTICS		-	-	-	•
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ = 0 V, $I_D$ = 250 $\mu A,~T_J$ = 25°C	650	_	-	V
$\begin{array}{c} \Delta \text{BV}_{\text{DSS}} \\ /  \Delta \text{T}_{\text{J}} \end{array}$	Breakdown Voltage Temperature Coefficient	$I_D$ = 250 µA, Referenced to 25°C	_	0.65	-	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 650 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 520 \text{ V}, T_C = 125^{\circ}\text{C}$			1 10	μΑ μΑ
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	$V_{GS} = 30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$	-	-	100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	$V_{GS} = -30 \text{ V},  V_{DS} = 0 \text{ V}$	-	-	-100	nA
ON CHARA	ACTERISTICS					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	3.0	-	5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.5 A	-	0.36	0.44	Ω
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 40 V, I <sub>D</sub> = 7.5 A	-	19.2	-	S
OYNAMIC	CHARACTERISTICS					
C <sub>iss</sub>	Input Capacitance	$V_{DS}$ = 25 V, $V_{GS}$ = 0 V, f = 1 MHz	-	2380	3095	pF
C <sub>oss</sub>	Output Capacitance		-	295	385	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	23.6	35.5	pF
SWITCHIN	G CHARACTERISTICS					
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD}$ = 325 V, I_D = 15 A, $V_{GS}$ = 10 V, $R_G$ = 21.7 $\Omega$ (Note 4)	-	65	140	ns
t <sub>r</sub>	Turn–On Rise Time		-	125	260	ns
t <sub>d(off)</sub>	Turn–Off Delay Time		-	105	220	ns
t <sub>f</sub>	Turn-Off Fall Time		-	65	140	ns
Qg	Total Gate Charge	$V_{DS} = 520 \text{ V}, \text{ I}_{D} = 15 \text{ A}, \text{ V}_{GS} = 10 \text{ V}$	-	48.5	63.0	nC
Q <sub>gs</sub>	Gate-Source Charge	(Note 4)	-	14.0	-	nC
Q <sub>gd</sub>	Gate-Drain Charge		-	21.2	-	nC
DRAIN-SO	URCE DIODE CHARACTERISTICS AND MAX	IMUM RATINGS				
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current		-	-	15*	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current		-	-	60	Α
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS} = 0 V, I_{S} = 15 A$	-	-	1.4	V
t <sub>rr</sub>	Reverse Recovery Time	$V_{GS} = 0 V, I_S = 15 A,$	-	496	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI <sub>F</sub> /dt = 100 A/µs	-	5.69	-	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 4. Essentially independent of operating temperature typical characteristics.

## **TYPICAL PERFORMANCE CHARACTERISTICS**



## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

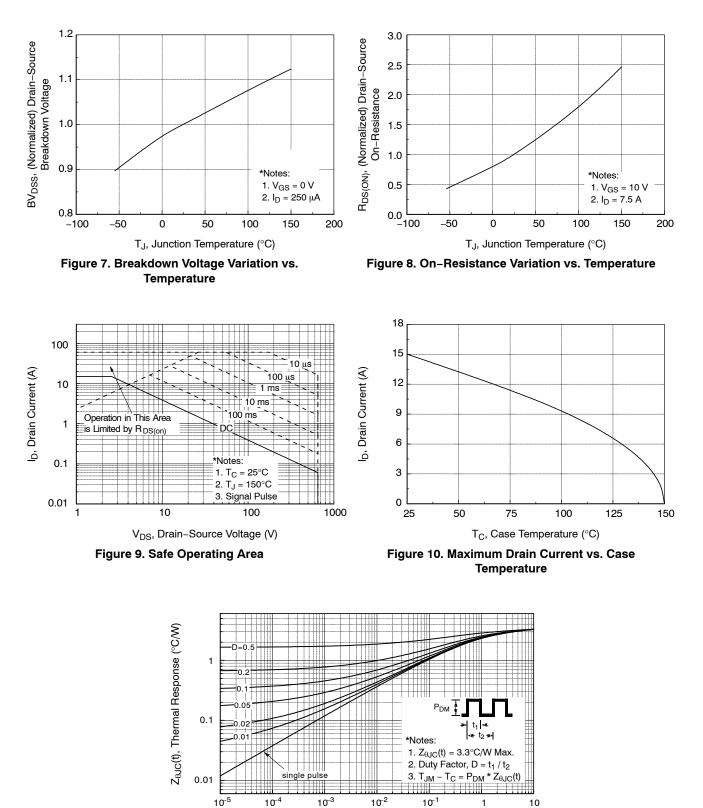


Figure 11. Transient Thermal Response Curve

t1, Square Wave Pulse Duration (s)

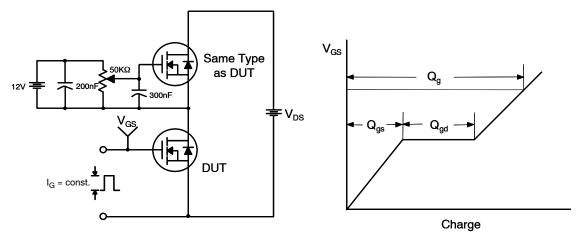


Figure 12. Gate Charge Test Circuit & Waveform

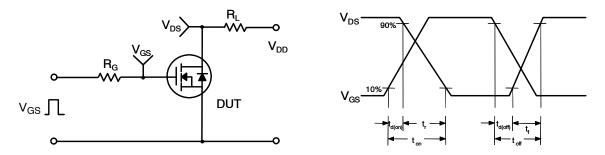


Figure 13. Resistive Switching Test Circuit & Waveforms

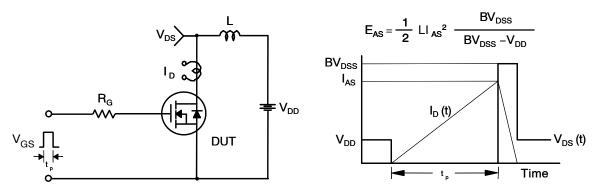


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

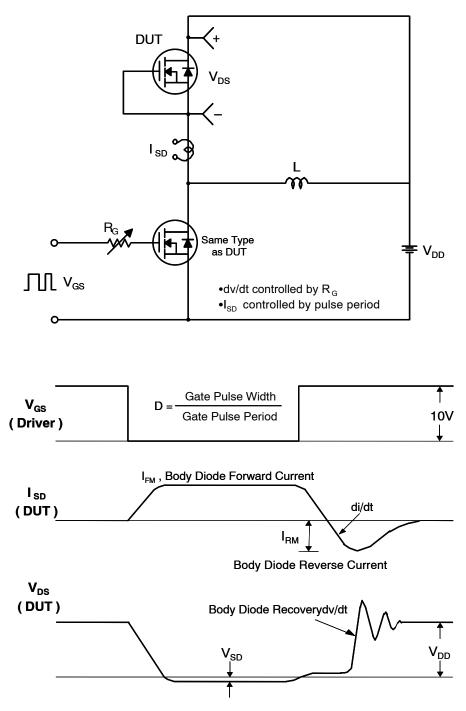


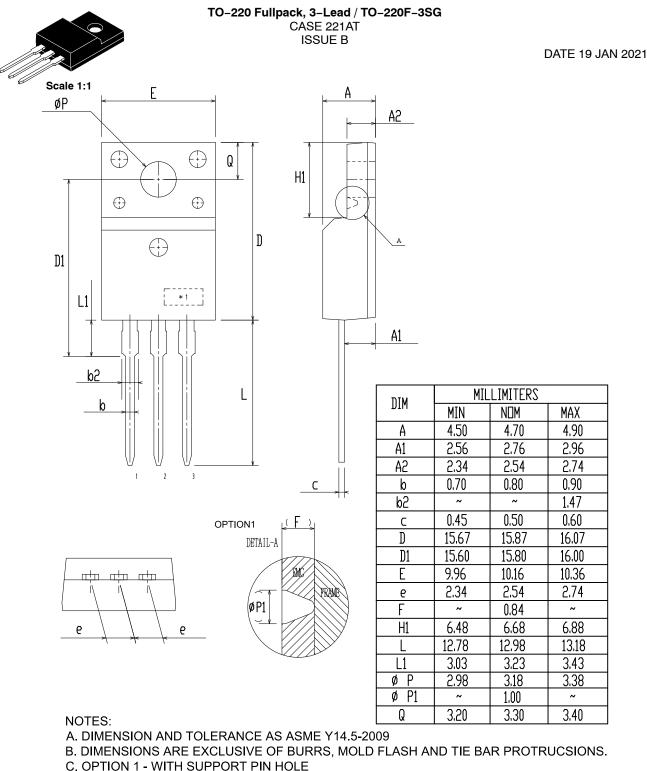
Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

## PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Shipping
FDPF15N65	FDPF15N65	TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT	1,000 Units / Tube

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OPTION 2 - NO SUPPORT PIN HOLE

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DESCRIPTION:	TO-220 FULLPACK, 3-LEAD / TO-220F-3SG		PAGE 1 OF 1	

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