

### **KSB1151**

#### **Feature**

- Low Collector-Emitter Saturation Voltage
- Large Collector Current
- High Power Dissipation : P<sub>C</sub>=1.3W (T<sub>a</sub>=25°C)
- Complement to KSD 1691



1. Er. er 2 'olle ... Base

## **PNP Epitaxial Silicon Transistor**

### Absolute Maximum Ratings T<sub>C</sub>=25°C unless other in a note

Symbol	Parameter	Va'u e	Units
$V_{CBO}$	Collector-Base Voltage	- 60	N. P.
$V_{CEO}$	Collector-Emitter Voltage	- 60	
$V_{EBO}$	Emitter-Base Voltage	7	V
I <sub>C</sub>	Collector Current (D	- 5	A
I <sub>CP</sub>	*Collector Current (Pu. )	- 3	А
I <sub>B</sub>	Base Curr , (	\(\frac{1}{2}\)	А
P <sub>C</sub>	Collector ssipatior a=20°C)	1.3	W
	Juector D inatio (T <sub>C</sub> =25°C)	20	W
T <sub>J</sub>	unction Temperature	150	°C
T <sub>STG</sub>	rage mperature	- 55 ~ 150	°C

#### cti a! aracter! stics TC=25°C unless otherwise noted

Syı. ol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I <sub>CB</sub> r	Collector Cut-off Current	$V_{CB} = -50V, I_{E} = 0$			- 10	μΑ
00	Emilier Cut-off Current	$V_{EB} = -7V, I_{C} = 0$			- 10	μΑ
h <sub>FE</sub>	* DC Cu.ront Gain	$V_{CE} = -1V, I_{C} = -0.1A$	60			
h <sub>⊏L</sub> .	-21	$V_{CE} = -1V, I_{C} = -2A$	100	200	400	
h <sub>F <u>!</u>3</sub>		$V_{CE} = -2V, I_{C} = -5A$	50			
V <sub>CE</sub> (sat)	* Collector Emirter Saturation Voltage	$I_C = -2A, I_B = -0.2A$		- 0.14	- 0.3	V
V <sub>BE</sub> (sat)	* Base Emitter Saturation Voltage	$I_C = -2A, I_B = -0.2A$		- 0.9	- 1.2	V
t <sub>ON</sub>	Turn On Time	$V_{CC} = -10V, I_{C} = -2A$		0.15	1	μs
t <sub>STG</sub>	Storage Time	$I_{B1} = -I_{B2} = 0.2A$		0.78	2.5	μs
t <sub>F</sub>	Fall Time	$RL = 5\Omega$		0.18	1	μs

#### \* Pulse test: PW≤350μs, Duty Cycle≤2% Pulsed

### **h**<sub>FE</sub> Classification

Classification	0	Y	G
h <sub>FE2</sub>	100 ~ 200	160 ~ 320	200 ~ 400

## **Typical Characteristics**

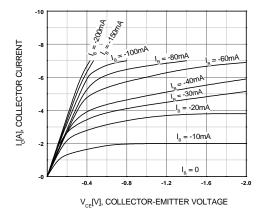


Figure 1. Static Characteristic

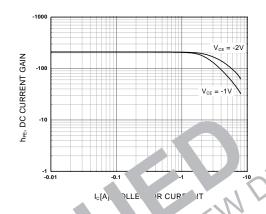
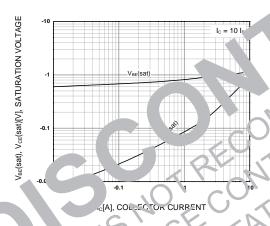


Fig. e 2. D. current Gain



gure 3. Collector Emitter Saturation Voltage Post-Emitter Saturation Voltage

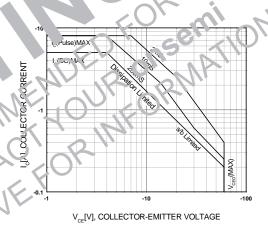


Figure 4. Forward Bias Operating Area

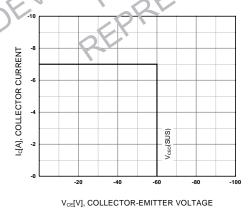


Figure 5. Reverse Bias Safe Operating Area

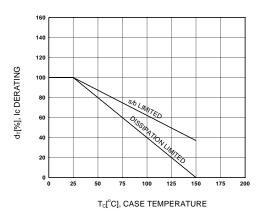
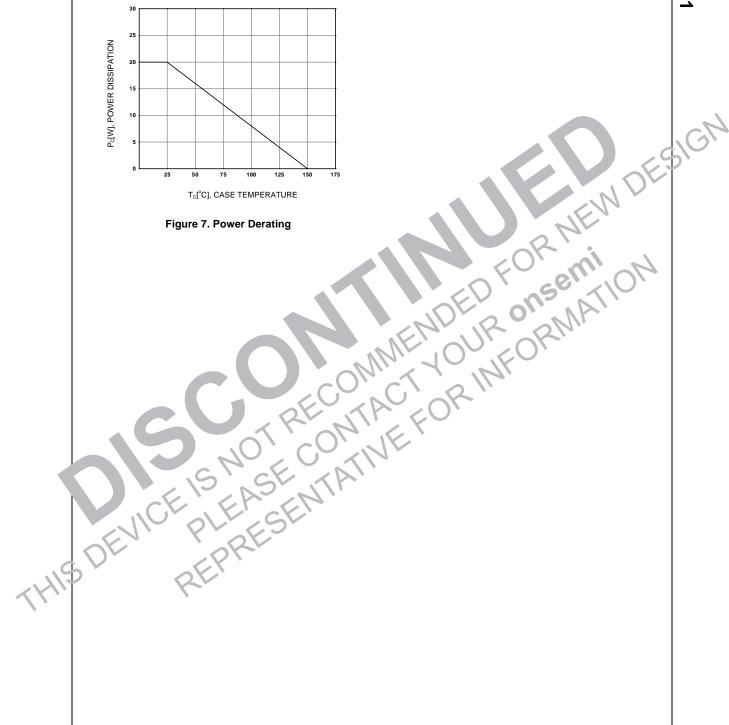
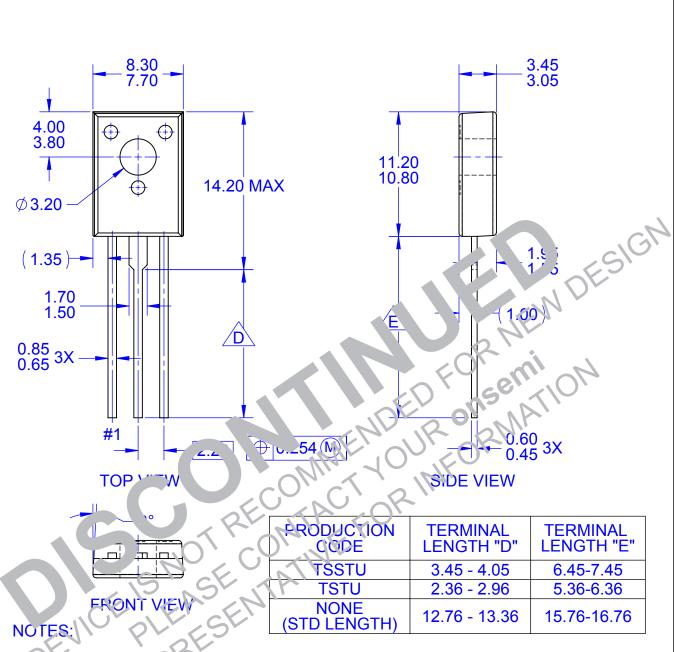


Figure 6. Derating Curve of Safe Operating Areas

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# **Typical Characteristics** (Continued)





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