Surface Mount Schottky Power Rectifier

MBRAF360T3G, NRVBAF360T3G

This device employs the Schottky Barrier principle in a large area metal−to−silicon power diode. State−of−the−art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity protection diodes in surface mount applications where compact size and weight are critical to the system.

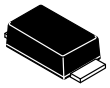
Features

- Low Profile Package for Space Constrained Applications
- Rectangular Package for Automated Handling
- Highly Stable Oxide Passivated Junction
- 150°C Operating Junction Temperature
- Guard−Ring for Stress Protection
- NRVB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC−Q101 Qualified and PPAP Capable
- These are Pb−Free and Halide−Free Devices

Mechanical Charactersistics

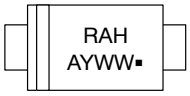
- Case: Epoxy, Molded, Epoxy Meets UL 94, V−0
- Weight: 95 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Cathode Polarity Band
- Device Meets MSL 1 Requirements
- ESD Ratings: Machine Model = C
  Human Body Model = 3B

SCHOTTKY BARRIER RECTIFIER 3.0 AMPERE 60 VOLTS



SMA−FL
CASE 403AA
STYLE 6

MARKING DIAGRAM



RAH	= Specific Device Code
A	= Assembly Location
Y	= Year
WW	= Work Week
▪	= Pb−Free Package

ORDERING INFORMATION

Device	Package	Shipping†
MBRAF360T3G	SMA−FL (Pb−Free)	5000 / Tape & Reel
NRVBAF360T3G	SMA−FL (Pb−Free)	5000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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December, 2016 − Rev. 3
Publication Order Number:
MBRAF360/D

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	60	V
Average Rectified Forward Current	$I_{F(AV)}$	3.0 @ T_L = 100°C 4.0 @ T_L = 80°C	A
Peak Repetitive Forward Current (Rated V_R, Square Wave, 20 kHz) T_C = 125°C	I_{FRM}	6	A
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	I_{FSM}	125	A
Storage Temperature Range	T_{stg}	− 65 to +150	°C
Operating Junction Temperature (Note 1)	T_J	− 65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. The heat generated must be less than the thermal conductivity from Junction−to−Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction−to−Lead (Note 2)	$R_{\theta JL}$	25	°C/W
Thermal Resistance, Junction−to−Ambient (Note 2)	$R_{\theta JA}$	90	°C/W

2. 1 inch square pad size (1 x 0.5 inch for each lead) on FR4 board.

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 3) (i_F = 3.0 A, T_J = 25°C)	V_F	0.63	V
Maximum Instantaneous Reverse Current (Note 3) (Rated dc Voltage, T_J = 25°C) (Rated dc Voltage, T_J = 100°C)	i_R	0.03 3.0	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse Test: Pulse Width = 300 μs, Duty Cycle ≤ 2.0%.

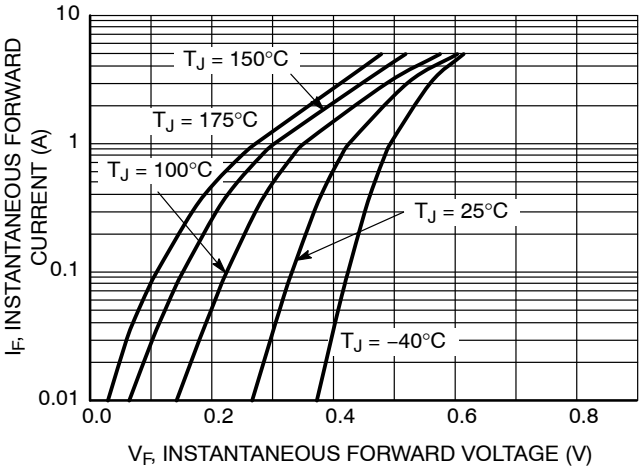


Figure 1. Typical Forward Voltage

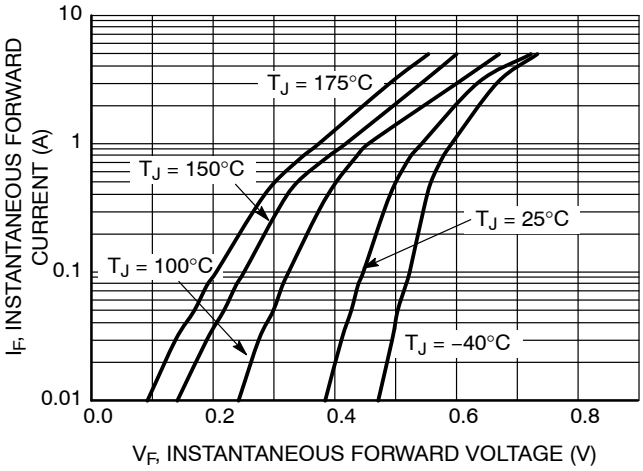


Figure 2. Maximum Forward Voltage

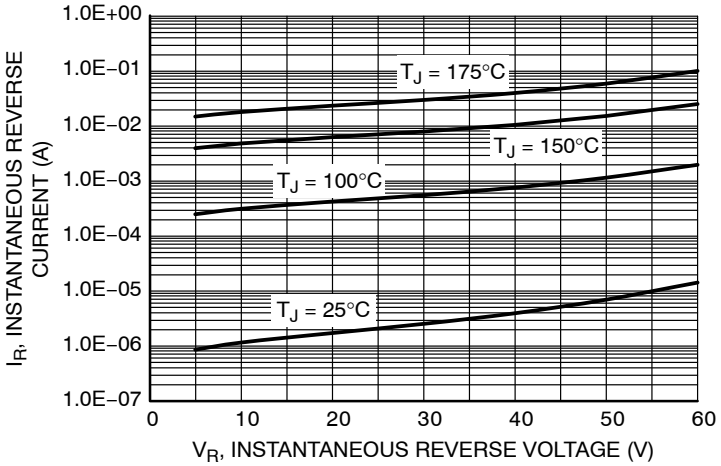


Figure 3. Typical Reverse Current

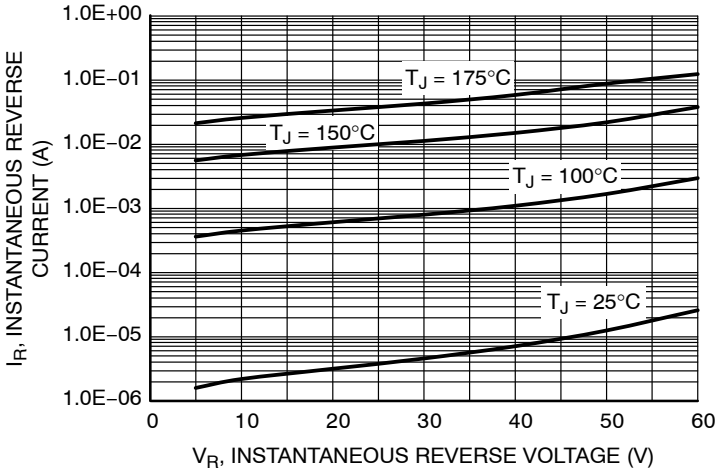


Figure 4. Maximum Reverse Current

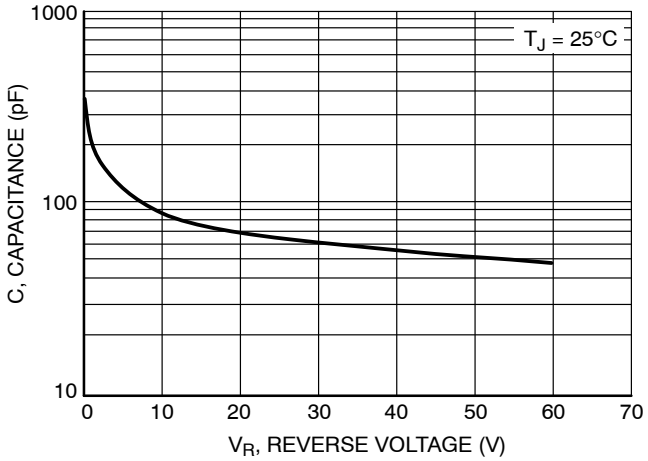


Figure 5. Typical Capacitance

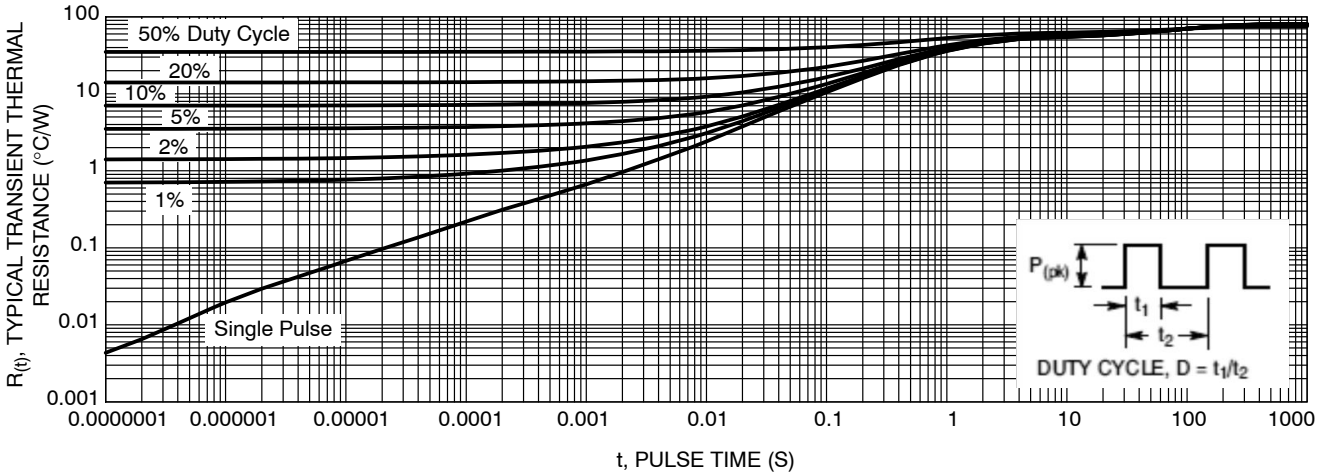
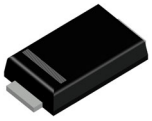
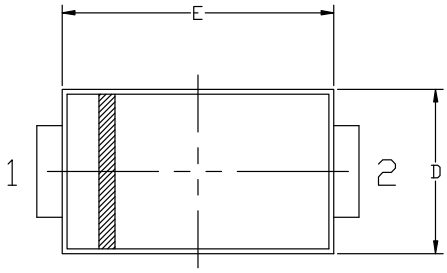


Figure 6. Typical Transient Thermal Response, Junction−to−Ambient

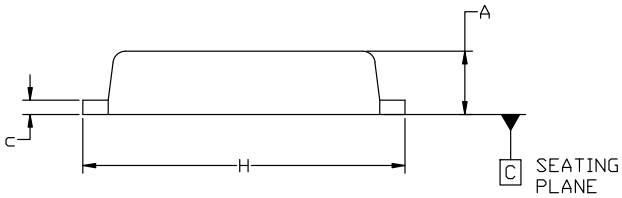


SMA 2.60x4.30x1.00
CASE 403AA
ISSUE A

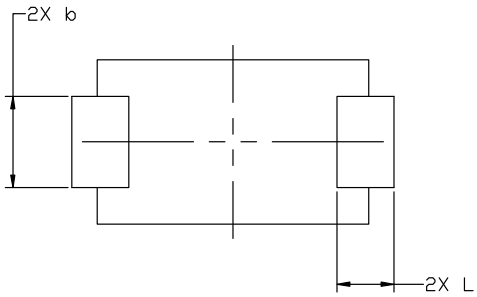
DATE 18 JAN 2024



TOP VIEW



SIDE VIEW

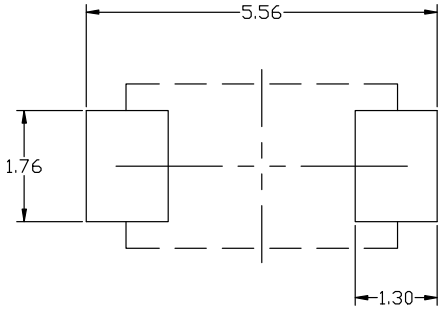


BOTTOM VIEW

NOTES:

1. DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. FL

MILLIMETERS			
DIM	MIN	NOM	MAX
A	0.90	1.00	1.10
b	1.25	1.45	1.65
c	0.15	0.225	0.30
D	2.40	2.60	2.80
E	4.00	4.30	4.60
H	4.80	5.10	5.40
L	0.70	0.90	1.10



RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb-Free strategy and soldering details, please download th e ON Semiconductor Soldering and Mounting Techniques Reference manual, SOLDERRM/D.

GENERIC
MARKING DIAGRAM*

XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
▪ = Pb−Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb−Free indicator, “G” or microdot “▪”, may or may not be present. Some products may not follow the Generic Marking.

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