

# TinyLogic UHS Three-Input Exclusive-OR Gate

## NC7SZ386

### Description

The NC7SZ386 is a single three-input Exclusive-OR Gate from onsemi's Ultra-High Speed Series of TinyLogic. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a broad  $V_{CC}$  operating range. The device is specified to operate over the 1.65 V to 5.5 V  $V_{CC}$  operating range. The inputs and output are high impedance when  $V_{CC}$  is 0 V. Inputs tolerate voltages up to 5.5 V independent of  $V_{CC}$  operating voltage.

### Features

- Ultra-High Speed:  $t_{PD} = 4.8$  ns (Typical) into 50 pF at 5 V  $V_{CC}$
- High Output Drive:  $\pm 24$  mA at 3 V  $V_{CC}$
- Broad  $V_{CC}$  Operating Range: 1.65 V to 5.5 V
- Power Down High Impedance Inputs / Outputs
- Over-Voltage Tolerance Inputs Facilitate 5 V to 3 V Translation
- Proprietary Noise / EMI Reduction Circuitry
- Ultra-Small MicroPak™ Packages
- Space-Saving SC70 Package
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

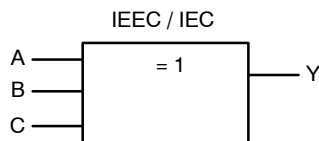
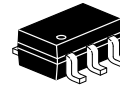
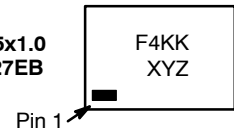


Figure 1. Logic Symbol

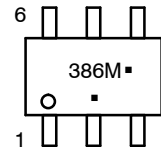
### MARKING DIAGRAMS



SIP6 1.45x1.0  
CASE 127EB



SC-88  
CASE 419B-02



|         |                                     |
|---------|-------------------------------------|
| F4, 386 | = Specific Device Code              |
| KK      | = 2-Digit Lot Run Traceability Code |
| XY      | = 2-Digit Date Code Format          |
| Z       | = Assembly Plant Code               |
| M       | = Date Code*                        |
| ▪       | = Pb-Free Package                   |

(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

### ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

NOTE: Some of the devices on this data sheet have been **DISCONTINUED**. Please refer to the table on page 5.

## Pin Configurations

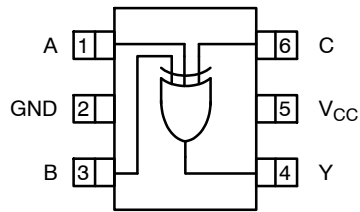


Figure 2. SC-88 (Top View)

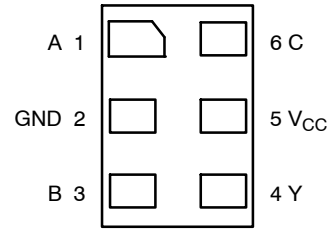
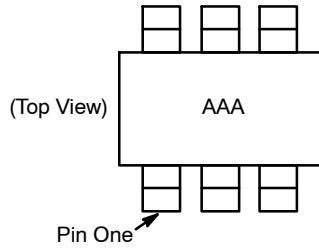


Figure 4. MicroPak (Top Through View)



AAA represents Product Code Top Mark – see ordering code.

NOTE: Orientation of Top Mark determines Pin One location.  
Read the Top Product Code Mark left to right, Pin One is the lower left pin (see diagram).

Figure 3. Pin 1 Orientation

### PIN DEFINITIONS

| Pin # SC-88 | Pin # MicroPak | Name            | Description    |
|-------------|----------------|-----------------|----------------|
| 1           | 1              | A               | Input          |
| 2           | 2              | GND             | Ground         |
| 3           | 3              | B               | Input          |
| 4           | 4              | Y               | Output         |
| 5           | 5              | V <sub>CC</sub> | Supply Voltage |
| 6           | 6              | C               | Input          |

### FUNCTION TABLE

| Inputs |   |   | Output |
|--------|---|---|--------|
| A      | B | C | Y      |
| L      | L | L | L      |
| L      | L | H | H      |
| L      | H | L | H      |
| L      | H | H | L      |
| H      | L | L | H      |
| H      | L | H | L      |
| H      | H | L | L      |
| H      | H | H | H      |

H = HIGH Logic Level  
L = LOW Logic Level

## ABSOLUTE MAXIMUM RATINGS

| Symbol                              | Parameter                                         |                        | Min  | Max  | Unit |
|-------------------------------------|---------------------------------------------------|------------------------|------|------|------|
| V <sub>CC</sub>                     | Supply Voltage                                    |                        | -0.5 | 6.5  | V    |
| V <sub>IN</sub>                     | DC Input Voltage                                  |                        | -0.5 | 6.5  | V    |
| V <sub>OUT</sub>                    | DC Output Voltage                                 |                        | -0.5 | 6.5  | V    |
| I <sub>IK</sub>                     | DC Input Diode Current                            | V <sub>IN</sub> < 0 V  | -    | -50  | mA   |
| I <sub>OK</sub>                     | DC Output Diode Current                           | V <sub>OUT</sub> < 0 V | -    | -50  | mA   |
| I <sub>OUT</sub>                    | DC Output Current                                 |                        | -    | ±50  | mA   |
| I <sub>CC</sub> or I <sub>GND</sub> | DC V <sub>CC</sub> or Ground Current              |                        | -    | ±50  | mA   |
| T <sub>STG</sub>                    | Storage Temperature Range                         |                        | -65  | +150 | °C   |
| T <sub>J</sub>                      | Junction Temperature Under Bias                   |                        | -    | +150 | °C   |
| T <sub>L</sub>                      | Junction Lead Temperature (Soldering, 10 Seconds) |                        | -    | +260 | °C   |
| P <sub>D</sub>                      | Power Dissipation in Still Air                    | SC-88                  | -    | 332  | mW   |
|                                     |                                                   | MicroPak-6             | -    | 812  |      |
| ESD                                 | Human Body Model, JESD22-A114                     |                        | -    | 4000 | V    |
|                                     | Charge Device Model, JESD22-C101                  |                        | -    | 2000 |      |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## RECOMMENDED OPERATING CONDITIONS

| Symbol                          | Parameter                     | Conditions                             | Min  | Max             | Unit |
|---------------------------------|-------------------------------|----------------------------------------|------|-----------------|------|
| V <sub>CC</sub>                 | Supply Voltage Operating      |                                        | 1.65 | 5.50            | V    |
|                                 | Supply Voltage Data Retention |                                        | 1.50 | 5.50            |      |
| V <sub>IN</sub>                 | Input Voltage                 |                                        | 0    | 5.5             | V    |
| V <sub>OUT</sub>                | Output Voltage                |                                        | 0    | V <sub>CC</sub> | V    |
| T <sub>A</sub>                  | Operating Temperature         |                                        | -40  | +85             | °C   |
| t <sub>r</sub> , t <sub>f</sub> | Input Rise and Fall Times     | V <sub>CC</sub> at 1.8 V, 2.5 V ±0.2 V | 0    | 20              | ns/V |
|                                 |                               | V <sub>CC</sub> at 3.3 V ±0.3 V        | 0    | 10              |      |
|                                 |                               | V <sub>CC</sub> at 5.0 V ±0.5 V        | 0    | 5               |      |
| θ <sub>JA</sub>                 | Thermal Resistance            | SC-88                                  | -    | 377             | °C/W |
|                                 |                               | MicroPak-6                             | -    | 154             |      |

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

1. Unused inputs must be held HIGH or LOW. They may not float.

## DC ELECTRICAL CHARACTERISTICS

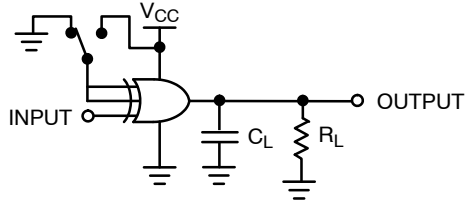
| Symbol           | Parameter                 | V <sub>CC</sub> (V) | Conditions                                                                          | T <sub>A</sub> = 25°C |      |                      | T <sub>A</sub> = -40 to +85°C |                      | Unit |
|------------------|---------------------------|---------------------|-------------------------------------------------------------------------------------|-----------------------|------|----------------------|-------------------------------|----------------------|------|
|                  |                           |                     |                                                                                     | Min                   | Typ  | Max                  | Min                           | Max                  |      |
| V <sub>IH</sub>  | HIGH Level Input Voltage  | 1.65 to 1.95        |                                                                                     | 0.65 V <sub>CC</sub>  | –    | –                    | 0.65 V <sub>CC</sub>          | –                    | V    |
|                  |                           | 2.30 to 5.50        |                                                                                     | 0.70 V <sub>CC</sub>  | –    | –                    | 0.70 V <sub>CC</sub>          | –                    |      |
| V <sub>IL</sub>  | LOW Level Input Voltage   | 1.65 to 1.95        |                                                                                     | –                     | –    | 0.35 V <sub>CC</sub> | –                             | 0.35 V <sub>CC</sub> | V    |
|                  |                           | 2.30 to 5.50        |                                                                                     | –                     | –    | 0.30 V <sub>CC</sub> | –                             | 0.30 V <sub>CC</sub> |      |
| V <sub>OH</sub>  | HIGH Level Output Voltage | 1.65                | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> ,<br>I <sub>OH</sub> = -100 µA | 1.55                  | 1.65 | –                    | 1.55                          | –                    | V    |
|                  |                           | 2.30                |                                                                                     | 2.20                  | 2.30 | –                    | 2.20                          | –                    |      |
|                  |                           | 3.00                |                                                                                     | 2.90                  | 3.00 | –                    | 2.90                          | –                    |      |
|                  |                           | 4.50                |                                                                                     | 4.40                  | 4.50 | –                    | 4.40                          | –                    |      |
|                  |                           | 1.65                | I <sub>OH</sub> = -4 mA                                                             | 1.29                  | 1.52 | –                    | 1.29                          | –                    |      |
|                  |                           | 2.30                | I <sub>OH</sub> = -8 mA                                                             | 1.90                  | 2.15 | –                    | 1.90                          | –                    |      |
|                  |                           | 3.00                | I <sub>OH</sub> = -16 mA                                                            | 2.50                  | 2.80 | –                    | 2.40                          | –                    |      |
|                  |                           | 3.00                | I <sub>OH</sub> = -24 mA                                                            | 2.40                  | 2.68 | –                    | 2.30                          | –                    |      |
|                  |                           | 4.50                | I <sub>OH</sub> = -32 mA                                                            | 3.90                  | 4.20 | –                    | 3.80                          | –                    |      |
| V <sub>OL</sub>  | LOW Level Output Voltage  | 1.65                | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> ,<br>I <sub>OL</sub> = 100 µA  | –                     | 0.00 | 0.10                 | –                             | 0.10                 | V    |
|                  |                           | 2.30                |                                                                                     | –                     | 0.00 | 0.10                 | –                             | 0.10                 |      |
|                  |                           | 3.00                |                                                                                     | –                     | 0.00 | 0.10                 | –                             | 0.10                 |      |
|                  |                           | 4.50                |                                                                                     | –                     | 0.00 | 0.10                 | –                             | 0.10                 |      |
|                  |                           | 1.65                | I <sub>OL</sub> = 4 mA                                                              | –                     | 0.80 | 0.24                 | –                             | 0.24                 |      |
|                  |                           | 2.30                | I <sub>OL</sub> = 8 mA                                                              | –                     | 0.10 | 0.30                 | –                             | 0.30                 |      |
|                  |                           | 3.00                | I <sub>OL</sub> = 16 mA                                                             | –                     | 0.15 | 0.40                 | –                             | 0.40                 |      |
|                  |                           | 3.00                | I <sub>OL</sub> = 24 mA                                                             | –                     | 0.22 | 0.55                 | –                             | 0.55                 |      |
|                  |                           | 4.50                | I <sub>OL</sub> = 32 mA                                                             | –                     | 0.22 | 0.55                 | –                             | 0.55                 |      |
| I <sub>IN</sub>  | Input Leakage Current     | 1.65 to 5.5         | V <sub>IN</sub> = 5.5 V, GND                                                        | –                     | –    | ±1                   | –                             | ±10                  | µA   |
| I <sub>OFF</sub> | Power Off Leakage Current | 0                   | V <sub>IN</sub> or V <sub>OUT</sub> = 5.5 V                                         | –                     | –    | 1                    | –                             | 10                   | µA   |
| I <sub>CC</sub>  | Quiescent Supply Current  | 1.65 to 5.50        | V <sub>IN</sub> = 5.5 V, GND                                                        | –                     | –    | 2                    | –                             | 20                   | µA   |

## AC ELECTRICAL CHARACTERISTICS

| Symbol                              | Parameter                                            | V <sub>CC</sub> (V) | Conditions                                        | T <sub>A</sub> = 25°C |      |      | T <sub>A</sub> = -40 to +85°C |      | Unit |
|-------------------------------------|------------------------------------------------------|---------------------|---------------------------------------------------|-----------------------|------|------|-------------------------------|------|------|
|                                     |                                                      |                     |                                                   | Min                   | Typ  | Max  | Min                           | Max  |      |
| t <sub>PLH</sub> , t <sub>PHL</sub> | Propagation Delay<br>(Figure 5, 6)                   | 1.80 ±0.15          | C <sub>L</sub> = 15 pF,<br>R <sub>L</sub> = 1 MΩ  | –                     | 14.0 | 22.5 | –                             | 23.0 | ns   |
|                                     |                                                      | 2.50 ±0.20          |                                                   | –                     | 8.0  | 12.5 | –                             | 13.0 |      |
|                                     |                                                      | 3.30 ±0.30          |                                                   | –                     | 6.0  | 9.2  | –                             | 9.5  |      |
|                                     |                                                      | 5.00 ±0.50          |                                                   | –                     | 4.3  | 5.7  | –                             | 6.1  |      |
|                                     |                                                      | 3.30 ±0.30          | C <sub>L</sub> = 50 pF,<br>R <sub>L</sub> = 500 Ω | –                     | 6.1  | 9.5  | –                             | 9.8  |      |
|                                     |                                                      | 5.00 ±0.50          |                                                   | –                     | 4.8  | 6.5  | –                             | 6.9  |      |
| C <sub>IN</sub>                     | Input Capacitance                                    | 0.00                |                                                   | –                     | 4    | –    | –                             | –    | pF   |
| C <sub>PD</sub>                     | Power Dissipation Capacitance<br>(Note 2) (Figure 7) | 3.30                |                                                   | –                     | 25   | –    | –                             | –    | pF   |
|                                     |                                                      | 5.00                |                                                   | –                     | 31   | –    | –                             | –    |      |

2. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I<sub>CCD</sub>) at no output loading and operating at 50% duty cycle. (See Figure 7) C<sub>PD</sub> is related to I<sub>CCD</sub> dynamic operating current by the expression:  
I<sub>CCD</sub> = (C<sub>PD</sub>) (V<sub>CC</sub>) (f<sub>IN</sub>) + (I<sub>CC</sub>static).

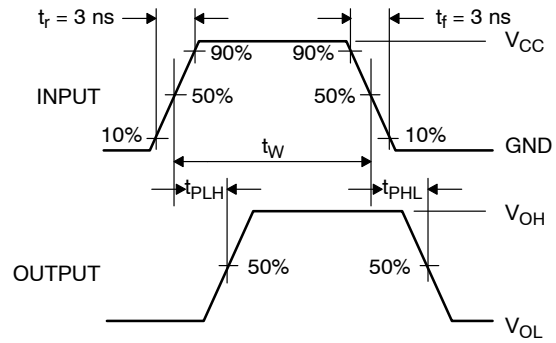
## AC Loading and Waveforms



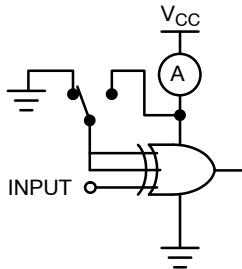
**NOTES:**

3.  $C_L$  includes load and stray capacitance
4. Input PRR = 1.0 MHz,  $t_W$  = 500 ns.

**Figure 5. AC Test Circuit**



**Figure 6. AC Waveforms**



**NOTE:**

5. Input = AC Waveform;  $t_r = t_f = 1.8$  ns; PRR = 10 MHz; Duty Cycle = 50%.

**Figure 7.  $I_{CCD}$  Test Circuit**

## DEVICE ORDERING INFORMATION

| Device      | Top Mark | Packages                              | Shipping <sup>†</sup> |
|-------------|----------|---------------------------------------|-----------------------|
| NC7SZ386P6X | 386      | 6-Lead SC70, EIAJ SC-88, 1.25 mm Wide | 3000 / Tape & Reel    |

## DISCONTINUED (Note 6)

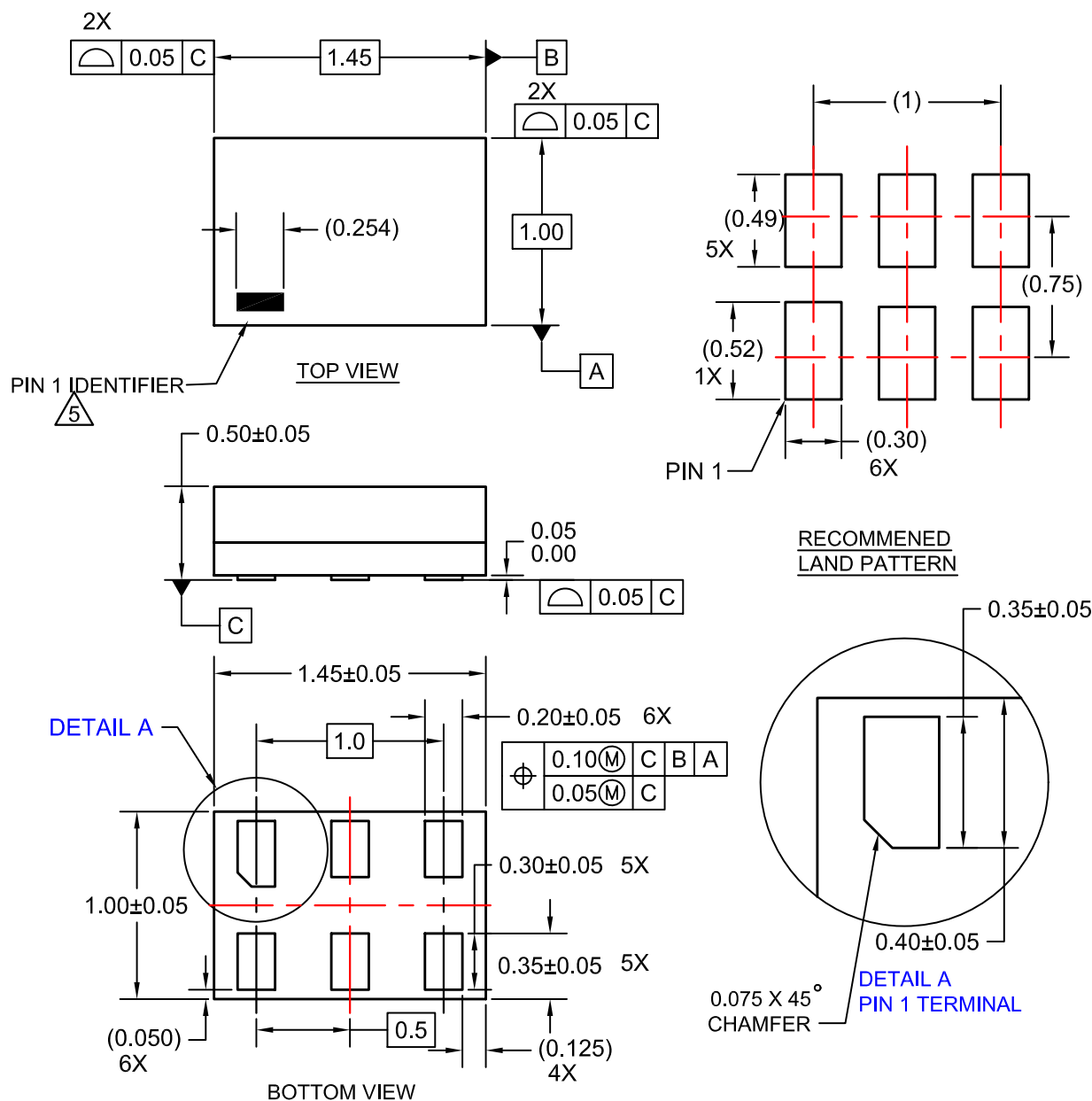
|                    |     |                                       |                    |
|--------------------|-----|---------------------------------------|--------------------|
| NC7SZ386P6X-L22347 | 386 | 6-Lead SC70, EIAJ SC-88, 1.25 mm Wide | 3000 / Tape & Reel |
| NC7SZ386L6X        | F4  | 6-Lead MicroPak, 1.00 mm Wide         | 5000 / Tape & Reel |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

6. **DISCONTINUED:** These devices are not recommended for new design. Please contact your **onsemi** representative for information. The most current information on these devices may be available on [www.onsemi.com](http://www.onsemi.com).

**SIP6 1.45X1.0**  
**CASE 127EB**  
**ISSUE O**

DATE 31 AUG 2016



NOTES:

1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
2. DIMENSIONS ARE IN MILLIMETERS
3. DRAWING CONFORMS TO ASME Y14.5M-2009
4. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY OTHER LINE IN THE MARK CODE LAYOUT.

|                         |                      |                                                                                                                                                                                     |
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| <b>DESCRIPTION:</b>     | <b>SIP6 1.45X1.0</b> | <b>PAGE 1 OF 1</b>                                                                                                                                                                  |

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SC-88 2.00x1.25x0.90, 0.65P  
CASE 419B-02  
ISSUE Z

DATE 18 APR 2024

NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
2. ALL DIMENSION ARE IN MILLIMETERS.
3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
4. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H.
5. DATUMS A AND B ARE DETERMINED AT DATUM H.
6. DIMENSIONS b AND c APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
7. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION b AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.



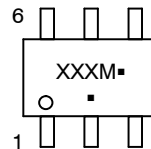
| DIM | MILLIMETERS |      |      |
|-----|-------------|------|------|
|     | MIN.        | NOM. | MAX. |
| A   | ---         | ---  | 1.10 |
| A1  | 0.00        | ---  | 0.10 |
| A2  | 0.70        | 0.90 | 1.00 |
| b   | 0.15        | 0.20 | 0.25 |
| c   | 0.08        | 0.15 | 0.22 |
| D   | 2.00 BSC    |      |      |
| E   | 2.10 BSC    |      |      |
| E1  | 1.25 BSC    |      |      |
| e   | 0.65 BSC    |      |      |
| L   | 0.26        | 0.36 | 0.46 |
| L2  | 0.15 BSC    |      |      |
| aaa | 0.15        |      |      |
| bbb | 0.30        |      |      |
| ccc | 0.10        |      |      |
| ddd | 0.10        |      |      |



RECOMMENDED MOUNTING FOOTPRINT\*

\* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ONSEMI SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

GENERIC  
MARKING DIAGRAM\*



XXX = Specific Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

STYLES ON PAGE 2

|                  |                             |                                                                                                                                                                                  |
|------------------|-----------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
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| DESCRIPTION:     | SC-88 2.00x1.25x0.90, 0.65P | PAGE 1 OF 2                                                                                                                                                                      |

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SC-88 2.00x1.25x0.90, 0.65P  
CASE 419B-02  
ISSUE Z

DATE 18 APR 2024

|                                                                                                            |                                                                                                        |                                                                                                             |                                                                                                             |                                                                                                             |                                                                                                       |
|------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------|
| STYLE 1:<br>PIN 1. EMITTER 2<br>2. BASE 2<br>3. COLLECTOR 1<br>4. EMITTER 1<br>5. BASE 1<br>6. COLLECTOR 2 | STYLE 2:<br>CANCELLED                                                                                  | STYLE 3:<br>CANCELLED                                                                                       | STYLE 4:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. ANODE               | STYLE 5:<br>PIN 1. ANODE<br>2. ANODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. CATHODE                 | STYLE 6:<br>PIN 1. ANODE 2<br>2. N/C<br>3. CATHODE 1<br>4. ANODE 1<br>5. N/C<br>6. CATHODE 2          |
| STYLE 7:<br>PIN 1. SOURCE 2<br>2. DRAIN 2<br>3. GATE 1<br>4. SOURCE 1<br>5. DRAIN 1<br>6. GATE 2           | STYLE 8:<br>CANCELLED                                                                                  | STYLE 9:<br>PIN 1. EMITTER 2<br>2. EMITTER 1<br>3. COLLECTOR 1<br>4. BASE 1<br>5. BASE 2<br>6. COLLECTOR 2  | STYLE 10:<br>PIN 1. SOURCE 2<br>2. SOURCE 1<br>3. GATE 1<br>4. DRAIN 1<br>5. DRAIN 2<br>6. GATE 2           | STYLE 11:<br>PIN 1. CATHODE 2<br>2. CATHODE 2<br>3. ANODE 1<br>4. CATHODE 1<br>5. CATHODE 1<br>6. ANODE 2   | STYLE 12:<br>PIN 1. ANODE 2<br>2. ANODE 2<br>3. CATHODE 1<br>4. ANODE 1<br>5. ANODE 1<br>6. CATHODE 2 |
| STYLE 13:<br>PIN 1. ANODE<br>2. N/C<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. CATHODE                 | STYLE 14:<br>PIN 1. VREF<br>2. GND<br>3. GND<br>4. IOUT<br>5. VEN<br>6. VCC                            | STYLE 15:<br>PIN 1. ANODE 1<br>2. ANODE 2<br>3. ANODE 3<br>4. CATHODE 3<br>5. CATHODE 2<br>6. CATHODE 1     | STYLE 16:<br>PIN 1. BASE 1<br>2. EMITTER 2<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER 1<br>6. COLLECTOR 1 | STYLE 17:<br>PIN 1. BASE 1<br>2. EMITTER 1<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER 2<br>6. COLLECTOR 1 | STYLE 18:<br>PIN 1. VIN1<br>2. VCC<br>3. VOUT2<br>4. VIN2<br>5. GND<br>6. VOUT1                       |
| STYLE 19:<br>PIN 1. I OUT<br>2. GND<br>3. GND<br>4. V CC<br>5. V EN<br>6. V REF                            | STYLE 20:<br>PIN 1. COLLECTOR<br>2. COLLECTOR<br>3. BASE<br>4. EMITTER<br>5. COLLECTOR<br>6. COLLECTOR | STYLE 21:<br>PIN 1. ANODE 1<br>2. N/C<br>3. ANODE 2<br>4. CATHODE 2<br>5. N/C<br>6. CATHODE 1               | STYLE 22:<br>PIN 1. D1 (i)<br>2. GND<br>3. D2 (i)<br>4. D2 (c)<br>5. VBUS<br>6. D1 (c)                      | STYLE 23:<br>PIN 1. Vn<br>2. CH1<br>3. Vp<br>4. N/C<br>5. CH2<br>6. N/C                                     | STYLE 24:<br>PIN 1. CATHODE<br>2. ANODE<br>3. CATHODE<br>4. CATHODE<br>5. CATHODE<br>6. CATHODE       |
| STYLE 25:<br>PIN 1. BASE 1<br>2. CATHODE<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER<br>6. COLLECTOR 1    | STYLE 26:<br>PIN 1. SOURCE 1<br>2. GATE 1<br>3. DRAIN 2<br>4. SOURCE 2<br>5. GATE 2<br>6. DRAIN 1      | STYLE 27:<br>PIN 1. BASE 2<br>2. BASE 1<br>3. COLLECTOR 1<br>4. EMITTER 1<br>5. EMITTER 2<br>6. COLLECTOR 2 | STYLE 28:<br>PIN 1. DRAIN<br>2. DRAIN<br>3. GATE<br>4. SOURCE<br>5. DRAIN<br>6. DRAIN                       | STYLE 29:<br>PIN 1. ANODE<br>2. ANODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE/ANODE<br>6. CATHODE          | STYLE 30:<br>PIN 1. SOURCE 1<br>2. DRAIN 2<br>3. DRAIN 2<br>4. SOURCE 2<br>5. GATE 1<br>6. DRAIN 1    |

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

|                  |                             |                                                                                                                                                                                  |
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