

# Silicon Carbide (SiC) MOSFET - EliteSiC, 12 mohm, 650 V, M2, D2PAK-7L

# NTBG015N065SC1

#### **Features**

- Typ.  $R_{DS(on)} = 12 \text{ m}\Omega$  @  $V_{GS} = 18 \text{ V}$ Typ.  $R_{DS(on)} = 15 \text{ m}\Omega$  @  $V_{GS} = 15 \text{ V}$
- Ultra Low Gate Charge (Q<sub>G(tot)</sub> = 283 nC)
- Low Effective Output Capacitance (Coss = 424 pF)
- 100% Avalanche Tested
- $T_I = 175^{\circ}C$
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb–Free 2LI (on second level interconnection)

## **Typical Applications**

- SMPS (Switching Mode Power Supplies)
- Solar Inverters
- UPS (Uninterruptable Power Supplies)
- Energy Storages

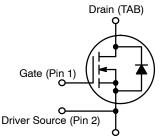
# **MAXIMUM RATINGS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			$V_{DSS}$	650	V
Gate-to-Source Voltage	ge		$V_{GS}$	-8/+22	V
Recommended Operation Values of Gate – Source Voltage		$V_{GSop}$	-5/+18	>	
Continuous Drain Current $R_{\theta JC}$ (Note 2)	Steady State	T <sub>C</sub> = 25°C	I <sub>D</sub>	145	Α
Power Dissipation $R_{\theta JC}$ (Note 2)			P <sub>D</sub>	500	W
Continuous Drain Current R <sub>0JA</sub> (Notes 1, 2)	Steady State	T <sub>C</sub> = 100°C	I <sub>D</sub>	103	Α
Power Dissipation $R_{\theta JA}$ (Notes 1, 2)			P <sub>D</sub>	250	W
Pulsed Drain Current (Note 3) T <sub>C</sub> = 25°C			I <sub>DM</sub>	422	Α
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	ç
Source Current (Body Diode)			I <sub>S</sub>	111	Α
Single Pulse Drain-to-Source Avalanche Energy (I <sub>L</sub> = 13 A <sub>pk</sub> , L = 1 mH) (Note 4)			E <sub>AS</sub>	84	mJ
Maximum Lead Temperature for Soldering, 1/8" from Case for 10 Seconds			TL	245	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Surface mounted on a FR-4 board using1 in2 pad of 2 oz copper.
- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 3. Repetitive rating, limited by max junction temperature.
- 4.  $E_{AS}$  of 84 mJ is based on starting  $T_J$  = 25°C; L = 1 mH,  $I_{AS}$  = 13 A,  $V_{DD}$  = 50 V,  $V_{GS}$  = 18 V.

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
650 V	18 mΩ @ 18 V	145 A



Power Source (Pins 3, 4, 5, 6, 7)

#### **N-CHANNEL MOSFET**



D2PAK-7L CASE 418BJ

#### MARKING DIAGRAM

BG015N 065SC1 AYWWZZ

BG015N065SC1 = Specific Device Code

A = Assembly Location

Y = Year WW = Work Week ZZ = Lot Traceability

# **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>	
NTBG015N065SC1	D2PAK-7L	800 / Tape & Reel	

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# THERMAL CHARACTERISTICS

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-to-Case (Note 2)	$R_{ heta JC}$	0.3	°C/W
Thermal Resistance Junction-to-Ambient (Notes 1, 2)	$R_{ hetaJA}$	40	°C/W

# **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise stated)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS				•		•	•
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA		650			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>	I <sub>D</sub> = 20 mA, refer to 25°C			0.12		V/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	I <sub>DSS</sub> V <sub>GS</sub> = 0 V, T <sub>J</sub> = 25°C				10	μΑ
		V <sub>DS</sub> = 650 V	T <sub>J</sub> = 175°C			1	mA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = +22/-8 V, V <sub>DS</sub> = 0 V				250	nA
ON CHARACTERISTICS	•				-	•	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}$ , $I_D =$	25 mA	1.8	2.8	4.3	V
Recommended Gate Voltage	$V_{GOP}$			-5		+18	V
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 15 V, I <sub>D</sub> =	75 A, T <sub>J</sub> = 25°C		15		mΩ
		V <sub>GS</sub> = 18 V, I <sub>D</sub> =	75 A, T <sub>J</sub> = 25°C		12	18	1
		V <sub>GS</sub> = 18 V, I <sub>D</sub> =	75 A, T <sub>J</sub> = 175°C		16		
Forward Transconductance	9FS	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 75 A			42		S
CHARGES, CAPACITANCES & GATE RES	ISTANCE				-		
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> = 0 V, f = 1 MHz, V <sub>DS</sub> = 325 V			4689		pF
Output Capacitance	C <sub>OSS</sub>				424		
Reverse Transfer Capacitance	C <sub>RSS</sub>				37		1
Total Gate Charge	Q <sub>G(TOT)</sub>	$V_{GS} = -5/18 \text{ V}, V_{DS} = 520 \text{ V},$ $I_{D} = 75 \text{ A}$ $f = 1 \text{ MHz}$			283		nC
Gate-to-Source Charge	$Q_{GS}$				72		1
Gate-to-Drain Charge	$Q_{GD}$				64		1
Gate-Resistance	$R_{G}$				1.6		Ω
SWITCHING CHARACTERISTICS	1				1		
Turn-On Delay Time	t <sub>d(ON)</sub>	$V_{GS} = -5/18 \text{ V}, \text{ V}$			23		ns
Rise Time	t <sub>r</sub>	$I_D = 75 A, R_G = 3$ Inductive Load	2.2 Ω,		26		
Turn-Off Delay Time	t <sub>d(OFF)</sub>				49		1
Fall Time	t <sub>f</sub>				9.6		
Turn-On Switching Loss	E <sub>ON</sub>				167		μJ
Turn-Off Switching Loss	E <sub>OFF</sub>				276		1
Total Switching Loss	E <sub>TOT</sub>				443		
DRAIN-SOURCE DIODE CHARACTERIST	ics	•					
Continuous Drain-Source Diode Forward Current	I <sub>SD</sub>	$V_{GS} = -5 \text{ V}, T_{J} =$	: 25°C			111	Α
Pulsed Drain-Source Diode Forward Current (Note 3)	I <sub>SDM</sub>	V <sub>GS</sub> = -5 V, T <sub>J</sub> = 25°C				422	Α
Forward Diode Voltage	$V_{SD}$	V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 75 A, T <sub>J</sub> = 25°C			4.8		V

# **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise stated)(continued)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit		
DRAIN-SOURCE DIODE CHARACTERISTICS								
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = -5/18 \text{ V, } I_{SD} = 75 \text{ A,}$ $dI_{S}/dt = 1000 \text{ A/}\mu\text{s}$		28		ns		
Reverse Recovery Charge	Q <sub>RR</sub>			234		nC		
Reverse Recovery Energy	E <sub>REC</sub>			23		μJ		
Peak Reverse Recovery Current	I <sub>RRM</sub>			16		Α		
Charge Time	Ta			17		ns		
Discharge Time	Tb			11		ns		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### **TYPICAL CHARACTERISTICS**

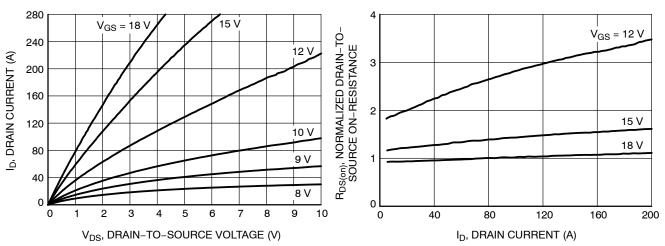


Figure 1. On-Region Characteristics

Figure 2. Normalized On–Resistance vs. Drain Current and Gate Voltage

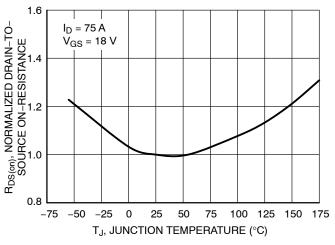


Figure 3. On–Resistance Variation with Temperature

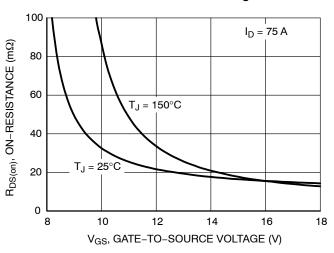


Figure 4. On-Resistance vs. Gate-to-Source Voltage

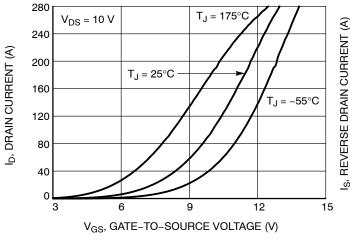


Figure 5. Transfer Characteristics

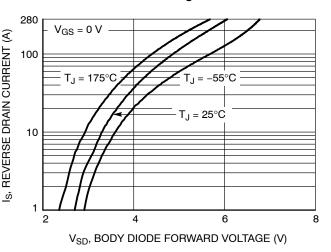


Figure 6. Diode Forward Voltage vs. Current

## TYPICAL CHARACTERISTICS (continued)

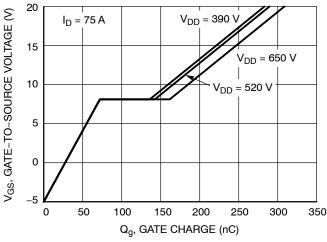


Figure 7. Gate-to-Source Voltage vs. Total Charge

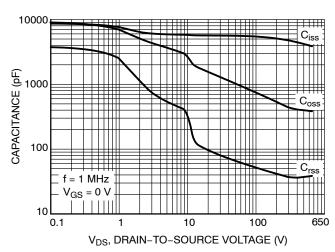


Figure 8. Capacitance vs. Drain-to-Source Voltage

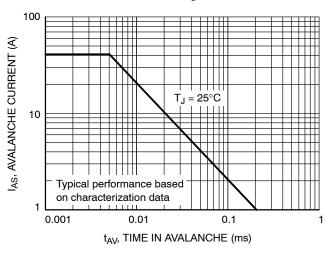


Figure 9. Unclamped Inductive Switching Capability

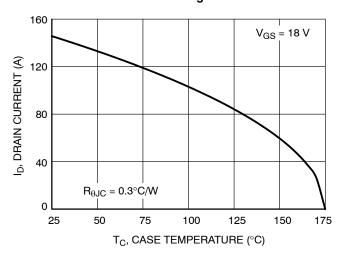
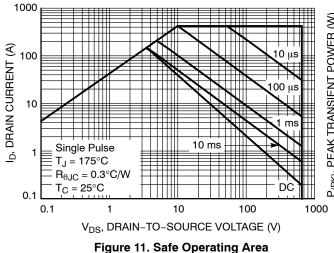


Figure 10. Maximum Continuous Drain **Current vs. Case Temperature** 



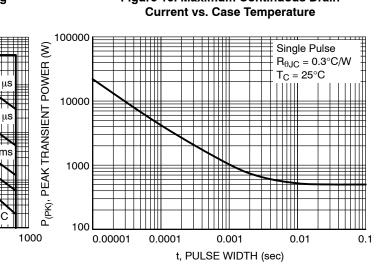


Figure 12. Single Pulse Maximum Power Dissipation

# TYPICAL CHARACTERISTICS (continued)

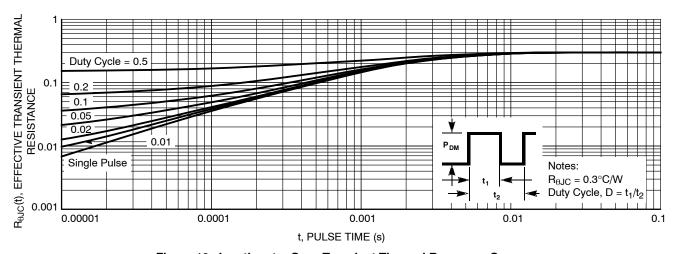
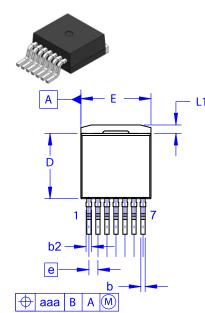


Figure 13. Junction-to-Case Transient Thermal Response Curve



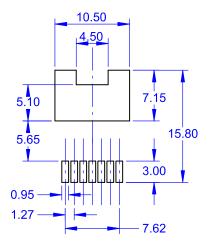


E1

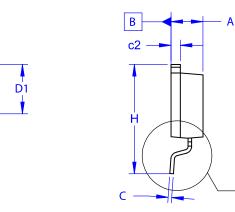
8

3.20 MIN

# D<sup>2</sup>PAK7 (TO-263-7L HV) CASE 418BJ ISSUE B



LAND PATTERN RECOMMENDATION



#### **DATE 16 AUG 2019**

#### NOTES:

A. PACKAGE CONFORMS TO JEDEC TO-263 VARIATION CB EXCEPT WHERE NOTED. B. ALL DIMENSIONS ARE IN MILLIMETERS.

OUT OF JEDEC STANDARD VALUE.
D. DIMENSION AND TOLERANCE AS PER ASME
Y14.5-2009.

E. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.

54	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	4.30	4.50	4.70		
A1	0.00	0.10	0.20		
b2	0.60	0.70	0.80		
b	0.51	0.60	0.70		
С	0.40	0.50	0.60		
c2	1.20	1.30	1.40		
D	9.00	9.20	9.40		
D1	6.15	6.80	7.15		
Е	9.70	9.90	10.20		
E1	7.15	7.65	8.15		
е	~	1.27	~		
Н	15.10	15.40	15.70		
L	2.44	2.64	2.84		
L1	1.00	1.20	1.40		
L3	~	0.25	~		
aaa	~	~	0.25		

# GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code

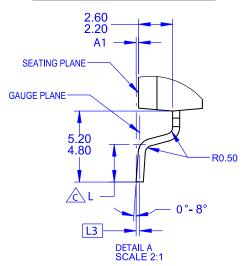
A = Assembly Location

Y = Year

WW = Work Week

G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



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DESCRIPTION:	D <sup>2</sup> PAK7 (TO-263-7L HV)		PAGE 1 OF 1	

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