onsemi

<u>Silicon Carbide (SiC)</u> <u>MOSFET</u> – EliteSiC, 80 mohm, 1200 V, M1, TO-247-4L

NTH4L080N120SC1

Description

Silicon Carbide (SiC) MOSFET uses a completely new technology that provide superior switching performance and higher reliability compared to Silicon. In addition, the low ON resistance and compact chip size ensure low capacitance and gate charge. Consequently, system benefits include highest efficiency, faster operation frequency, increased power density, reduced EMI, and reduced system size.

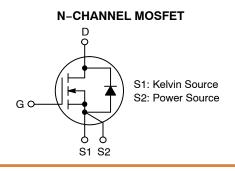
Features

- 1200 V @ T_J = 175°C
- Max $R_{DS(on)} = 110 \text{ m}\Omega$ at $V_{GS} = 20 \text{ V}$, $I_D = 20 \text{ A}$
- High Speed Switching with Low Capacitance
- 100% Avalanche Tested
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb–Free 2LI (on second level interconnection)

Applications

- Industrial Motor Drive
- UPS
- Boost Inverter
- PV Charger

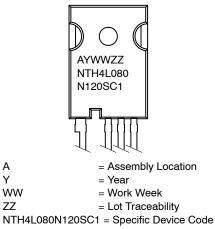
V _{DSS}	R _{DS(ON)} TYP	I _D MAX
1200 V	80 mΩ	29 A





CASE 340CJ

MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping
NTH4L080N120SC1	TO-247-4LD	30 Units / Tube

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise noted)

Symbol	Parameter		Ratings	Unit	
V _{DSmax}	Drain-to-Source Voltage		1200	V	
V _{GSmax}	Max. Gate-to-Source Voltage	@ T _C < 150°C	-15 / +25	V	
V _{GSop} (DC)	Recommended operation Values of Gate – Source Voltage	@ T _C < 150°C	-5 / +20	V	
V _{GSop} (AC)	Recommended operation Values of Gate – Source Voltage (f > 1 Hz)			V	
ID	Continuous Drain Current	V_{GS} = 20 V, T_C = 25°C	29	А	
		V_{GS} = 20 V, T_{C} = 100°C	21		
I _{D(Pulse)}	Pulse Drain Current	Pulse width tp limited by Tj max	125	A	
E _{AS}	Single Pulse Avalanche Energy (Note 1)		171	mJ	
P _{tot}	Power Dissipation	$T_{\rm C} = 25^{\circ}{\rm C}$	170	W	
		T _C = 150°C	28	1	
T _J , T _{STG}	Operating and Storage Junction Temperature	Range	–55 to +175	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. E_{AS} of 171 mJ is based on starting Tj = 25°C, L = 1 mH, I_{AS} = 18.5 A, , V_{DD} = 50 V, R_G = 25 Ω .

THERMAL CHARACTERISTICS

Symbol	Parameter	Ratings	Unit	
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction-to-Case	0.88	°C/W	
$R_{ ext{ heta}JA}$	JA Thermal Resistance, Junction-to-Ambient 40			

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
BV _{DSS}	Drain-to-Source Breakdown Voltage	I_D = 100 μ A, V_{GS} = 0 V	1200	-	-	V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	$I_D = 5$ mA, Referenced to $25^{\circ}C$	-	0.3	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 1200 V, V_{GS} = 0 V T _C = 25°C T _C = 150°C	-		100 1.0	μA mA
I _{GSS}	Gate-to-Source Leakage Current	$V_{GS} = 25 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	1	μΑ
I _{GSSR}	Gate-to-Source Leakage Current, Reverse	V_{GS} = -15 V, V_{DS} = 0 V	-	-	-1	μΑ

ON CHARACTERISTICS

V _{GS(th)}	Gate-to-Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 5 \text{ mA}$	1.8	2.75	4.3	V
R _{DS(on)}	Static Drain-to-Source On Resistance	V_{GS} = 20 V, I _D = 20 A	-	80	110	mΩ
		V_{GS} = 20 V, I _D = 20 A, T _C = 150°C	-	127	162	
9 FS	Forward Transconductance	V _{DS} = 20 V, I _D = 20 A	-	11.3	-	S
		V_{DS} = 20 V, I _D = 20 A, T _C = 150°C	-	9.8	1	

DYNAMIC CHARACTERISTICS

ſ	C _{iss}	Input Capacitance	V_{DS} = 800 V, V_{GS} = 0 V, f = 1 MHz	-	1112	1670	pF
ſ	C _{oss}	Output Capacitance		-	80	120	pF
	C _{rss}	Reverse Transfer Capacitance		-	6.5	10	pF
ſ	E _{oss}	C _{oss} Stored Energy		-	32	-	μJ

SWITCHING CHARACTERISTICS

t _{d(on)}	Turn-On Delay Time	$V_{CC} = 800 \text{ V}, \text{ I}_{C} = 20 \text{ A},$	-	9	18	ns
t _r	Rise Time	$V_{GS} = -5/20 \text{ V}, \text{ R}_{G} = 4.7 \Omega$ Inductive Load, $T_{C} = 25^{\circ}C$	-	4.2	10	ns
t _{d(off)}	Turn-Off Delay Time		-	26.8	43	ns
t _f	Fall Time		-	5.4	11	ns
Eon	Turn-on Switching Loss		-	314	-	μJ
E _{off}	Turn-off Switching Loss		-	32	-	μJ
E _{ts}	Total Switching Loss		-	346	-	μJ
Qg	Total Gate Charge	$V_{DD} = 600 \text{ V}, I_D = 20 \text{ A}$	-	56	-	nC
Q _{gs}	Gate-to-Source Charge	V _{GS} = -5/20 V	-	11	-	nC
Q _{gd}	Gate-to-Drain Charge		-	12	-	nC
R _G	Gate input resistance	f = 1 MHz, D–S short	-	1.7	-	Ω

DIODE CHARACTERISTICS

V_{SD}	Source-to-Drain Diode Forward	V _{GS} = -5 V, I _{SD} = 10 A	$T_{C} = 25^{\circ}C$	_	3.7	_	V
	Voltage	$I_{SD} = 10 \text{ A}$	T _C = 150°C	-	3.3	-	
E _{rec}	Reverse Recovery Energy	$I_{SD} = 20 \text{ A},$	T _C = 150°C	-	29	-	μJ
t _{rr}	Diode Reverse Recovery Time	$V_{GS} = -5 V,$ $V_{R} = 600 V,$	$T_{C} = 25^{\circ}C$	-	18	-	ns
		dl _{SD} /dt = 1000 A/µs	T _C = 150°C	-	31	-	
Q _{rr}	Diode Reverse Recovery Charge		$T_{C} = 25^{\circ}C$	-	80	-	nC
			T _C = 150°C	-	212	-	
I _{rrm}	Peak Reverse Recovery Current		$T_{C} = 25^{\circ}C$	-	9	-	А
			T _C = 150°C	_	14	-	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS (T_J = 25°C UNLESS OTHERWISE NOTED)

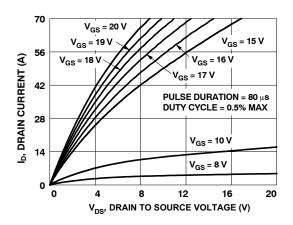


Figure 1. On Region Characteristics

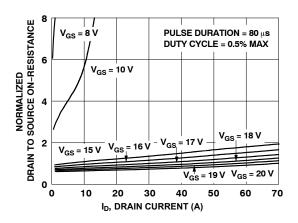


Figure 2. Normalized On–Resistance vs. Drain Current and Gate Voltage

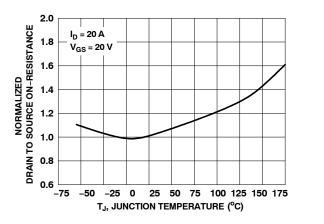


Figure 3. Normalized On Resistance vs. Junction Temperature

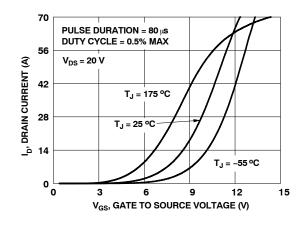


Figure 5. Transfer Characteristics

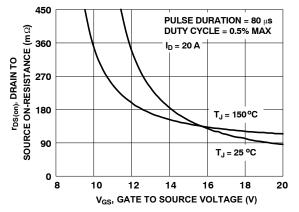
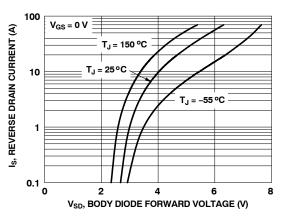
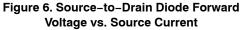


Figure 4. On-Resistance vs. Gate-to-Source Voltage





TYPICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ UNLESS OTHERWISE NOTED) (CONTINUED)

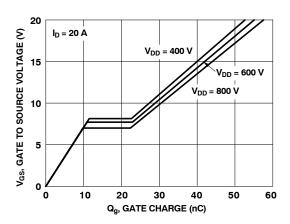


Figure 7. Gate Charge Characteristics

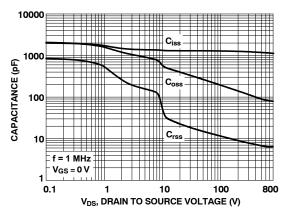
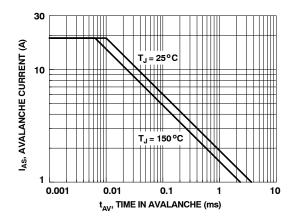
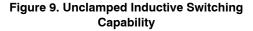


Figure 8. Capacitance vs. Drain-to-Source Voltage





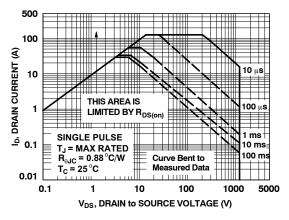


Figure 11. Forward Bias Safe Operating Area

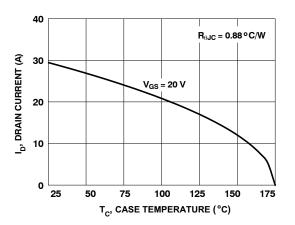
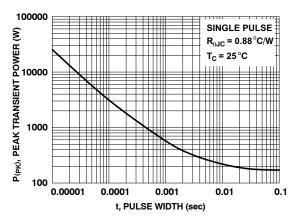


Figure 10. Maximum Continuous Drain Current vs. Case Temperature





TYPICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ UNLESS OTHERWISE NOTED) (CONTINUED)

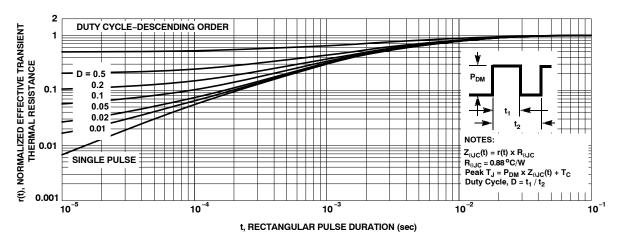


Figure 13. Junction-to-Case Transient Thermal Response Curve



TO-247-4LD CASE 340CJ **ISSUE A**

DATE 16 SEP 2019

NOM

5.00

2.40

2.00

1.20

1.40

2.22

0.60

22.54

16.25

1.17

2.54 BSC

5.08 BSC

15.60

13.00

5.00

18.42

2.62

3.60

6.80

6.17

6.17

3.40

6.60

5.97

5.97

р p1

Q

S

MAX

5.20

2.70

2.20

1.33

1.60

2.42

0.70

22.74

16.50

1.37

15.80

13.20

5.20

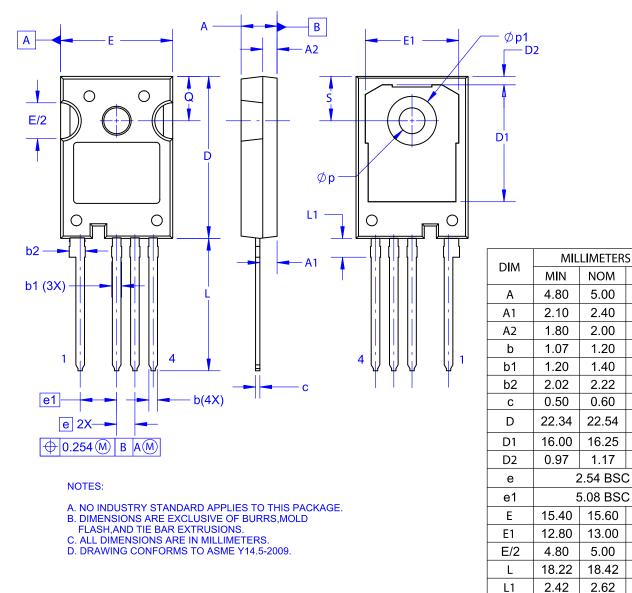
18.62

2.82

3.80

7.00 6.37

6.37



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