

Silicon Carbide (SiC) MOSFET – EliteSiC, 12 mohm, 650 V, M2, TO-247-3L

NTHL015N065SC1

Features

- Typ. $R_{DS(on)} = 12 \text{ m}\Omega$ @ $V_{GS} = 18 \text{ V}$ Typ. $R_{DS(on)} = 15 \text{ m}\Omega$ @ $V_{GS} = 15 \text{ V}$
- Ultra Low Gate Charge (Q_{G(tot)} = 283 nC)
- High Speed Switching with Low Capacitance (Coss = 430 pF)
- 100% Avalanche Tested
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb–Free 2LI (on second level interconnection)

Typical Applications

- SMPS (Switching Mode Power Supplies)
- Solar Inverters
- UPS (Uninterruptable Power Supplies)
- Energy Storage

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

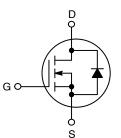
Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	650	V
Gate-to-Source Voltage	!		V_{GS}	-8/+22	V
Recommended Operatio of Gate-to-Source Volta		T _C < 175°C	V_{GSop}	-5/+18	٧
Continuous Drain Current (Note 1)	Steady State	T _C = 25°C	I _D	163	Α
Power Dissipation (Note 1)			P _D	643	W
Continuous Drain Current (Note 1)	Steady State	T _C = 100°C	I _D	115	Α
Power Dissipation (Note 1)			P _D	321	W
Pulsed Drain Current (Note 2)	T _C = 25°C		I _{DM}	484	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode)			IS	157	Α
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 13 A, L = 1 mH) (Note 3)			E _{AS}	84	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)			TL	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature.
- 3. E_{AS} of 84 mJ is based on starting T_J = 25°C; L = 1 mH, I_{AS} = 13 A, V_{DD} = 50 V, V_{GS} = 18 V.

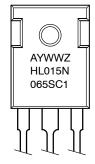
V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX	
650 V	18 mΩ @ 18 V	163 A	

N-CHANNEL MOSFET





MARKING DIAGRAM



A = Assembly Location
Y = Year
WW = Work Week
ZZ = Lot Traceability
HL015N065SC1 = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping
NTHL015N065SC1	TO-247-3LD	30 Units / Tube

Table 1. THERMAL CHARACTERISTICS

Parameter	Symbol	Max	Unit
Junction-to-Case - Steady State (Note 1)	$R_{ heta JC}$	0.24	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{ hetaJA}$	40	

Table 2. ELECTRICAL CHARACTERISTICS (T. J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condit	tion	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•						
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 1 mA		650	-	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 20 mA, referenced to 25°C		-	0.12	-	V/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V,	T _J = 25°C	-	-	10	μΑ
		V _{DS} = 650 V	T _J = 175°C	-	_	1	mA
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = +22/-8 \text{ V}, V_{DS}$	= 0 V	-	_	250	nA
ON CHARACTERISTICS (Note 2)					•		
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_{D} = 25 \text{ m}$	Α	1.8	2.63	4.3	V
Recommended Gate Voltage	V_{GOP}			-5	-	+18	V
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 15 V, I _D = 75 A	, T _J = 25°C	_	15	-	mΩ
		V _{GS} = 18 V, I _D = 75 A	, T _J = 25°C	_	12	18	
		V _{GS} = 18 V, I _D = 75 A	, T _J = 175°C	_	16	-	
Forward Transconductance	9 _{FS}	V _{DS} = 10 V, I _D = 75 A		-	44	-	S
CHARGES, CAPACITANCES & GATE RES	SISTANCE			ı	ı		
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 325 V		-	4790	-	pF
Output Capacitance	C _{OSS}			_	430	-	
Reverse Transfer Capacitance	C _{RSS}			_	33	-	
Total Gate Charge	Q _{G(TOT)}	V _{GS} = -5/18 V, V _{DS} = 520 V, I _D = 75 A		_	283	-	nC
Gate-to-Source Charge	Q _{GS}			_	72	-	
Gate-to-Drain Charge	Q_{GD}			_	64	-	
Gate-Resistance	R_{G}	f = 1 MHz		-	1.6	-	Ω
SWITCHING CHARACTERISTICS	•						
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -5/18 \text{ V}, V_{DS} =$	400 V,	-	25	-	ns
Rise Time	t _r	I_D = 75 A, R_G = 2.2 Ω Inductive load		-	77	-	
Turn-Off Delay Time	t _{d(OFF)}			-	47	-	
Fall Time	t _f			-	11	-	
Turn-On Switching Loss	E _{ON}			-	1371	-	μJ
Turn-Off Switching Loss	E _{OFF}			-	470	-	
Total Switching Loss	E _{tot}			-	1841	-	
SOURCE-DRAIN DIODE CHARACTERIST	rics	•					
Continuous Source-Drain Diode Forward Current	I _{SD}	$V_{GS} = -5 \text{ V}, T_{J} = 25^{\circ}\text{C}$	0	-	-	157	Α
Pulsed Source-Drain Diode Forward Current (Note 2)	I _{SDM}			-	-	484	
Forward Diode Voltage	V_{SD}	V _{GS} = -5 V, I _{SD} = 75	A, T _J = 25°C	_	4.6	_	V

 $\textbf{Table 2. ELECTRICAL CHARACTERISTICS} \ (T_J = 25^{\circ}C \ unless \ otherwise \ specified) \ (continued)$

	<u>=</u>					
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
SOURCE-DRAIN DIODE CHARACTERIST	rics					
Reverse Recovery Time	t _{RR}	V _{GS} = -5/18 V, I _{SD} = 75 A, dI _S /dt = 1000 A/μs	-	33	_	ns
Reverse Recovery Charge	Q_{RR}	di _S /αt = 1000 A/μs	-	261	_	nC
Reverse Recovery Energy	E _{REC}]	-	9.2	-	μJ
Peak Reverse Recovery Current	I _{RRM}		-	16	_	Α
Charge Time	Ta		_	19	_	ns
Discharge Time	Tb		_	15	-	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

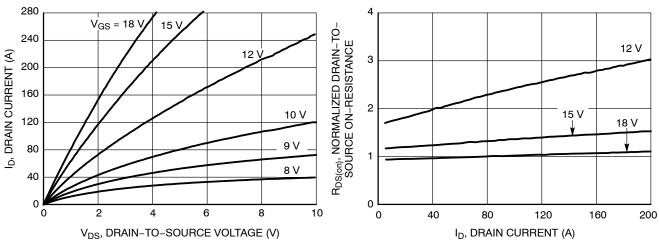


Figure 1. On-Region Characteristics

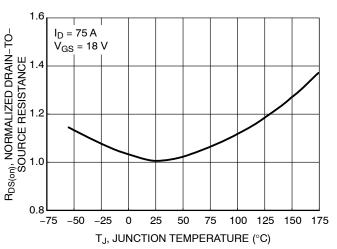


Figure 3. On–Resistance Variation with Temperature

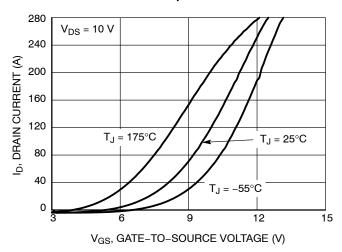


Figure 5. Transfer Characteristics

Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

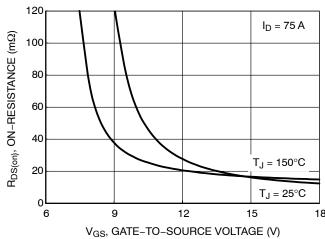
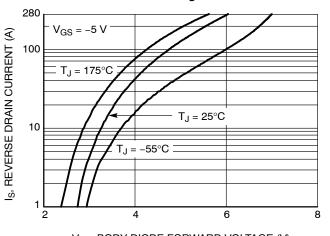


Figure 4. On-Resistance vs. Gate-to-Source Voltage



 V_{SD} , BODY DIODE FORWARD VOLTAGE (V)

Figure 6. Diode Forward Voltage vs. Current

TYPICAL CHARACTERISTICS (CONTINUED)

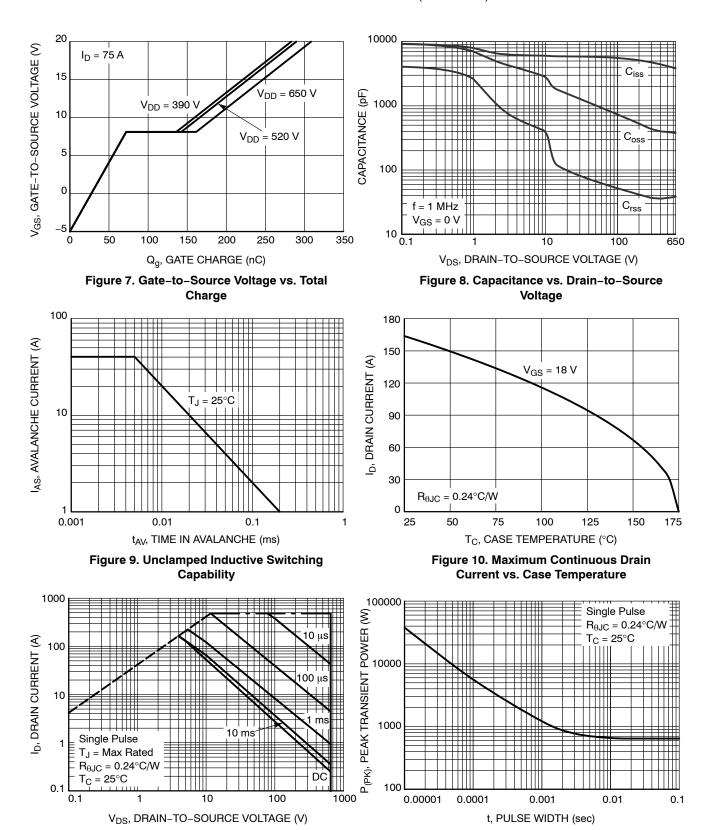


Figure 12. Single Pulse Maximum Power Dissipation

Figure 11. Safe Operating Area

TYPICAL CHARACTERISTICS (CONTINUED)

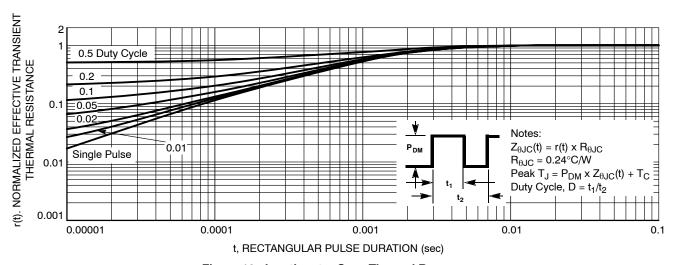
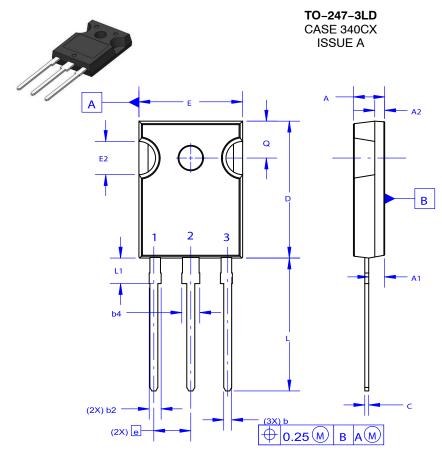


Figure 13. Junction-to-Case Thermal Response

DATE 06 JUL 2020





NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present. Some products may not follow the Generic Marking.

Ø _P —		Φ _{P1} D2
E1 —	2	D1

DIM	MIL	LIMETER	S
DIM	MIN	NOM	MAX
Α	4.58	4.70	4.82
A 1	2.20	2.40	2.60
A2	1.40	1.50	1.60
D	20.32	20.57	20.82
Е	15.37	15.62	15.87
E2	4.96	5.08	5.20
е	~	5.56	~
L	19.75	20.00	20.25
L1	3.69	3.81	3.93
ØΡ	3.51	3.58	3.65
Q	5.34	5.46	5.58
S	5.34	5.46	5.58
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
b4	2.42	2.54	2.66
С	0.51	0.61	0.71
D1	13.08	~	~
D2	0.51	0.93	1.35
E1	12.81	~	~
ØP1	6.60	6.80	7.00

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