

Silicon Carbide (SiC) MOSFET – EliteSiC, 57 mohm, 650 V, M2, TO-247-3L

NTHL075N065SC1

Features

- Typ. $R_{DS(on)} = 57 \text{ m}\Omega$ @ $V_{GS} = 18 \text{ V}$ Typ. $R_{DS(on)} = 75 \text{ m}\Omega$ @ $V_{GS} = 15 \text{ V}$
- Ultra Low Gate Charge $(Q_{G(tot)} = 61 \text{ nC})$
- Low Output Capacitance (Coss = 107 pF)
- 100% Avalanche Tested
- $T_I = 175^{\circ}C$
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb–Free 2LI (on second level interconnection)

Typical Applications

- SMPS (Switching Mode Power Supplies)
- Solar Inverters
- UPS (Uninterruptable Power Supplies)
- Energy Storages

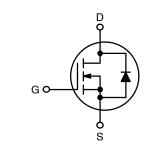
MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

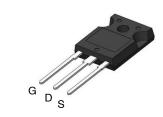
Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	650	V
Gate-to-Source Voltage	!		V_{GS}	-8/+22	٧
Recommended Operation Values of Gate-to-Source Voltage		V_{GSop}	-5/+18	>	
Continuous Drain Current (Note 1)	Steady State	T _C = 25°C	I _D	38	Α
Power Dissipation (Note 1)			P _D	148	W
Continuous Drain Current (Note 1)	Steady State	T _C = 100°C	I _D	26	Α
Power Dissipation (Note 1)			P _D	74	W
Pulsed Drain Current (Note 2)	T _C = 25°C		I _{DM}	120	Α
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C	
Source Current (Body Diode)			Is	38	Α
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 12.9 A, L = 1 mH) (Note 3)			E _{AS}	83	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)		TL	260	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature.
- 3. E_{AS} of 83 mJ is based on starting $T_J = 25$ °C; L = 1 mH, $I_{AS} = 12.9$ A, $V_{DD} = 50$ V, $V_{GS} = 18$ V.

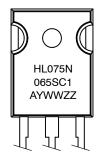
V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
650 V	85 mΩ @ 18 V	38 A





TO-247-3LD CASE 340CX

MARKING DIAGRAM



HL075N65SC1= Specific Device Code
A = Assembly Location
YWW = Data Code (Year & Week)
ZZ = Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
NTHL075N065SC1	TO-247-3LD	30 Units / Tube

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Case - Steady State (Note 1)	$R_{ heta JC}$	1.01	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	40	

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS		•					
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 1 mA		650	_	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 20 mA, referenced to 25°C		-	0.15	-	V/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V,	T _J = 25°C	-	_	10	μΑ
		V _{DS} = 650 V	T _J = 175°C	-	-	1	mA
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = +18/-5 \text{ V}, V_{DS}$	= 0 V	-	_	250	nA
ON CHARACTERISTICS (Note 2)	•					•	
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, $I_D = 5 \text{ mA}$		1.8	2.8	4.3	V
Recommended Gate Voltage	V_{GOP}			-5	-	+18	V
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 15 V, I _D = 15 A,	, T _J = 25°C	-	75	_	mΩ
		V _{GS} = 18 V, I _D = 15 A,	, T _J = 25°C	_	57	85	
		V _{GS} = 18 V, I _D = 15 A,	, T _J = 175°C	-	68	_	
Forward Transconductance	9FS	V _{DS} = 10 V, I _D = 15 A		-	9	_	S
CHARGES, CAPACITANCES & GATE RES	SISTANCE	•		•	•		
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 325 V		-	1196	_	pF
Output Capacitance	Coss			_	107	_	
Reverse Transfer Capacitance	C _{RSS}			_	9	-	
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -5/18 \text{ V}, V_{DS} = 520 \text{ V},$ $I_{D} = 15 \text{ A}$ $f = 1 \text{ MHz}$		-	61	-	nC
Gate-to-Source Charge	Q_{GS}			-	19	-	
Gate-to-Drain Charge	Q_{GD}			-	18	-	
Gate-Resistance	R_{G}			-	5.8	-	Ω
SWITCHING CHARACTERISTICS							
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -5/18 \text{ V},$		-	10	-	ns
Rise Time	t _r	V _{DS} = 400 V, I _D = 15 A,		-	12	-	
Turn-Off Delay Time	t _{d(OFF)}	$R_G = 2.2 \Omega$ inductive load		-	20	-	
Fall Time	t _f			-	7	-	
Turn-On Switching Loss	E _{ON}	-		-	38	-	μЈ
Turn-Off Switching Loss	E _{OFF}			_	16	-	
Total Switching Loss	E _{tot}			_	54	_	
DRAIN-SOURCE DIODE CHARACTERIST	rics						
Continuous Drain-Source Diode Forward Current	I _{SD}	$V_{GS} = -5 \text{ V}, T_{J} = 25^{\circ}\text{C}$;	_	-	38	Α
Pulsed Drain-Source Diode Forward Current (Note 2)	I _{SDM}	1		-	-	120	
Forward Diode Voltage	V_{SD}	$V_{GS} = -5 \text{ V}, I_{SD} = 15 \text{ A}$	A, T _J = 25°C	-	4.4	-	V

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified) (continued)

· · ·		1 / 1					
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
DRAIN-SOURCE DIODE CHARACTERISTICS							
Reverse Recovery Time	t _{RR}	$V_{GS} = -5/18 \text{ V}, I_{SD} = 15 \text{ A},$ $dI_S/dt = 1000 \text{ A}/\mu\text{s}$	-	18	-	ns	
Reverse Recovery Charge	Q _{RR}	di _S /dt = 1000 A/μs	-	85	-	nC	
Reverse Recovery Energy	E _{REC}		-	10.6	-	μJ	
Peak Reverse Recovery Current	I _{RRM}		-	10	-	Α	
Charge Time	Ta		-	10	-	ns	
Discharge Time	Tb		-	8	-	ns	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

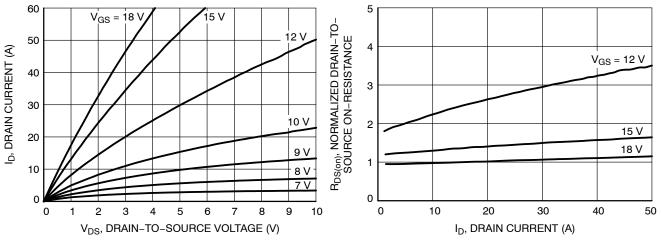


Figure 1. On-Region Characteristics

Figure 2. Normalized On-Resistance vs. Drain **Current and Gate Voltage**

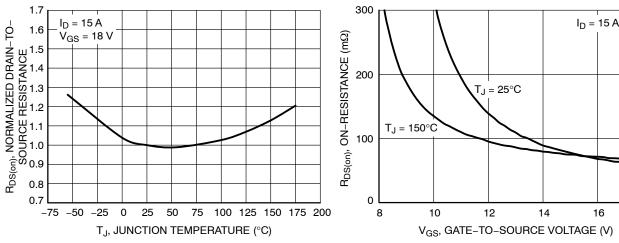
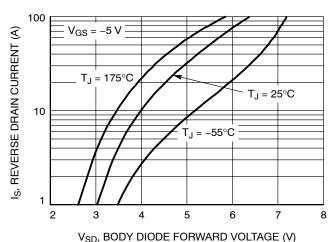


Figure 3. On-Resistance Variation with Temperature



 $V_{DS} = 10 V$ 70 ID, DRAIN CURRENT (A) 60 $T_J = 25^{\circ}C$ 50 40 $T_J = 175^{\circ}C$ 30 20 T_J = -55°C 10 6 10 12 14 16 18 V_{GS}, GATE-TO-SOURCE VOLTAGE (V)

80

Figure 5. Transfer Characteristics

Figure 4. On-Resistance vs. Gate-to-Source Voltage

16

18

Figure 6. Diode Forward Voltage vs. Current

TYPICAL CHARACTERISTICS (CONTINUED)

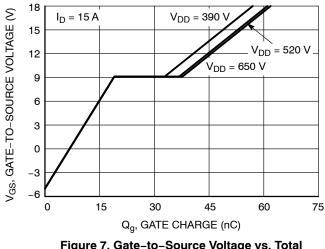


Figure 7. Gate-to-Source Voltage vs. Total Charge

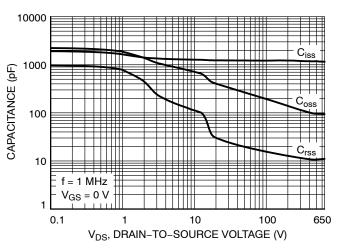


Figure 8. Capacitance vs. Drain-to-Source Voltage

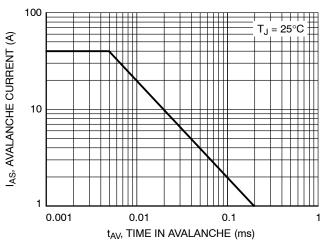


Figure 9. Unclamped Inductive Switching Capability

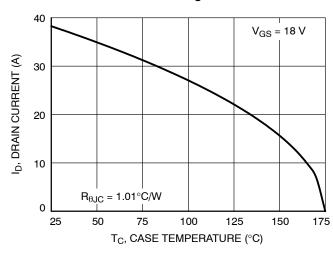


Figure 10. Maximum Continuous Drain **Current vs. Case Temperature**

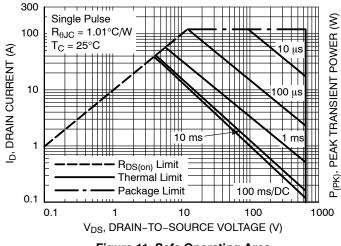


Figure 11. Safe Operating Area

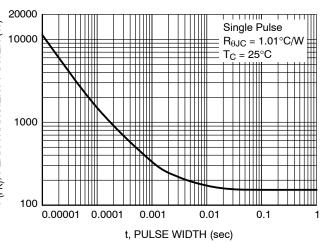


Figure 12. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS (CONTINUED)

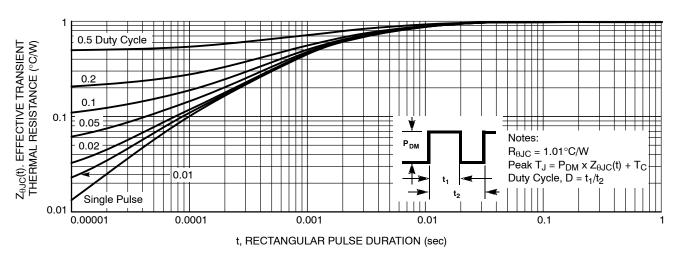
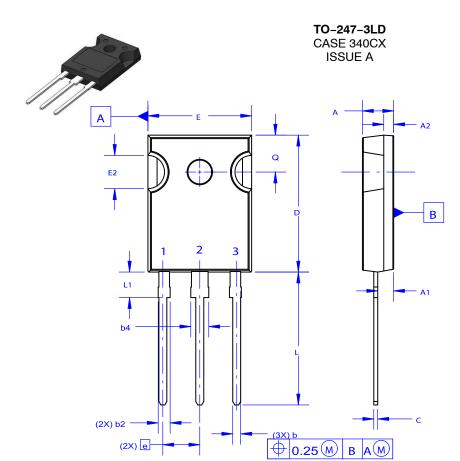
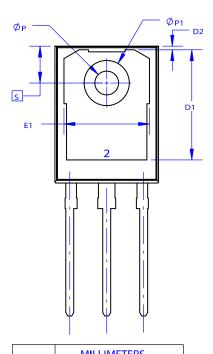


Figure 13. Junction-to-Case Thermal Response



DATE 06 JUL 2020

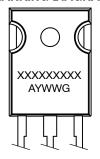


NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

 B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code = Assembly Location

= Year WW = Work Week G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " =", may or may not be present. Some products may not follow the Generic Marking.

DIM	MILLIMETERS					
DIM	MIN	NOM	MAX			
Α	4.58	4.70	4.82			
A 1	2.20	2.40	2.60			
A2	1.40	1.50	1.60			
D	20.32	20.57	20.82			
Е	15.37	15.62	15.87			
E2	4.96	5.08	5.20			
е	~	5.56	~			
L	19.75	20.00	20.25			
L1	3.69	3.81	3.93			
ØΡ	3.51	3.58	3.65			
Q	5.34	5.46	5.58			
S	5.34	5.46	5.58			
b	1.17	1.26	1.35			
b2	1.53	1.65	1.77			
b4	2.42	2.54	2.66			
С	0.51	0.61	0.71			
D1	13.08	~	~			
D2	0.51	0.93	1.35			
E1	12.81	~	~			
Ø P 1	6.60	6.80	7.00			

DOCUMENT NUMBER:	98AON93302G	Electronic versions are uncontrolled except when accessed directly from the Document Repos Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	TO-247-3LD		PAGE 1 OF 1	

ON Semiconductor and un are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales