## PCGA300T65DF8M1

# 650 V, 300 A Field Stop Trench IGBT with Solderable Top Metal



#### ON Semiconductor®

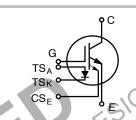
# www.onsemi.com

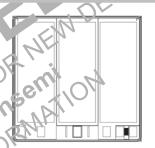
#### **Features**

- AEC-Q101 Qualified
- Maximum Junction Temperature 175°C
- Positive Temperature Coefficient
- Easy Paralleling
- Short Circuit Rated
- Very Low Saturation Voltage: VCE(SAT) = 1.5 V (Typ.) @ IC = 300 A
- Optimized For Motor Control Applications
- Integrated Temp Sensor And Current Sensor
- Emitter Pad Covered With Solderable Metal Layer

#### **Applications**

- Automotive Traction modules
- General Power Modules





### ORDERING INFORMATION

Part Number PCGA300T65DF8M1					
Packing	Water (sawn on foil)				
	mils	μm			
Die Size	472 × 472	12,000 × 12,000			
Emitter Attach Area	3 × (141 × 383)	3 × (3,580 × 9,720)			
Gate / Sensor Pad Attach Area	6 × (27 × 39)	6 × (680 × 980)			
Die Thickness	3	78			
Top Metal	5 um AlSiCu + 1.15 um Ti/NiV/Ag (STM)				
Back Metal	0.65 um NiV/Ag				
Topside Passivation	Silicon Nitride plus Polyimide				
Wafer Diameter	200 mm				
Max Possible Die Per Wafer	136				

1

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### **ABSOLUTE MAXIMUM RATINGS** ( $T_{VJ} = 25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Ratings	Units
Collector-Emitter Voltage	V <sub>CES</sub>	650	V
Gate-Emitter Voltage	V <sub>GES</sub>	±20	V
DC Collector Current, limited by T <sub>VJ</sub> max	lc	(Note 1)	А
Pulsed Collector Current, V <sub>GE</sub> =15 V, tp limited by T <sub>VJ</sub> max (Note 2)	Ісм	900	А
Short Circuit Withstand Time, $V_{GE}$ = 15 V, $V_{CE} \le 400$ V, $T_{VJ} \le 150$ °C	t <sub>sc</sub>	5	μs
Operating Junction Temperature	T <sub>VJ</sub>	-40 to +175	°C
Storage Temperature Range	Tstg	+17 to +25	°C

- Depends on the thermal properties of assembly
   Not subject to production test verified by design/characterization

### ELECTRICAL CHARACTERISTICS OF THE IGBT (T<sub>VJ</sub> = 25°C unless otherwise noted)

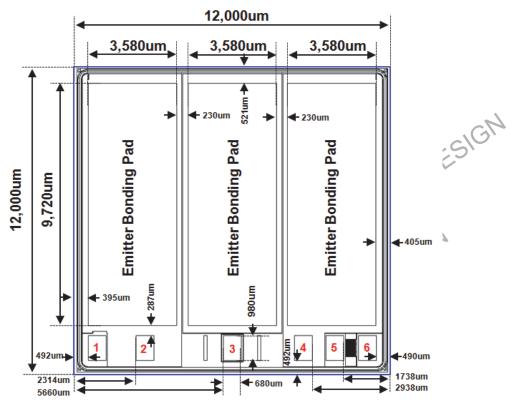
Parameter	Symbol	Test Condition		Min.	Тур.	Max.	Units
Static Characteristics (Tested on wafer	s)		4 11		1/		
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>	$V_{GE} = 0 \text{ V, } I_{C} =$	1 mA	650	_	_	V
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> = 100 A, V <sub>GE</sub>	= 15 V		1.25	1.55	V
Gate-Emitter Threshold Voltage	V <sub>GE(th)</sub>	V <sub>GE</sub> = V <sub>CE</sub> , I <sub>C</sub> =	= 300 mA	4.5	5.5	6.5	V
Collector Cut-Off Current	I <sub>CES</sub>	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>C</sub>	GE = 0 V		_	40	μΑ
Gate Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> = V <sub>GES</sub> , V	CE = 0 V	5/1/1	-	±400	nA
On-chip temperature – sense diode voltage	V <sub>F</sub>	I <sub>E</sub> = 0.5 mA		2.0	2.4	2.8	V
Integrated Temp and Current Sensor (not subjected to production test – yerifie		zation)	DR!				
On-chip temperature-sense diode voltage	NO KE CO	I <sub>F</sub> = 0.5 mA, T <sub>VJ</sub> = 100 °C		-	1.9	-	V
Emitter Sense Area Ratio	β <sub>AREA</sub>	Sense Area/Total Area			1/10K		-
Emitter Current Sense Ratio	β <sub>10</sub> Ω	$I_{CE}$ = 300 A, $V_{GE}$ = 15 V $R_{SENSE}$ = 10 $\Omega$		-	18K		_
Electrical Characteristics (Not subjecte	ed to production test – v	erified by design/	characterization)				
Collector to Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	$I_{C} = 300 \text{ A},$ $V_{GE} = 15 \text{ V}$ $T_{VJ} = 25 ^{\circ}\text{C}$ $T_{VJ} = 175 ^{\circ}\text{C}$	_	1.5	1.9	V	
III,			T <sub>VJ</sub> = 175 °C	-	1.8	-	V
Input Capacitance	C <sub>IES</sub>	V <sub>CE</sub> = 30 V, V <sub>GE</sub> = 0 V f = 1 MHz		-	14.0	-	nF
Output Capacitance	C <sub>OES</sub>				690		pF
Reverse Transfer Capacitance	C <sub>RES</sub>			_	106	_	pF
Internal Gate Resistance	R <sub>G</sub>	f = 1 MHz		-	1.7	-	Ω
Total Gate Charge	Q <sub>G(Total)</sub>	V <sub>CE</sub> = 400 V, I <sub>C</sub> = 300 A V <sub>GE</sub> = 15 V			307	-	nC
Gate-to-Emitter Charge	$Q_{GE}$			_	97	-	nC
Gate-to-Collector Charge	$Q_{GC}$			-	64	-	nC
Turn-On Delay Time	t <sub>d(on)</sub>	$\begin{array}{c} V_{CE} = 300 \text{ V, } I_{C} = 300 \text{ A} \\ R_{G} = 15 \Omega \\ V_{GE} = 15 \text{ V} \\ \text{Inductive Load} \\ T_{VJ} = 25 \ ^{\circ}\text{C} \end{array}$		-	167		ns
Rise Time	t <sub>r</sub>			_	107	-	ns
Turn-Off Delay Time	t <sub>d(off)</sub>			_	298	-	ns
Fall Time	t <sub>f</sub>			_	38	_	ns

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Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>CF</sub> = 300 V, I <sub>C</sub> = 300 A	-	130	-	ns
Rise Time	t <sub>r</sub>	$R_G = 15 \Omega$ $V_{GE} = 15 V$	-	93	-	ns
Turn-Off Delay Time	t <sub>d(off)</sub>	Inductive Load	-	395	-	ns
Fall Time	t <sub>f</sub>	T <sub>VJ</sub> = 150 °C	-	78	-	ns

<sup>3.</sup> For ordering, technique and other information on Onsemi automotive bare die products, please contact automotivebaredie@onsemi.com





- 1. Current Sense Bonding Pad
- 2. Emitter Sense Bonding Pad
- 3. Gate Bonding Pad
- 4. Emitter Sense Bonding Pad
- 5. Temp Sense Anode Bonding Pad
- 6. Temp Sense Cathode Bonding Pad

Figure 1. Dimensional Outline and Pad Layout

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