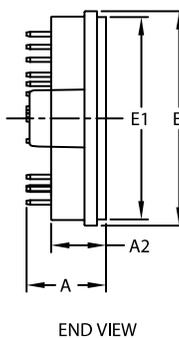
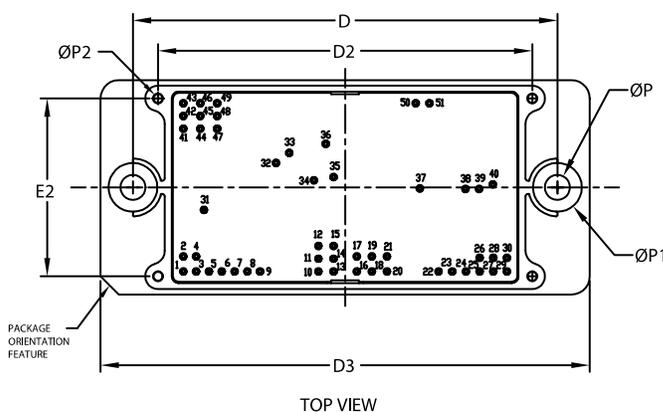
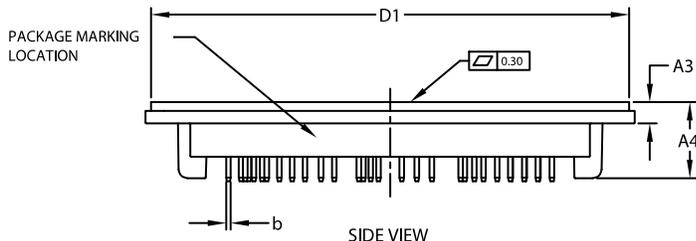
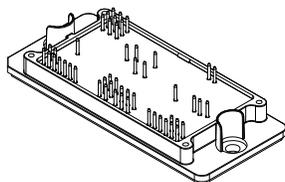


PIM51, 93x47 (SOLDER PIN)
CASE 180BM
ISSUE O

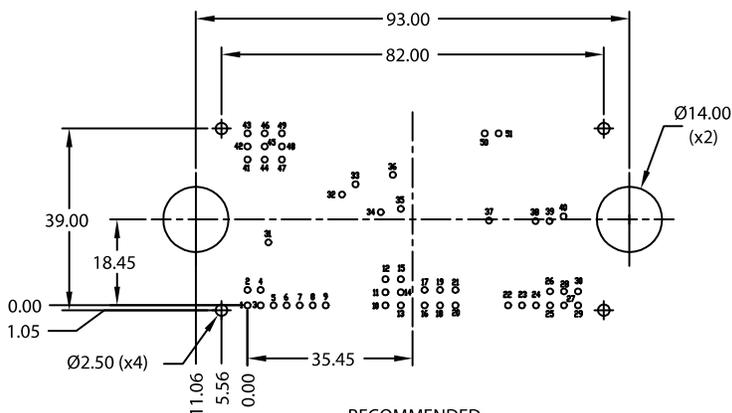
DATE 27 OCT 2021



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009
2. CONTROLLING DIMENSION : MILLIMETERS
3. DIMENSIONS b AND b1 APPLY TO THE PLATED TERMINALS AND ARE MEASURED AT DIMENSION A1
4. PIN POSITION TOLERANCE IS $\pm 0.4\text{mm}$
5. PACKAGE MARKING IS LOCATED AS SHOWN ON THE SIDE OPPOSITE THE PACKAGE ORIENTATION FEATURES

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	17.00	17.40	17.80
A2	11.70	12.00	12.30
A3	4.40	4.70	5.00
A4	16.40	16.70	17.00
b	0.95	1.00	1.05
D	92.90	93.00	93.10
D1	104.45	104.75	105.05
D2	81.80	82.00	82.20
D3	106.90	107.20	107.50
E	46.70	47.00	47.30
E1	44.10	44.40	44.70
E2	38.80	39.00	39.20
P	5.40	5.50	5.60
P1	10.60	10.70	10.80
P2	1.80	2.00	2.20



RECOMMENDED
MOUNTING PATTERN

* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTE 4

PIN	PIN POSITION		PIN	PIN POSITION	
	X	Y		X	Y
1	0.00	0.00	27	67.90	0.00
2	0.00	3.30	28	67.90	3.00
3	2.80	0.00	29	70.90	0.00
4	2.80	3.30	30	70.90	3.00
5	5.60	0.00	31	4.50	13.50
6	8.40	0.00	32	20.30	23.80
7	11.20	0.00	33	23.20	26.00
8	14.00	0.00	34	28.60	20.00
9	16.80	0.00	35	32.90	20.70
10	29.60	0.00	36	31.20	28.00
11	29.60	2.80	37	51.80	18.20
12	29.60	5.60	38	61.80	18.10
13	32.90	0.00	39	64.80	18.10
14	32.90	2.80	40	67.80	19.10
15	32.90	5.60	41	0.00	31.30
16	38.00	0.00	42	0.00	34.10
17	38.00	3.30	43	0.00	36.90
18	41.30	0.00	44	3.70	31.30
19	41.30	3.30	45	3.70	34.10
20	44.60	0.00	46	3.70	36.90
21	44.60	3.30	47	7.40	31.30
22	55.90	0.00	48	7.40	34.10
23	58.90	0.00	49	7.40	36.90
24	61.90	0.00	50	50.90	36.90
25	64.90	0.00	51	53.90	36.90
26	64.90	3.00			

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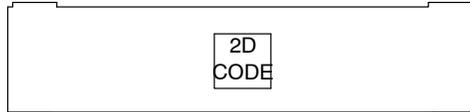
**PIM51, 93x47 (SOLDER PIN)
CASE 180BM
ISSUE O**

DATE 27 OCT 2021

**GENERIC
MARKING DIAGRAM***



FRONTSIDE MARKING



BACKSIDE MARKING

XXXXX = Specific Device Code
AT = Assembly & Test Site Code
YYWW = Year and Work Week Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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