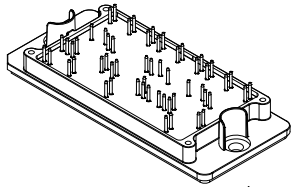


# MECHANICAL CASE OUTLINE

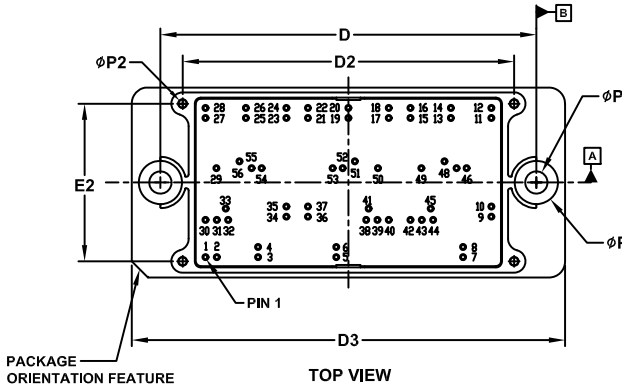
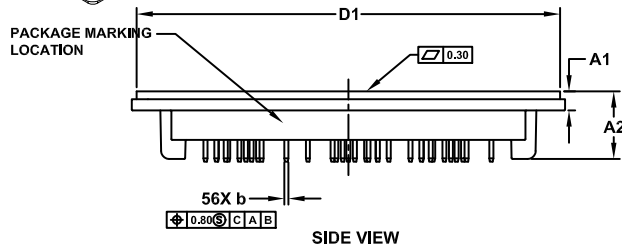
## PACKAGE DIMENSIONS

ON Semiconductor®



**PIM56, 93x47 (SOLDER PIN)**  
**CASE 180BR**  
**ISSUE O**

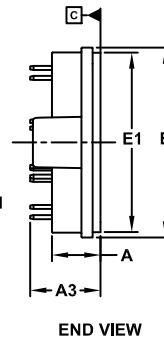
DATE 03 DEC 2019



**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSIONS b AND b1 APPLY TO THE PLATED TERMINALS AND ARE MEASURED AT DIMENSION A4.
4. POSITION OF THE CENTER OF THE TERMINALS IS DETERMINED FROM DATUM B THE CENTER OF DIMENSION D, X DIRECTION, AND FROM DATUM A, Y DIRECTION. POSITIONAL TOLERANCE, AS NOTED IN DRAWING, APPLIES TO EACH TERMINAL IN BOTH DIRECTIONS.
5. PACKAGE MARKING IS LOCATED AS SHOWN ON THE SIDE OPPOSITE THE PACKAGE ORIENTATION FEATURES.

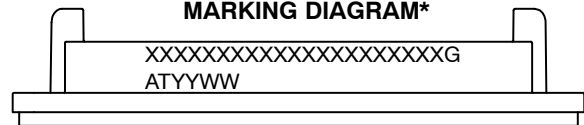
DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	11.70	12.00	12.30
A1	4.40	4.70	5.00
A2	16.40	16.70	17.00
A3	16.80	17.20	17.60
b	0.95	1.00	1.05
D	92.90	93.00	93.10
D1	104.45	104.75	105.05
D2	81.80	82.00	82.20
D3	106.90	107.20	107.50
E	46.70	47.00	47.30
E1	44.10	44.40	44.70
E2	38.80	39.00	39.10
P	5.40	5.50	5.60
P1	10.60	10.70	10.80
P2	1.80	2.00	2.20



**NOTE 4**

PIN	PIN POSITION		PIN	PIN POSITION	
	X	Y		X	Y
1	0.00	0.00	29	2.70	22.00
2	2.80	0.00	30	0.00	9.20
3	13.00	0.00	31	2.80	9.20
4	13.00	2.50	32	5.60	9.20
5	32.35	0.00	33	5.00	12.00
6	32.35	2.50	34	20.00	10.00
7	63.70	0.00	35	20.00	12.50
8	63.70	2.50	36	25.35	10.00
9	70.70	10.00	37	25.35	12.50
10	70.70	12.50	38	39.75	9.20
11	70.70	34.40	39	42.55	9.20
12	70.70	36.90	40	45.35	9.20
13	60.70	34.40	41	40.35	12.00
14	60.70	36.90	42	50.70	9.20
15	50.70	34.40	43	53.50	9.20
16	50.70	36.90	44	56.30	9.20
17	45.35	34.40	45	55.70	12.00
18	45.35	36.90	46	64.60	22.00
19	35.35	34.40	47	62.10	22.00
20	35.35	36.90	48	59.10	23.70
21	25.35	34.40	49	53.40	22.00
22	25.35	36.90	50	42.65	22.00
23	20.00	34.40	51	36.95	23.70
24	20.00	36.90	52	33.95	22.00
25	10.00	34.40	53	31.45	22.00
26	10.00	36.90	54	13.90	22.00
27	0.00	34.40	55	11.40	22.00
28	0.00	36.90	56	8.40	23.70

**GENERIC MARKING DIAGRAM\***



XXXXX = Specific Device Code  
 G = Pb-Free Package  
 AT = Assembly & Test Site Code  
 YYWW = Year and Work Week Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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<b>DESCRIPTION:</b>	<b>PIM56 93X47 (SOLDER PIN)</b>	<b>PAGE 1 OF 1</b>

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