

MECHANICAL CASE OUTLINE

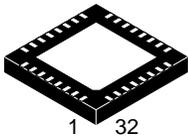
PACKAGE DIMENSIONS

ON Semiconductor®

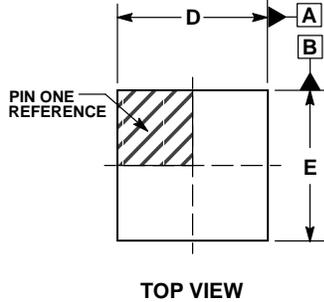


QFNW32 7x7, 0.65P CASE 484AG ISSUE A

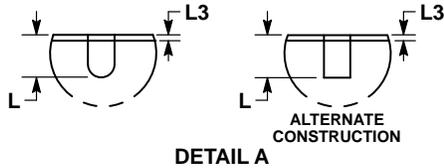
DATE 07 AUG 2018



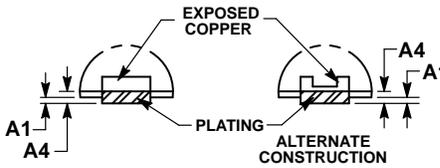
SCALE 2:1



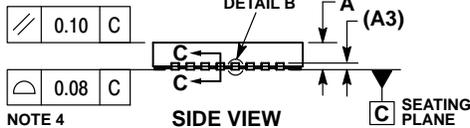
TOP VIEW



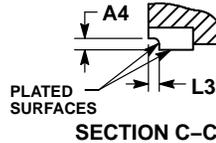
DETAIL A



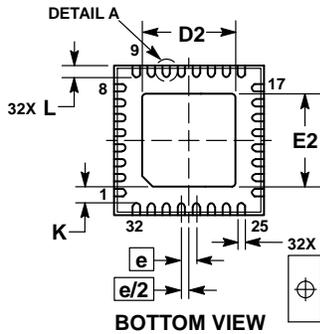
DETAIL B



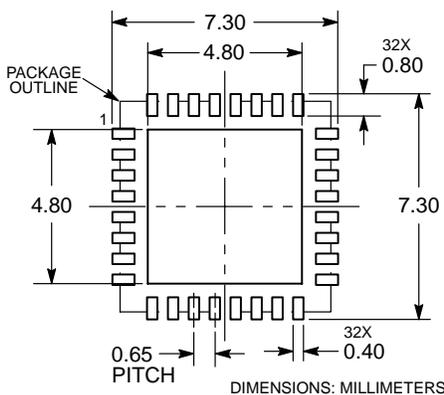
SIDE VIEW



SECTION C-C



RECOMMENDED
SOLDERING FOOTPRINT



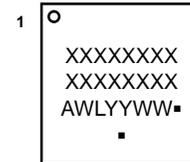
DIMENSIONS: MILLIMETERS

NOTES:

1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.10 AND 0.20MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
5. THIS DEVICE CONTAINS WETTABLE FLANK DESIGN FEATURES TO AID IN FILLET FORMATION ON THE LEADS DURING MOUNTING.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	0.80	0.85	0.90
A1	---	---	0.05
A3	0.20 REF		
A4	0.10	---	---
b	0.25	0.30	0.35
D	6.90	7.00	7.10
D2	4.60	4.70	4.80
E	6.90	7.00	7.10
E2	4.60	4.70	4.80
e	0.65 BSC		
K	0.60 REF		
L	0.50	0.55	0.60
L3	0.05 REF		

GENERIC MARKING DIAGRAM*



- XXXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	QFNW32 7x7, 0.65P	PAGE 1 OF 1

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