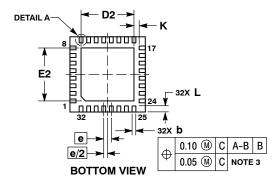
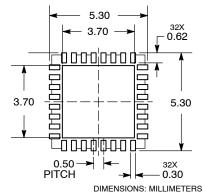


### CASE 485CE ISSUE O SCALE 2:1 D Α В PIN ONE — REFERENCE **DETAIL A** ALTERNATE CONSTRUCTIONS Ε С 0.15 **EXPOSED Cu** MOLD CMPD С △ 0.15 **TOP VIEW DETAIL B** (A3) **DETAIL B** С 0.10 **ALTERNATE** CONSTRUCTION 0.08 С SEATING PLANE NOTF 4 SIDE VIEW



## **RECOMMENDED SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# QFN32 5x5, 0.5P

**DATE 07 FEB 2012** 

#### NOTES:

- NOTES:

  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

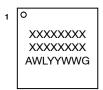
  2. CONTROLLING DIMENSION: MILLIMETERS.

  3. DIMENSION 6 APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN

  145. AND 100 AND AND TOOK THE TERMINAL TO
- COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS		
DIM	MIN	MAX	
Α	0.80	1.00	
A1		0.05	
A3	0.20 REF		
b	0.20	0.30	
D	5.00 BSC		
D2	3.40	3.60	
E	5.00 BSC		
E2	3.40	3.60	
е	0.50	BSC	
K	0.20		
L	0.30	0.50	
L1		0.15	

## **GENERIC** MARKING DIAGRAM\*



XXXXX = Specific Device Code = Assembly Location

WL = Wafer Lot VV = Year ww = Work Week = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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