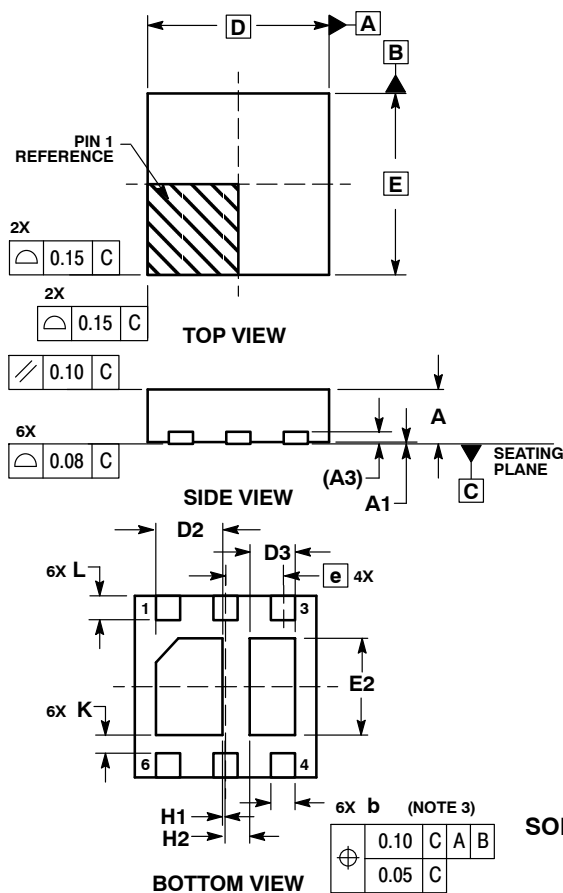




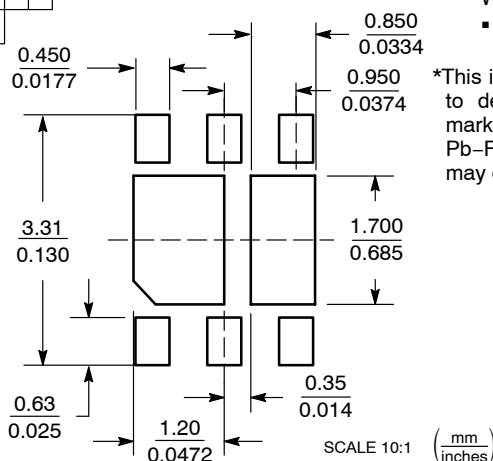
**SCALE 2:1**

**DFN6 3X3 MM, 0.95 PITCH**  
CASE 506AG  
ISSUE O

DATE 18 NOV 2004



## SOLDERING FOOTPRINT\*



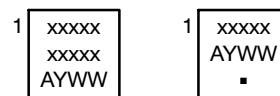
\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES:

1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 MM FROM TERMINAL.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS		
DIM	MIN	NOM	MAX
A	0.80	0.90	1.00
A1	0.00	0.03	0.05
A3	0.20 REF		
b	0.35	0.40	0.45
D	3.00 BSC		
D2	1.00	1.10	1.20
D3	0.65	0.75	0.85
E	3.00 BSC		
E2	1.50	1.60	1.70
e	0.95 BSC		
K	0.21		
L	0.30	0.40	0.50
H1	0.05 REF		
H2	0.40 REF		

### GENERIC MARKING DIAGRAM\*



### Standard

### Pb-Free

xxxxx = Specific Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
▪ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking.  
Pb-Free indicator, "G" or microdot "▪", may or may not be present.

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<b>DESCRIPTION:</b>	<b>DFN6 3X3 MM, 0.95 PITCH, DUAL FLAG</b>	<b>PAGE 1 OF 1</b>

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