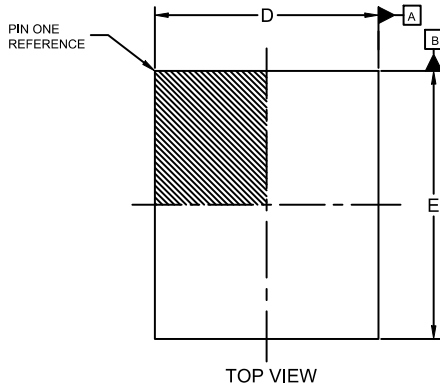


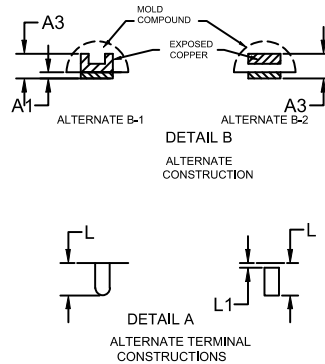
WQFN16 5x6, 1.05P
CASE 510CM
ISSUE O

DATE 14 JUN 2019

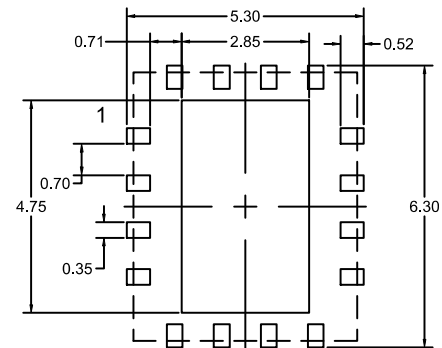
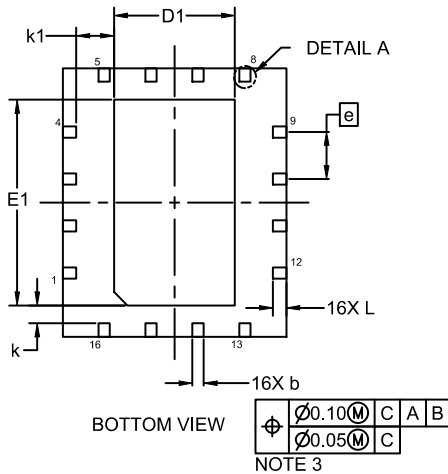
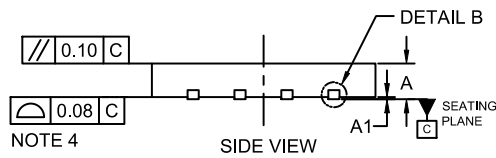


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION b APPLIES TO THE PLATED TERMINALS AND IS MEASURED BETWEEN 0.20 AND 0.25 FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE PLATED TERMINALS.



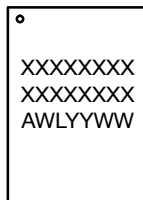
DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
A1	0.00	0.025	0.05
A3	0.15	0.20	0.25
b	0.20	0.25	0.30
D	4.90	5.00	5.10
D1	2.65	2.70	2.75
E	5.90	6.00	6.10
E1	4.55	4.60	4.65
e	1.05 BSC		
k	0.40 REF		
k1	0.85 REF		
L	0.25	0.30	0.35
L1	0.05 REF		



RECOMMENDED MOUNTING PATTERN

* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
 A = Assembly Location
 WL = Wafer Lot
 YY = Year
 WW = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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