

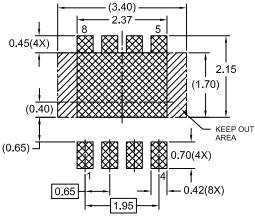


**DATE 02 FEB 2022** 

## NOTES:

- A. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009.
- B. SEATING PLANE IS DEFINED BY TERMINAL TIPS ONLY
- C. BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH PROTRUSIONS NOR GATE BURRS. MOLD FLASH PROTRUSION OR GATE BURR DOES NOT EXCEED 0.150MM.

DIM	MILLIMETERS			
	MIN	NOM	MAX	
Α	0.70	0.75	0.80	
A1	0.00	ı	0.05	
A3	0.15	0.20	0.25	
b	0.27	0.32	0.37	
D	3.20	3.30	3.40	
D1	3.10	3.20	3.30	
D3	2.17	2.27	2.37	
Е	3.20	3.30	3.40	
E1	2.90	3.00	3.10	
E2	1.95	2.05	2.15	
E3	0.15	0.20	0.25	
E4	0.30	0.40	0.50	
E5	0.40 REF			
е	0.65 BSC			
L	0.30	0.40	0.50	
θ	0°	-	12°	

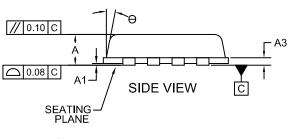


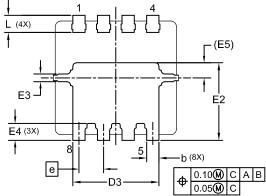
## RECOMMENDED LAND PATTERN

\*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

## ○ 0.10 C Α 2X В PIN1 □ 0.10 C IDENT

TOP VIEW





**BOTTOM VIEW** 

WDFN8 3.3x3.3, 0.65P

## **GENERIC MARKING DIAGRAM\***

XXXX AYWW= XXXX = Specific Device Code = Assembly Location = Year = Work Week WW

= Pb-Free Package (Note: Microdot may be in either location) \*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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**DESCRIPTION:** 

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