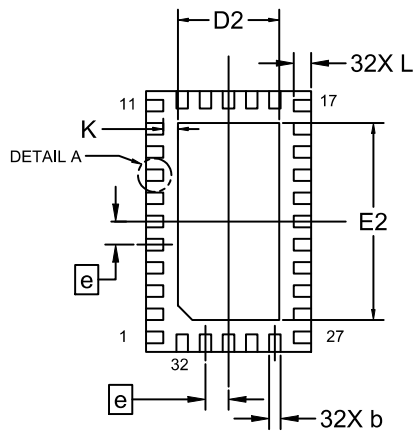
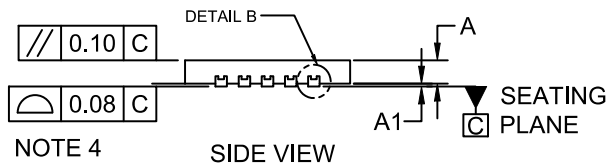
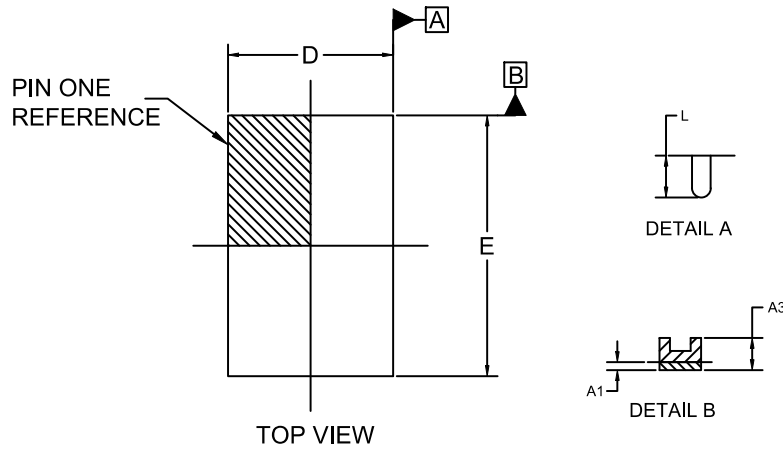


SCALE 2:1

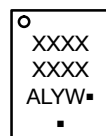
**X2QFN32 2.85x4.5, 0.4P**  
CASE 722AF  
ISSUE B

DATE 15 JAN 2020



0.10	C	A	B
0.05	C		

NOTE 3

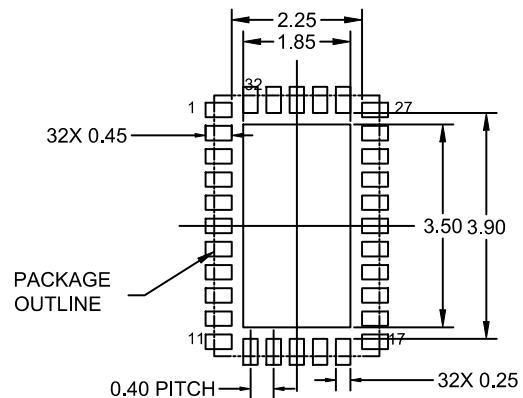
**GENERIC MARKING DIAGRAM\***

XXXX = Specific Device Code  
A = Assembly Location  
L = Wafer Lot  
Y = Year  
W = Work Week  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

## NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS
- DIMENSION b APPLIES TO THE PLATED TERMINALS AND IS MEASURED BETWEEN 0.20 AND 0.25 FROM THE TERMINAL TIP.
- COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.30	0.35	0.40
A1	--	--	0.05
A3	0.127 REF		
b	0.15	0.20	0.25
D	2.75	2.85	2.95
D2	1.65	1.75	1.85
E	4.40	4.50	4.60
E2	3.30	3.40	3.50
e	0.40 BSC		
K	0.25 REF		
L	0.20	0.30	0.40


**RECOMMENDED MOUNTING FOOTPRINT\***

- \* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present. Some products may not follow the Generic Marking.

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