

产品概览

MC100LVEL29: ECL Dual Differential Clock/Data D Flip-Flop with Set and Reset

欲看完整文档，请参阅数据表。

The MC100LVEL29 is a dual master-slave flip flop. The device features fully differential Data and Clock inputs as well as outputs. The MC100LVEL29 is pin and functionally equivalent to the MC100EL29. Data enters the master latch when the clock is LOW and transfers to the slave upon a positive transition on the clock input. The differential inputs have special circuitry which ensures device stability under open input conditions. When both differential inputs are left open the D input will pull down to VEE and the Dbar input will bias around VCC/2. The outputs will go to a defined state, however the state will be random based on how the flip flop powers up. Both flip flops feature asynchronous, overriding Set and Reset inputs. Note that the Set and Reset inputs cannot both be HIGH simultaneously. The VBB pin, an internally generated voltage supply, is available to this device only. For single-ended input conditions, the unused differential input is connected to VBB as a switching reference voltage. VBB may also rebias AC coupled inputs. When used, decouple VBB and VCC via a 0.01 F capacitor and limit current sourcing or sinking to 0.5 mA. When not used, VBB should be left open.

特性

- 1100MHz Flip-Flop Toggle Frequency
- ESD Protection: >2 KV HBM
- 580 ps Typical Propagation Delays
- The 100 Series Contains Temperature Compensation
- PECL Mode Operating Range: VCC = 3.0 V to 3.8 V with VEE = 0 V
- NECL Mode Operating Range: VCC = 0 V with VEE = -3.0 V to -3.8 V
- Internal Input Pulldown Resistors
- Meets or Exceeds JEDEC Spec EIA/JESD78 IC Latchup Test
- Flammability Rating: UL-94 code V-0 @ 1Å", Oxygen Index 28 to 34
- Transistor Count = 313 devices

For more features, see the data sheet

器件电气规格

| 产品 | Compliance | Status | Type | Bits | Input Level | Output Level | V _{CC} Typ (V) | t _{jitter} Typ (ps) | t _{pd} Typ (ns) | t _{su} Min (ns) | t _h Min (ns) | t _{rec} Typ (ns) | t _R & t _F Max (ps) | f _{Toggle} Typ (MHz) | Package Type |
|------------------|------------------------|--------|--------|------|-----------------|--------------|-------------------------|------------------------------|--------------------------|--------------------------|-------------------------|---------------------------|--|-------------------------------|--------------|
| MC100LVEL29DWG | Pb-free Halide free | Active | D-Type | 2 | ECL LVD S | ECL | 3.3 | 1 | 0.58 | 0 | 0.1 | 0.1 | 550 | 1100 | SOIC-20W |
| MC100LVEL29DWR2G | Pb-free Halide free | Active | D-Type | 2 | ECL LVD S | ECL | 3.3 | 1 | 0.58 | 0 | 0.1 | 0.1 | 550 | 1100 | SOIC-20W |

欲了解更多信息，请联系您当地的销售支援 www.onsemi.cn。

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