

产品概览

MC10EL31: 5.0 V ECL D Flip-Flop with Set and Reset

欲看完整文档，请参阅数据表。

The MC10EL/100EL31 is a D flip-flop with set and reset. The device is functionally equivalent to the E131 device with higher performance capabilities. With propagation delays and output transition times significantly faster than the E131 the EL31 is ideally suited for those applications which require the ultimate in AC performance. Both set and reset inputs are asynchronous, level triggered signals. Data enters the master portion of the flip-flop when clock is LOW and is transferred to the slave, and thus the outputs, upon a positive transition of the clock. The 100 Series contains temperature compensation.

特性

- 475ps Propagation Delay
- 2.8GHz Toggle Frequency
- ESD Protection: > 1 KV HBM, > 100 V MM
- PECL Mode Operating Range: VCC= 4.2 V to 5.7 V with VEE= 0 V
- NECL Mode Operating Range: VCC= 0 V with VEE= -4.2 V to -5.7 V
- Internal Input Pulldown Resistors on D, CLK, S, and R
- Meets or Exceeds JEDEC Spec EIA/JESD78 IC Latchup Test
- Moisture Sensitivity Level 1 For Additional Information, see Application Note AND8003/D
- Flammability Rating: UL-94 code V-0 @ 1/8", Oxygen Index 28 to 34
- Metastability 125 ps (see Application Note AN1504)

For more features, see the data sheet

器件电气规格

产品	Compliance	Status	Type	Bits	Input Level	Output Level	V _{CC} Typ (V)	t _{jitter} Typ (ps)	t _{pd} Typ (ns)	t _{su} Min (ns)	t _h Min (ns)	t _{ree} Typ (ns)	t _r & t _f Max (ps)	f _{Toggle} Typ (MHz)	Package Type
MC10EL31DG	Pb-free	Active	D-Type	1	ECL	ECL	5	1	0.475	0.15	0.25	0.2	350	2800	SOIC-8
	Halide free														
MC10EL31DR2G	Pb-free	Active	D-Type	1	ECL	ECL	5	1	0.475	0.15	0.25	0.2	350	2800	SOIC-8
	Halide free														

欲了解更多信息，请联系您当地的销售支援 www.onsemi.cn。

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