

产品概览

ESD8104: USB3.0 ESD Protection Array

欲看完整文档，请参阅数据表。

The ESD8104 is designed to protect high speed data lines from ESD. Ultra low capacitance and low ESD clamping voltage make this device an ideal solution for protecting voltage sensitive high speed data lines. The flow through style package allows for easy PCB layout and matched trace lengths necessary to maintain consistent impedance between high speed differential lines such as USB 3.0.

特性

- 0.37pF max capacitance
- Low clamping voltage of 11.4V at 16A

优势

- Maintains signal integrity for data lines up to 5Gb/s and greater
- Protects chipsets with small geometries used in USB3.0 applications

应用

- USB3.0

器件电气规格

产品	Pricing (\$/Unit)	Compliance	Status	Interface	Number of Lines	Direction	C Max (pF)	V _(BR) Min (V)	V _{RWM} Max (V)	I _R Max (μA)	P _{PK} Max (W)	Package Type
ESD8104MUTAG	0.156	Pb-free Halide free	Active	Display Port	4	Unidirectional	0.37	4	3.3	1	-	UDFN-10
				USB 3.1								
				HDMI 1.3/1.4								
				USB 3.0								
				eSATA 3.0								
HDMI 2.0												
SZESD8104MUTAG	0.1716	AEC Qualified PPAP Capable Pb-free Halide free	Active	HDMI 1.3/1.4 HDMI 2.0 Display Port eSATA 3.0 USB 3.1 USB 3.0	4	Unidirectional	0.37	4	3.3	1	-	UDFN-10

欲了解更多信息，请联系您当地的销售支援 www.onsemi.cn。

创建于：6/1/2020