







# PCA9655E

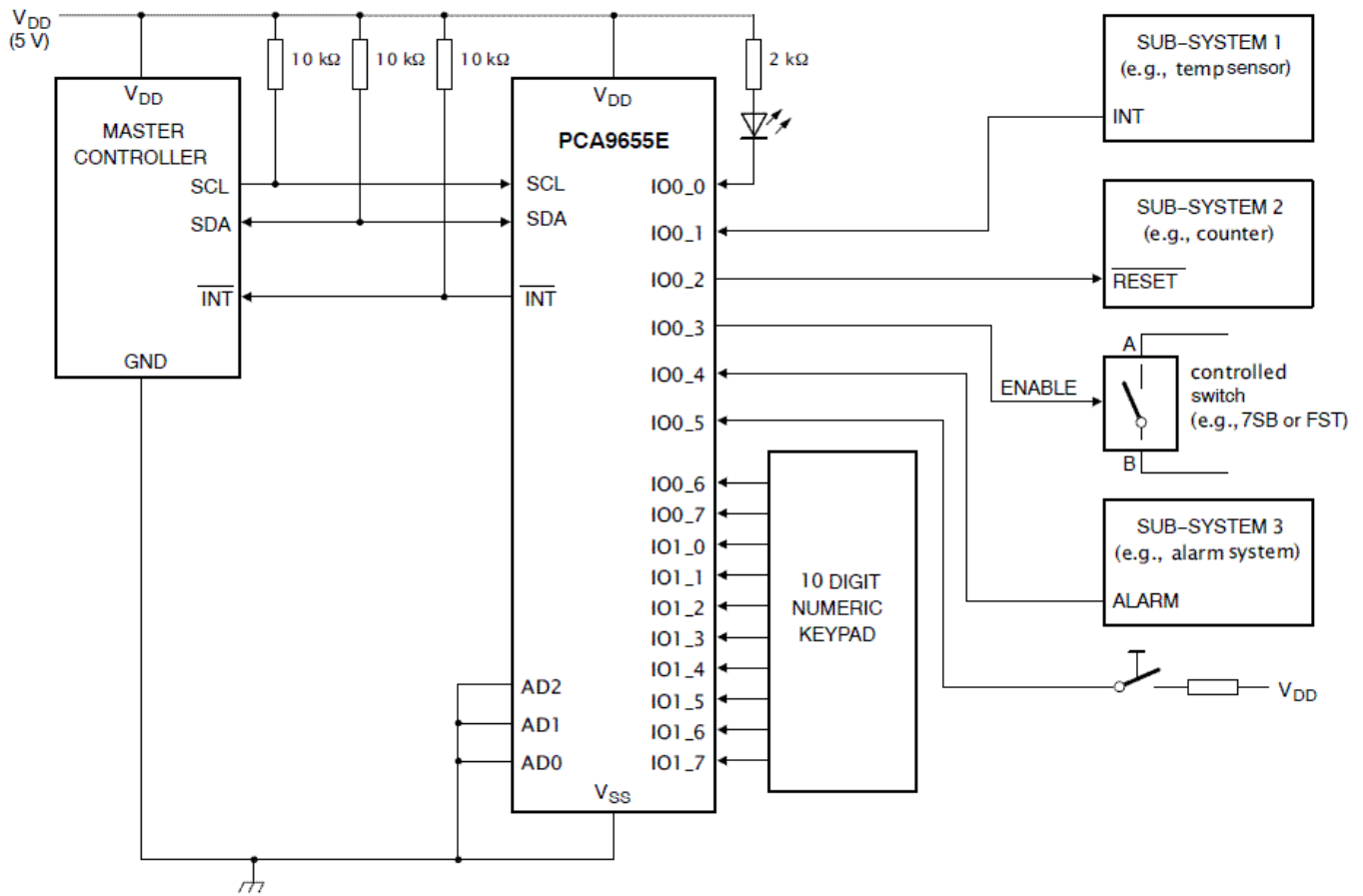
## I/O I2C16

PCA9535E I2C SMBus 16 / (GPIO) PCA9535E 8 I/O I/O I/O PCA9655E AD0AD1AD2 I2C 64 I2C SMBus

- VCC Operating Range: 1.65 V to 5.5 V
  - SDA Sink Capability: 30 mA
  - 5.5 V Tolerant I/Os
  - Polarity Inversion Register
  - Active LOW Interrupt Output
  - Low Standby Current
  - Noise Filter on SCL/SDA Inputs
  - No Glitch on Powerup
  - Internal Poweron Reset
  - 64 Programmable Slave Addresses Using Three Address Pins
- For more features, see the data sheet

- Input/output Port Expansion for Extended Addressing Capability
- Keyboards

	Pricing (\$/Unit)	Compliance	Status	I/O	Cascadable	V <sub>CC</sub> Min (V)	V <sub>CC</sub> Max (V)	Interrupt Output	I/O Pullups	LED Blink/PWM	I <sub>o</sub> Min (mA)	Package Type
PCA9655EDTR2G	0.9601	 	Active	16	8 Slave ID Addresses	1.65	5.5	Yes	Yes	No	25	TSSOP-24
PCA9655EDWR2G	0.889	 	Active	16	8 Slave ID Addresses	1.65	5.5	Yes	Yes	No	25	SOIC-24 WB
PCA9655EMTTXG	1.1339	 	Active	16	8 Slave ID Addresses	1.65	5.5	Yes	Yes	No	25	QFN-24



Device address configured as 0100 000xb for this example.

IO0\_0, IO0\_2, IO0\_3 configured as outputs.

IO0\_1, IO0\_4, IO0\_5 configured as inputs.

IO0\_6, IO0\_7, and IO1\_0 to IO1\_7 configured as inputs.