

PIN ONE REFERENCE

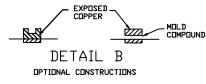
## WQFN12 3.0x2.0, 0.5P

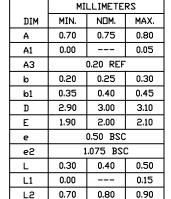
CASE 510BM ISSUE C

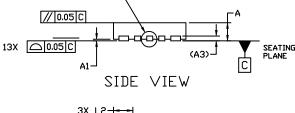
**DATE 09 DEC 2019** 

## NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. DIMENSION 6 AND 61 APPLY TO THE PLATED TERMINALS AND ARE MEASURED BETWEEN 0.15 AND 0.25MM FROM THE TERMINAL TIP.
- 4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.







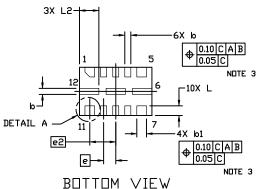
D

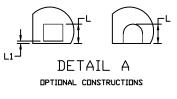
TOP VIEW

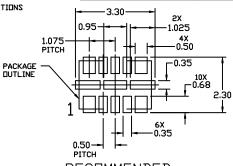
DETAIL B

A

В







## RECOMMENDED MOUNTING FOOTPRINT

\* For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

## GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code

A = Assembly Location

L = Wafer Lot Y = Year

W = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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