PIN ONE REFERENCE

 $2x \square 0.10$ 

8X \( \triangle \) 0.05 \( \triangle \) C

NOTE 3

△ 0.10 C

0.10 C

D3

**E**3

6X L

4X L3

SCALE 2:1

# WDFN6 2.6x4.0, 0.65P, Dual Flag CASE 511BZ **ISSUE B**

A B

E1 E

L2

4X b2

E2

6X b

Ф

0.10 M C A B

0.05 M

SEATING PLANE C

D

**TOP VIEW** 

**SIDE VIEW** 

**BOTTOM VIEW** 

**DATE 02 NOV 2016** 

#### NOTES

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  CONTROLLING DIMENSION: MILLIMETERS.
- PROFILE TOLERANCE APPLIES TO THE EXPOSED PADS AS WELL AS THE LEADS. 3.

|     | MILLIMETERS |       |  |
|-----|-------------|-------|--|
| DIM | MIN         | MAX   |  |
| Α   |             | 0.80  |  |
| A3  | 0.10        | 0.25  |  |
| b   | 0.25        | 0.40  |  |
| b2  | 0.15        | 0.30  |  |
| D   | 2.60 BSC    |       |  |
| D2  | 2.075       | 2.375 |  |
| D3  | 1.20        | 1.50  |  |
| D4  | 0.40        | 0.70  |  |
| E   | 4.00 BSC    |       |  |
| E1  | 3.80 REF    |       |  |
| E2  | 2.95        | 3.05  |  |
| E3  | 2.25        | 2.55  |  |
| е   | 0.65 BSC    |       |  |
| L   | 0.12        | 0.32  |  |
| L2  |             | 0.10  |  |
| L3  |             | 0.55  |  |

### **GENERIC**

## **MARKING DIAGRAM\***





w/o ejector pin

XXXXX = Specific Device Code

= Assembly Location Α

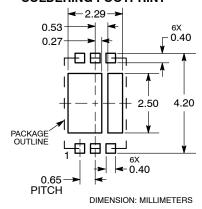
= Year WW

= Work Week = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking.

### RECOMMENDED **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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|------------------|---------------------------------|---|-------------|
| DESCRIPTION:     | WDFN6 2.6X4.0, 0.65P, DUAL FLAG |   | PAGE 1 OF 1 |

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