

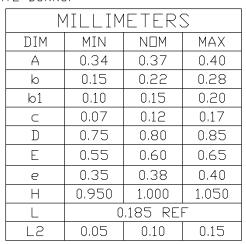


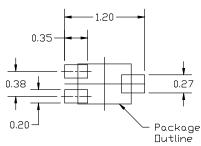
SOT-1123 0.80x0.60x0.37, 0.35P CASE 524AA ISSUE D

DATE 18 JAN 2024

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3, MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
 MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS
 OF BASE MATERIAL.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.





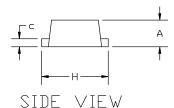
RECOMMENDED MOUNTING FOOTPRINT

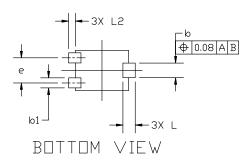
*For additional information on our Pb-Free strategy and soldering details, please download th e □N Semiconductor Soldering and Mounting Techniques Reference manual, S□LDERRM/D.

> STYLE 5: PIN 1. GATE 2. SOURCE

D A B

TOP VIEW





GENERIC MARKING DIAGRAM*



X = Specific Device Code

M = Date Code

^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:
PIN 1. BASE	PIN 1. ANODE	PIN 1. ANODE	PIN 1. CATHODE
2. EMITTER	2. N/C	2. ANODE	CATHODE
COLLECTOR	CATHODE	CATHODE	ANODE

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DESCRIPTION:	SOT-1123 0.80x0.60x0.37, 0.35P		PAGE 1 OF 1	

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