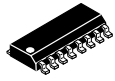


# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

ON Semiconductor®

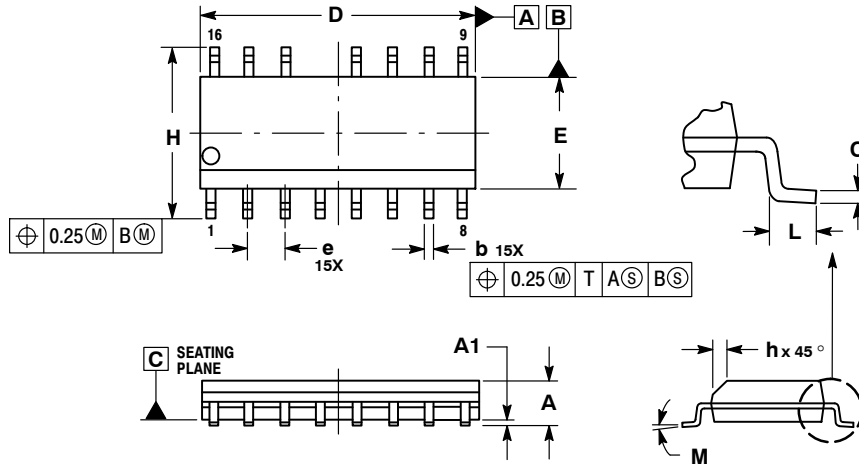


SCALE 1:1

### SOIC-16 NB, LESS PIN 13

CASE 751AM-01  
ISSUE O

DATE 20 AUG 2007

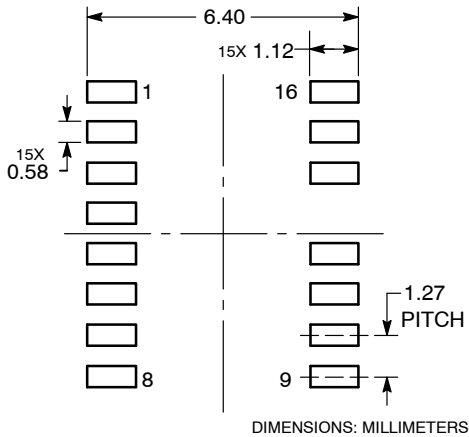


NOTES:

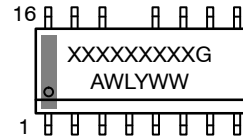
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF THE b DIMENSION AT MAXIMUM MATERIAL CONDITION.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSIONS.
5. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.

| MILLIMETERS |            |
|-------------|------------|
| DIM         | MIN MAX    |
| A           | 1.35 1.75  |
| A1          | 0.10 0.25  |
| b           | 0.35 0.49  |
| C           | 0.19 0.25  |
| D           | 9.80 10.00 |
| E           | 3.80 4.00  |
| e           | 1.27 BSC   |
| H           | 5.80 6.20  |
| h           | 0.25 0.50  |
| L           | 0.40 1.25  |
| M           | 0° 7°      |

### SOLDERING FOOTPRINT



### GENERIC MARKING DIAGRAM\*



- XXXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- Y = Year
- WW = Work Week
- G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

|                         |                                |  |
|-------------------------|--------------------------------|--|
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| <b>DESCRIPTION:</b>     | <b>SOIC-16 NB, LESS PIN 13</b> | <b>PAGE 1 OF 1</b>   |

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