κ

D

В

⊕ 0.010 (0.254) M T



D<sup>2</sup>PAK 5-LEAD CASE 936A-02 ISSUE D

TERMINAL 6

**DATE 15 SEP 2015** 

## NOTES:

- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. TAB CONTOUR OPTIONAL WITHIN DIMENSIONS A

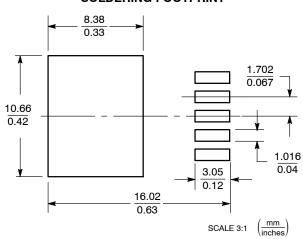
- AND K.
  DIMENSIONS U AND V ESTABLISH A MINIMUM
  MOUNTING SURFACE FOR TERMINAL 6.
  DIMENSIONS A AND B DO NOT INCLUDE MOLD
  FLASH OR GATE PROTRUSIONS. MOLD FLASH
  AND GATE PROTRUSIONS NOT TO EXCEED 0.025 (0.635) MAXIMUM.

	INC	HES	MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.386	0.403	9.804	10.236
В	0.356	0.368	9.042	9.347
С	0.170	0.180	4.318	4.572
D	0.026	0.036	0.660	0.914
E	0.045	0.055	1.143	1.397
G	0.067 BSC		1.702 BSC	
Н	0.539	0.579	13.691	14.707
K	0.050 REF		1.270 REF	
L	0.000	0.010	0.000	0.254
M	0.088	0.102	2.235	2.591
N	0.018	0.026	0.457	0.660
P	0.058	0.078	1.473	1.981
R	0°	8°	0°	8°
S	0.116 REF		2.946 REF	
U	0.200 MIN		5.080 MIN	
٧	0.250 MIN		6.350 MIN	

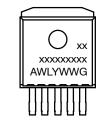
## **SOLDERING FOOTPRINT**

OPTIONAL CHAMFER

S



## **GENERIC MARKING DIAGRAM\***



xxxxxx = Device Code = Assembly Location

= Wafer Lot WL Υ = Year WW = Work Week = Pb-Free Package G

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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