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Advance Information Integrated Relay/Solenoid Driver

- Optimized to Switch 3 V to 5 V Relays from a 5 V Rail
- Compatible with "TX" and "TQ" Series Telecom Relays Rated up to 625 mW at 3 V to 5 V
- Features Low Input Drive Current
- Internal Zener Clamp Routes Induced Current to Ground Rather Than Back to Supply
- Guaranteed Off State with No Input Connection
- Supports Large Systems with Minimal Off-State Leakage
- ESD Resistant in Accordance with the 2000 V Human Body Model
- Provides a Robust Driver Interface Between Relay Coil and Sensitive Logic Circuits

Applications include:

- Telecom Line Cards and Telephony
- Industrial Controls
- Security Systems
- Appliances and White Goods
- Automated Test Equipment
- Automotive Controls

This device is intended to replace an array of three to six discrete components with an integrated part. It can be used to switch other 3 to 5 Vdc Inductive Loads such as solenoids and small DC motors.

MAXIMUM RATINGS

| Unit Vdc |
|-------------|
| Vdc |
| |
| Vdc |
| Vdc |
| Vdc |
| mA |
| °C |
| °C |
| °C |
| |

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Мах | Unit |
|--|---------------|-----|------|
| Total Device Dissipation ⁽¹⁾ Derate above 25°C | PD | 625 | mW |
| Thermal Resistance Junction to Ambient | $R_{	hetaJA}$ | 200 | °C/W |

1. FR–5 PCB of 1" x 0.75" x 0.062", T_A = 25°C

This document contains information on a new product. Specifications and information herein are subject to change without notice.





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RELAY/SOLENOID DRIVER SILICON MONOLITHIC CIRCUIT BLOCK

| | V _{out} Q (2) | - |
|---|------------------------|-------|
| V _{in} 1.0 k (³⁾ 33 k | | 6.8 V |
| • | | |

INTERNAL CIRCUIT DIAGRAM

CASE 29-11, STYLE 14

TO-92

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|---|-----|-------------|-----------|------|
| OFF CHARACTERISTICS | | | | | |
| Output Zener Breakdown Voltage (@ IT = 10 mA Pulse) | V _(BRout) V _(-BRout) | 6.4 | 6.8 -0.7 | 7.2 | V |
| Output Leakage Current @ 0 Input Voltage $(V_{out} = 5.5 \text{ Vdc}, V_{in} = 0.C., T_A = 25^{\circ}C)$ $(V_{out} = 5.5 \text{ Vdc}, V_{in} = 0.C., T_A = 85^{\circ}C)$ | I _{OO} | | | 5.0 30 | μΑ |

ON CHARACTERISTICS

| Input Bias Current @ V _{in} = 4.0 Vdc (I _O = 250 mA, V _{out} = 0.4 Vdc, T _A = -40°C) (correlated to a measurement @ 25°C) | l _{in} | _ | 2.5 | | mAdc |
|---|--------------------|-----|-----|-----|------|
| Output Saturation Voltage ($I_O = 250 \text{ mA}, V_{in} = 4.0 \text{ Vdc}, T_A = -40^{\circ}\text{C}$) (correlated to a measurement @ 25°C) | | - | 0.2 | 0.4 | Vdc |
| Output Sink Current —[Continuous (T _A = -40°C, V _{CE} = 0.4 Vdc, V _{in} = 4.0 Vdc) (correlated to a measurement @ 25°C) | I _{C(on)} | 250 | _ | S. | mA |

TYPICAL APPLICATION-DEPENDENT SWITCHING PERFORMANCE

SWITCHING CHARACTERISTICS

| Characteristic | Symbol | Vcc | Min | Тур | Мах | Units |
|--|--------------------------|-----|----------------|------|-----|-------|
| Propagation Delay Times: | | O' | | | | ns |
| High to Low Propagation Delay; Figures 1, 2 (5.0 V 74HC04) | t _{PHL} | 5.5 | | 55 | — | |
| Low to High Propagation Delay; Figures 1, 2 (5.0 V 74HC04) | t _{PLH} | 5.5 | O [×] | 430 | — | |
| High to Low Propagation Delay; Figures 1, 3 (3.0 V 74HC04) | CtPHL | 5.5 | (_ | 85 | _ | |
| Low to High Propagation Delay; Figures 1, 3 (3.0 V 74HC04) | t _{PLH} | 5.5 | _ | 315 | — | |
| High to Low Propagation Delay; Figures 1, 4 (5.0 V 74LS04) | teni | 5.5 | _ | 55 | _ | |
| Low to High Propagation Delay; Figures 1, 4 (5.0 V 74LS04) | tPLH | 5.5 | _ | 2385 | — | |
| Transition Times: | | | | | | ns |
| Fall Time; Figures 1, 2 (5.0 V 74HC04) | t _f | 5.5 | _ | 45 | — | |
| Rise Time; Figures 1, 2 (5.0 V 74HC04) | tr | 5.5 | — | 160 | — | |
| Fall Time; Figures 1, 3 (3.0 V 74HC04) | t _f | 5.5 | — | 70 | — | |
| Rise Time; Figures 1, 3 (3.0 V 74HC04) | t _r | 5.5 | — | 195 | — | |
| Fall Time; Figures 1, 4 (5.0 V 74LS04) | t _f | 5.5 | _ | 45 | — | |
| Rise Time; Figures 1, 4 (5.0 V 74LS04) | tr | 5.5 | — | 2400 | | |
| Input Slew Rate ⁽¹⁾ | $\Delta V / \Delta t$ in | 5.5 | TBD | — | — | V/ms |

1. Minimum input slew rate must be followed to avoid overdissipating the device.



Figure 1. Switching Waveforms











PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AL



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