MMBV3102LT1G

Silicon Tuning Diode

This device is designed in the Surface Mount package for general frequency control and tuning applications. It provides solid–state reliability in replacement of mechanical tuning methods.

Features

- High Q with Guaranteed Minimum Values at VHF Frequencies
- Controlled and Uniform Tuning Ratio
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	30	Vdc
Forward Current	I _F	200	mAdc
Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	225 1.8	mW mW/°C
Junction Temperature	TJ	+125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



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SOT-23 (TO-236) CASE 318 STYLE 8

MARKING DIAGRAM



M4C = Specific Device Code

M = Date Code*

= Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBV3102LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

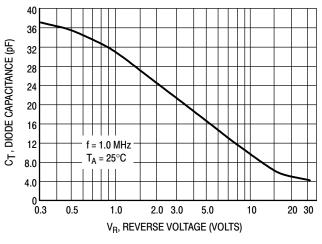
MMBV3102LT1G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage (I _R = 10 μAdc)	V _{(BR)R}	30	-	-	Vdc
Reverse Voltage Leakage Current (V _R = 25 Vdc, T _A = 25°C)	I _R	-	-	0.1	μAdc
Diode Capacitance Temperature Coefficient (V _R = 4.0 Vdc, f = 1.0 MHz)	TC _C	-	300	-	ppm/°C

	C _t , Diode Capacitance V _R = 3.0 Vdc, f = 1.0 MHz pF		Q, Figure of Merit V _R = 3.0 Vdc f = 50 MHz	C _R , Capacitance Ratio C ₃ /C ₂₅ f = 1.0 MHz		
Device	Min	Nom	Max	Min	Min	Тур
MMBV3102LT1	20	22	25	200	4.5	4.8

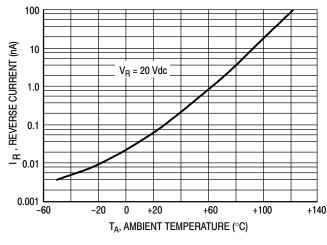
TYPICAL CHARACTERISTICS



T_A = 25°C f = 50 MHz Q, FIGURE OF MERIT (x 1000) 10 5.0 3.0 2.0 1.0 0.5 0.3 12 15 18 21 27 30 V_R, REVERSE VOLTAGE (VOLTS)

Figure 1. Diode Capacitance

Figure 2. Figure of Merit



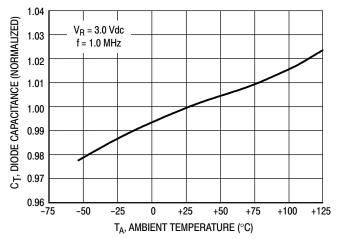


Figure 3. Leakage Current

Figure 4. Diode Capacitance

NOTES ON TESTING AND SPECIFICATIONS

1. C_{R} is the ratio of C_{T} measured at 3.0 Vdc divided by C_{T} measured at 25 Vdc.

MILLIMETERS

MIN

0.89

0.01

0.37

0.08

2.80

1.20

1.78

0.30

0.35

2.10

O°

NOM

1.00

0.06

0.44

0.14

2.90

1.30

1.90

0.43

0.54

2.40





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MAX

1.11

0.10

0.50

0.20

3.04

1.40

2.04

0.55

0.69

2.64

10°





DETAIL "A" Scale 3:1







NOTES:

DIM

Α

Α1

b

С

D

Ε

е L

L1

HE

Τ

- DIMENSIONING AND TOLERANCING 1. PER ASME Y14.5M, 2018. CONTROLLING DIMENSIONS:
- MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE
- BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package

RECOMMENDED MOUNTING FOOTPRINT

* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

SOT-23 (TO-236) 2.90x1.30x1.00 1.90P CASE 318 ISSUE AU

DATE 14 AUG 2024

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR			
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	2. CATHODE 2.	2: STYLE 13: CATHODE PIN 1. SOURCE CATHODE 2. DRAIN ANODE 3. GATE	STYLE 14: PIN 1. CATHODE 2. GATE 3. ANODE
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE	2. ANODE 2.	3: STYLE 19: NO CONNECTION PIN 1. CATHODE CATHODE 2. ANODE ANODE 3. CATHODE-ANODE	STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT			STYLE 26: PIN 1. CATHODE 2. ANODE 3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE			

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