Trench-based Schottky Rectifier

Features

- Fine Lithography Trench-based Schottky Technology for Very Low Forward Voltage and Low Leakage
- Fast Switching with Exceptional Temperature Stability
- Low Power Loss and Lower Operating Temperature
- Higher Efficiency for Achieving Regulatory Compliance
- High Surge Capability
- NRV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb-Free and Halide-Free Devices

Typical Applications

- Switching Power Supplies including Tablet Adapters, and Flat Panel
- High Frequency and DC-DC Converters
- Freewheeling and OR-ing diodes
- Reverse Battery Protection
- Instrumentation
- Automotive LED Lighting

Mechanical Characteristics:

- Case: Epoxy, Molded
- Epoxy Meets Flammability Rating UL 94-0 @ 0.125 in
- Lead Finish: 100% Matte Sn (Tin)
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Device Meets MSL 1 Requirements
- Weight: 95 mg (Approximately)



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TRENCH SCHOTTKY RECTIFIER 3.0 AMPERE 60 VOLTS



CASE 403AA STYLE 6

MARKING DIAGRAM



3M6 = Specific Device Code = Assembly Location Α

= Year WW = Work Week = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
NRTSAF360T3G	SMA-FL (Pb-Free)	5000 / Tape & Reel
NRVTSAF360T3G	SMA-FL (Pb-Free)	5000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

1

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	60	V
Average Rectified Forward Current (T _L = 143°C)	I _O	3.0	А
Peak Repetitive Forward Current (Rated V _R , Square Wave, 20 kHz) T _L = 138°C	IFRM	6.0	А
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	100	А
Storage Temperature Range	T _{stg}	-65 to +175	°C
Operating Junction Temperature (Note 1)	TJ	-55 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Lead (Note 2)	Ψ_{JCL}	24.5	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	Reja	81	°C/W

^{2. 1} inch square pad size $(1 \times 0.5 \text{ inch})$ for each lead on FR4 board.

ELECTRICAL CHARACTERISTICS

Rating	Тур	Max	Unit
Instantaneous Forward Voltage (Note 3) $ \begin{array}{c} (I_F=3~A,~T_J=25^{\circ}C)\\ (I_F=3~A,~T_J=125^{\circ}C)\\ (I_F=6~A,~T_J=25^{\circ}C) \end{array} $	0.48 0.41 0.57	0.64 0.58 0.78	V
Instantaneous Reverse Current (Note 3) (Rated dc Voltage, T_J = 25°C) (Rated dc Voltage, T_J = 125°C)	4.5 3.5	50 15	μ A mA

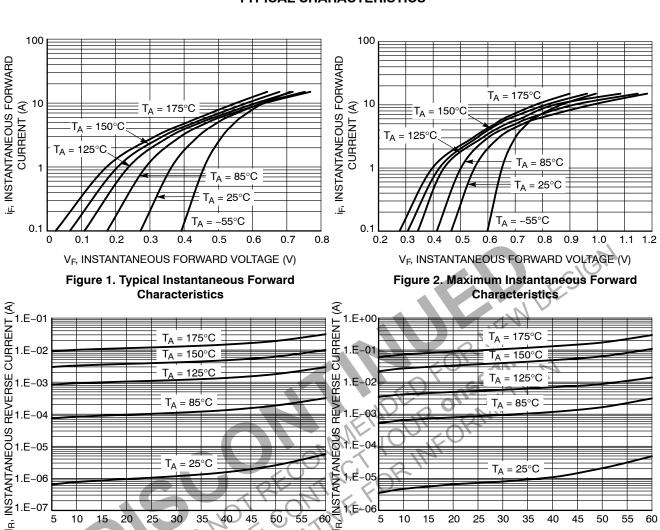
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse Test: Pulse Width ≤ 380 µs, Duty Cycle ≤ 2.0%

3. Pulse Test: Pulse Width ≤ 380 µs, Duty Cycle ≤ 2.0%

^{1.} The heat generated must be less than the thermal conductivity from Junction-to-Ambient: dP_D/dT_J < 1/R_{BJA}.

TYPICAL CHARACTERISTICS



V_R, INSTANTANEOUS REVERSE VOLTAGE (V) Figure 3. Typical Reverse Characteristics

30 35 40 45

50

5

20

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T_A = 25°C

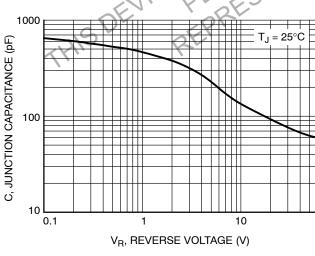


Figure 5. Typical Junction Capacitance



55 60

20 15

25 30

T_A = 25°C

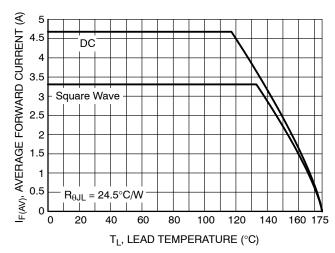


Figure 6. Current Derating

TYPICAL CHARACTERISTICS

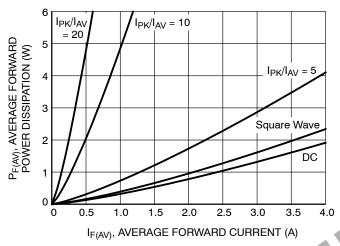
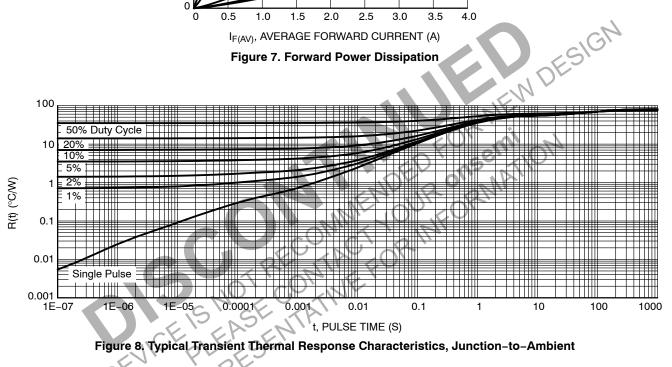


Figure 7. Forward Power Dissipation



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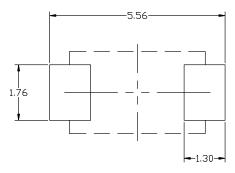
SMA 2.60x4.30x1.00 CASE 403AA ISSUE A

DATE 18 JAN 2024

NOTES:

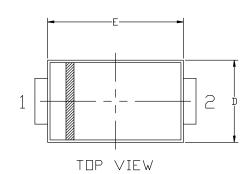
- DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M, 2018.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. FL

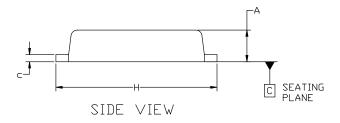
MILLIMETERS					
DIM	MIN	NDM	MAX		
Α	0.90	1.00	1.10		
b	1.25	1.45	1.65		
\subset	0.15	0.225	0.30		
D	2.40	2.60	2,80		
Ε	4.00	4.30	4.60		
Н	4.80	5.10	5.40		
L	0.70	0.90	1.10		

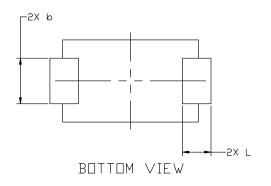


RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference manual, SDLDERRM/D.







GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code

A = Assembly Location

Y = Year

WW = Work Week

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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