# 1.5 A, 40 V Schottky Barrier Diode

These Schottky barrier diodes are optimized for low forward voltage drop and low leakage current and are offered in a Chip Scale Package (CSP) to reduce board space. The low thermal resistance enables designers to meet the challenging task of achieving higher efficiency and meeting reduced space requirements.

#### Features

- Low Forward Voltage Drop -510 mV (Typ.) @  $I_F = 1.5$  A
- Low Reverse Current 20  $\mu$ A (Typ.) @  $V_R = 40 \text{ V}$
- 1.5 A of Continuous Forward Current
- ESD Rating Human Body Model: Class 3B
  - Machine Model: Class C
- High Switching Speed
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

## **Typical Applications**

- LCD and Keypad Backlighting
- Camera Photo Flash
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping & Protection

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Reverse Voltage	$V_R$	40	V
Forward Current (DC)	lF	1.5	Α
Forward Surge Current (60 Hz @ 1 cycle)	I <sub>FSM</sub>	19	Α
Repetitive Peak Forward Current (Pulse Wave = 1 sec, Duty Cycle = 66%)	I <sub>FRM</sub>	3.5	А
ESD Rating: Human Body Model Machine Model	ESD	> 8 > 400	kV V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

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DSN2 (0603) CASE 152AT



PIN 1 6WM

6W = Specific Device Code M = Date Code

#### **PIN CONNECTIONS**



#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NSR15406NXT5G	DSN2 (Pb-Free)	5000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Min	Тур	Max	Unit
Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ T <sub>A</sub> = 25°C	R <sub>θJA</sub> P <sub>D</sub>			245 510	°C/W mW
Thermal Resistance Junction-to-Ambient (Note 2) Total Power Dissipation @ T <sub>A</sub> = 25°C	R <sub>θJA</sub> P <sub>D</sub>			90 1.4	°C/W W
Storage Temperature Range	T <sub>stg</sub>			-40 to +125	°C
Junction Temperature	TJ			+150	°C

- 1. Mounted onto a 4 in square FR-4 board 50 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.
- 2. Mounted onto a 4 in square FR-4 board 650 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.

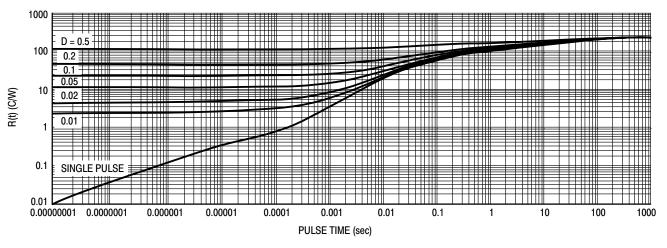


Figure 1. Thermal Response (Note 1)

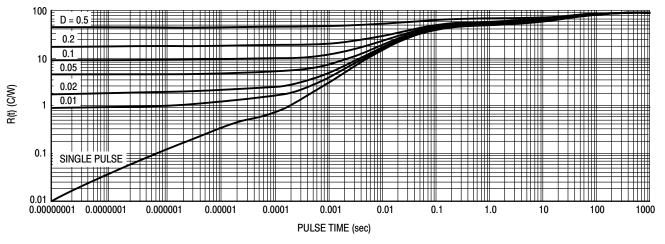
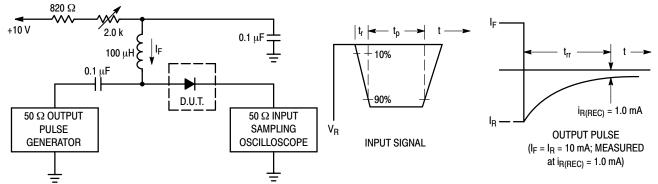


Figure 2. Thermal Response (Note 2)

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic		Min	Тур	Max	Unit
Reverse Leakage (V <sub>R</sub> = 10 V) (V <sub>R</sub> = 40 V)	I <sub>R</sub>	- -	2 20	10 75	μΑ
Forward Voltage (I <sub>F</sub> = 10 mA) (I <sub>F</sub> = 100 mA) (I <sub>F</sub> = 500 mA) (I <sub>F</sub> = 1 A) (I <sub>F</sub> = 1.5 A)	V <sub>F</sub>	- - - - -	260 325 395 450 510	300 370 440 490 560	mV
Total Capacitance (V <sub>R</sub> = 1.0 V, f = 1.0 MHz)	C <sub>T</sub>	_	140	-	pF
Reverse Recovery Time ( $I_F = I_R = 10 \text{ mA}$ , $I_{R(REC)} = 1.0 \text{ mA}$ , Figure 3)	t <sub>rr</sub>	-	53	-	ns
Peak Forward Recovery Voltage (I <sub>F</sub> = 100 mA, t <sub>r</sub> = 20 ns, Figure 4)	$V_{FRM}$	_	450	-	mV



- Notes: 1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current (I<sub>F</sub>) of 10 mA.
  - 2. Input pulse is adjusted so  $I_{R(peak)}$  is equal to 10 mA.
  - 3. t<sub>p</sub> » t<sub>rr</sub>

Figure 3. Recovery Time Equivalent Test Circuit

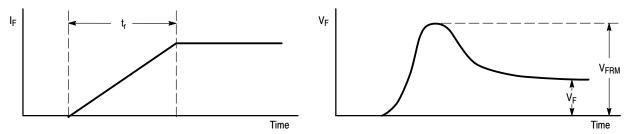
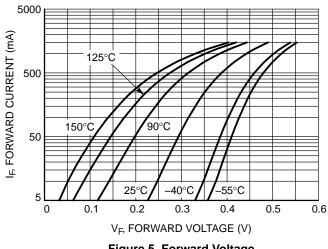


Figure 4. Peak Forward Recover Voltage Definition

### TYPICAL CHARACTERISTICS



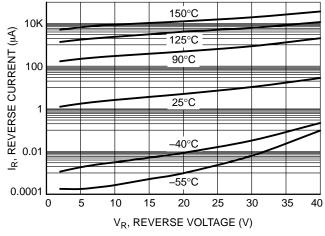
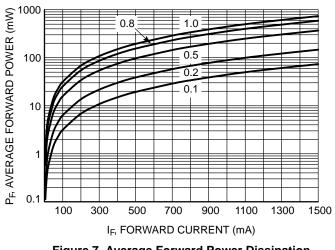


Figure 5. Forward Voltage

Figure 6. Leakage Current



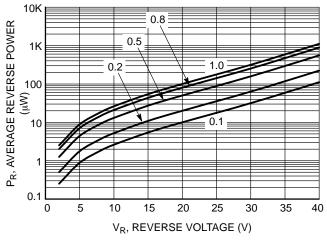
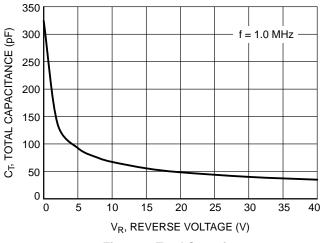
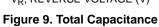


Figure 7. Average Forward Power Dissipation

Figure 8. Average Reverse Power Dissipation





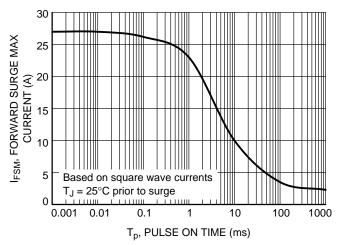


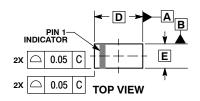
Figure 10. Forward Surge Maximum

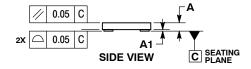


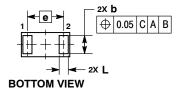


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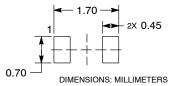
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#### **RECOMMENDED MOUNTING FOOTPRINT\***



See Application Note AND8464/D for more mounting details

#### NOTES:

- DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS.

	MILLIMETERS			
DIM	MIN	MAX		
Α	0.25	0.31		
A1	0.00	0.05		
b	0.55	0.65		
D	1.60 BSC			
Е	0.80 BSC			
е	1.20 BSC			
L	0.25	0.35		

### **GENERIC MARKING DIAGRAM1\***



XXXX = Specific Device Code YYY = Year Code

## **GENERIC MARKING DIAGRAM2\***

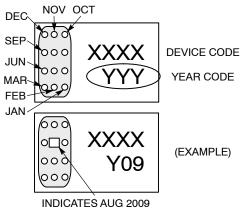


XX = Specific Device Code

M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

## **CATHODE BAND MONTH** CODING



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<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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