

# Electrical Rules Check I

Class	Document

# Design Rules Verification

Filename : C:\Users\Public\Doc

Warnings
Total

Rule Violations
Clearance Constraint (Gap=5mil) (InCom
Clearance Constraint (Gap=6mil) (All),(A
Clearance Constraint (Gap=3mil) (InCom
Clearance Constraint (Gap=6mil) (InCom
Short-Circuit Constraint (Allowed=No) (A
Un-Routed Net Constraint ( (InNetClass('
Modified Polygon (Allow modified: No), (A
Width Constraint (Min=5 mil) (Min=20 mil)