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User Guide for

FEBFLS0116_L32U003A

2.7 W LED Driver at Universal Line

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FLS0116

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Table of Contents

This user guide supports the evaluation kit for the FLS0116. It should be used in conjunction with the FLS0116 datasheet as well as Fairchild's application notes and technical support team. Please visit Fairchild's website at www.fairchildsemi.com.

1. Introduction

This document describes the proposed solution for an universal input, 2.7 W LED ballast using the FLS0116. The input voltage range is 90 V_{RMS} – 265 V_{RMS} and there is one DC output with a constant current of 97 mA at 28 V_{MAX}. This document contains general description of FLS0116, the power supply specification, schematic, bill of materials and the typical operating characteristics.

1.1. General Description

The FLS0116 LED lamp driver is a simple IC with PFC function and integrated switching MOSFET. The special "adopted digital" technique automatically detects input voltage condition and sends an internal reference signal, resulting in high power factor (PF). When AC input voltage is applied to the IC, PFC function is automatically enabled. When DC input voltage is applied to the IC, PFC function is automatically disabled. The FLS0116 does not require a bulk capacitor (electrolytic capacitor) for supply rail stability, which can significantly improve LED reliability.

1.2. Key Features

- Built-in MOSFET (1 A / 550 V)
- Digitally Implemented Active PFC Function
  (No Additional Circuit Necessary for High PF)
- Built-in HV Supplying Circuit: Self Biasing
- AOCP Function with Auto-Restart Mode
- Built-in Over-Temperature Protection (OTP)
- Cycle-by-Cycle Current Limit
- Low Operating Current: 0.85 mA (Typical)
- Under-Voltage Lockout with 5 V Hysteresis
- Programmable Oscillation Frequency
- Programmable LED Current
- Analog Dimming Function
- Soft-Start Function
- Precise Internal Reference: ±3%

1.3. Internal Block Diagram

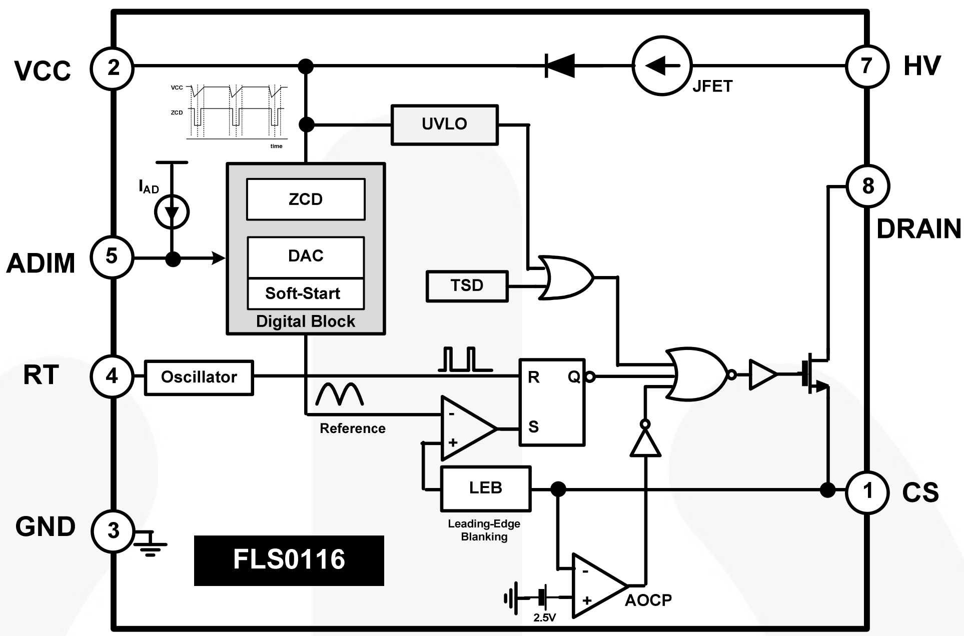


Figure 1. Internal Block Diagram

Pin No.	Symbol	Description
1	CS	**Current Sense**. Limits output current, depending on the sensing resistor voltage. The CS pin is also used to set the LED current regulation.
2	VCC	**VCC**. Supply pin for stable IC operation ZCD signal detection used for accurate PFC function.
3	GND	**GROUND**. Ground for the IC.
4	RT	**RT**. Programmable operating frequency using an external resistor. The IC has a fixed frequency when this pin is open or floating.
5	ADIM	**Analog Dimming**. Connects to the internal current source and can change the output current using an external resistor. If ADIM is not used, connect a 0.1 µF bypass capacitor between ADIM and GND.
6	NC	No Connection.
7	HV	**High Voltage**. Connects to the high-voltage line and supplies current to the IC.
8	DRAIN	**High Voltage**. Internal switching FET drain pin.

2. Specifications for Evaluation Board

All data for this table was measured at an ambient temperature of 25°C.

Table 1. Summary of Features and Performance

Description	Symbol	Value	Comments
Input Voltage Range	$V_{IN.MIN}$	90 V	Minimum Input Voltage
	$V_{IN.NORMAL}$	110 V / 220 V	Normal Input Voltage
	$V_{IN.MAX}$	265 V	Maximum Input Voltage
AC Input Frequency	$Freq_{IN.MIN}$	47 Hz	Minimum Input Frequency
	$Freq_{IN.MAX}$	64 Hz	Maximum Input Frequency
Output Voltage	$V_{OUT,MAX}$	30 V	Maximum Output Voltage
	$V_{OUT,NORMAL}$	28 V	Normal Output Voltage
	$V_{OUT,MIN}$	26 V	Minimum Output Voltage
Output Current(1)	$I_{OUT.NORMAL}$	97 mA	Normal Output Current
	CC Deviation	< ±1.3%	Line Input Voltage Change: 90~265 V_{AC}
Output Power(2)	Output Power	2.7 W	
Efficiency		>78%	At Full Load
Temperature	$T_{FLS0116}$	< 73°C	At Full Load (all at open-frame, room temperature / still air)
	$T_{DM\ filter}$	< 44°C	
	$T_{FRD,UF4007}$	< 47°C	
	$T_{CS\ resistor}$	< 59°C	
	$T_{inductor}$	< 66°C	
PCB Size			20 mm (width) x 30 mm (length)x 18 mm (height)
Initial Application			LED Bulb

Notes:

1. The output current has I_{LEDPK} ripple. To reduce ripple current, use a large electrolytic capacitor in parallel with the LED. Ensure the capacitor voltage rating is high enough to withstand an open-LED condition or use a Zener diode for protection.
2. The output power is not equal to the apparent power due to the slight phase shift between the output voltage and current.

3. Photographs

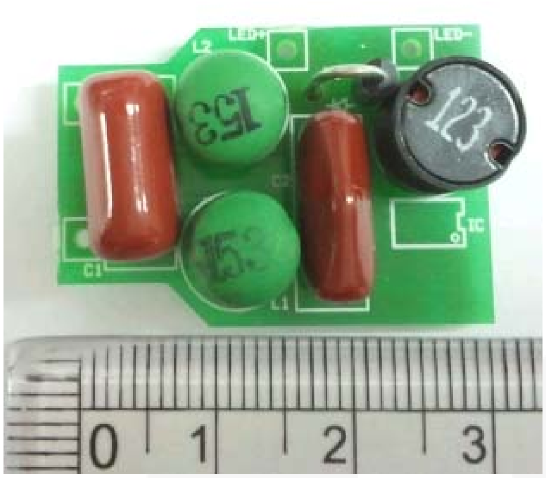


Figure 2. Top View (20 mm x 30 mm)

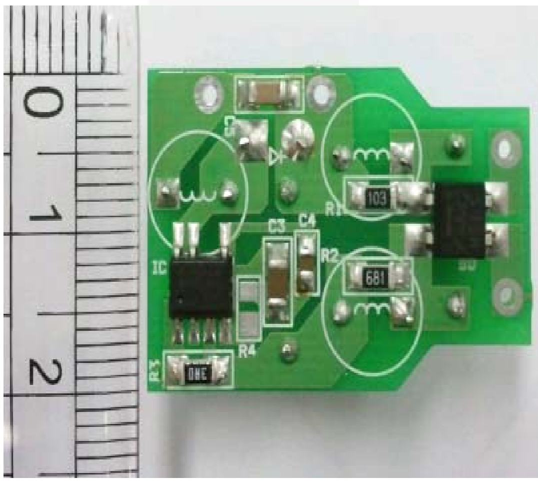


Figure 3. Bottom View (20 mm x 30 mm)

Figure 4. Side View (18 mm)

4. Printed Circuit Board

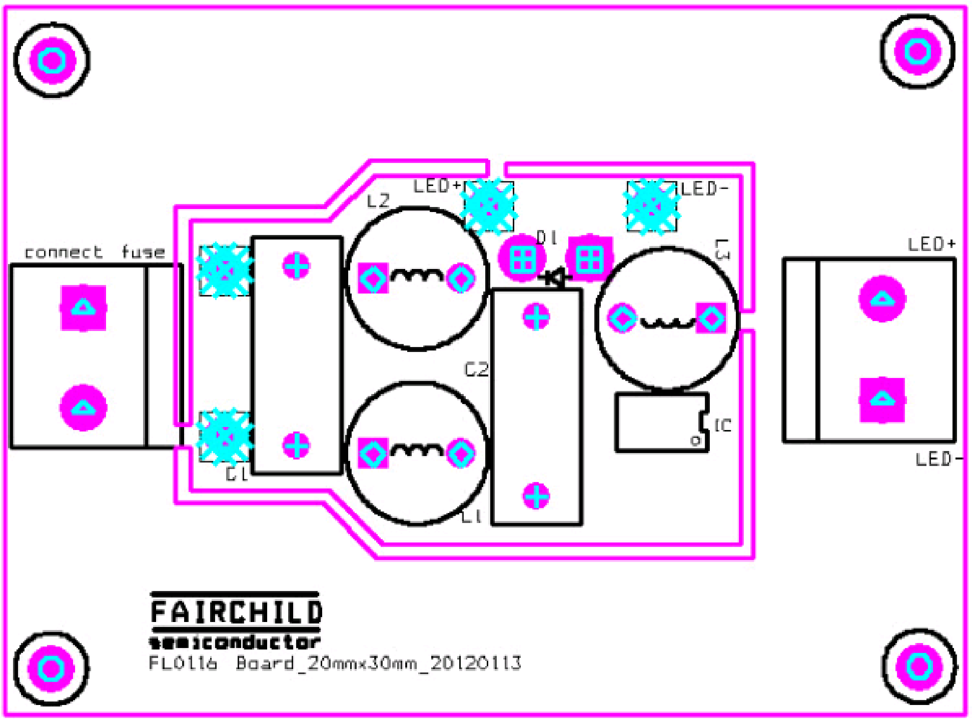


Figure 5. Top Side

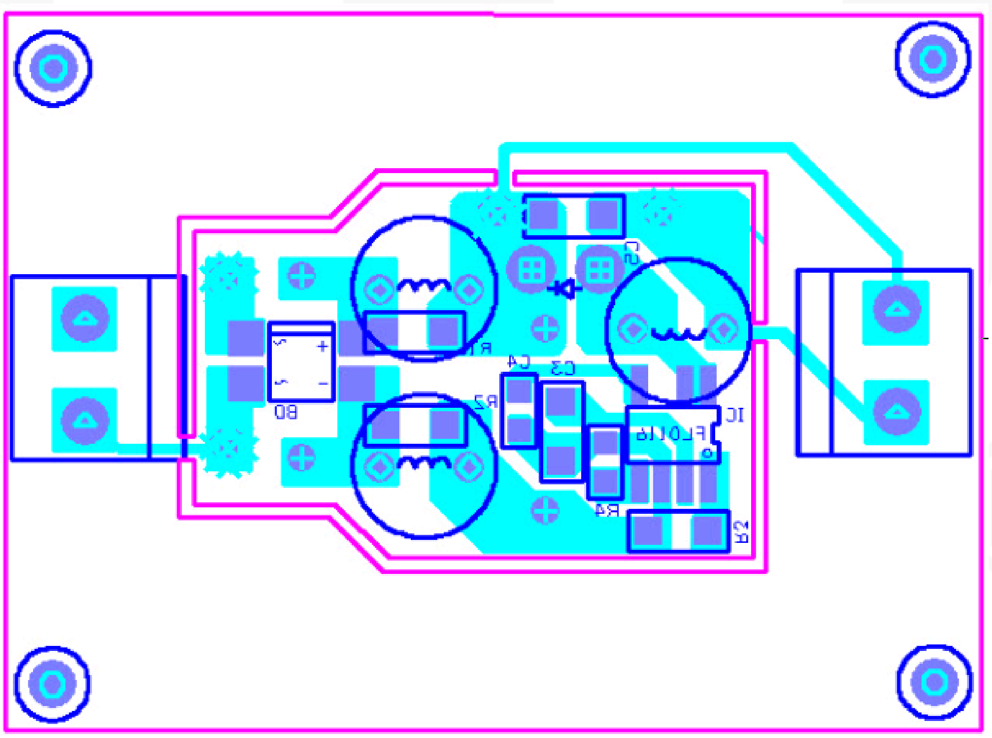


Figure 6. Bottom Side

5. Schematic

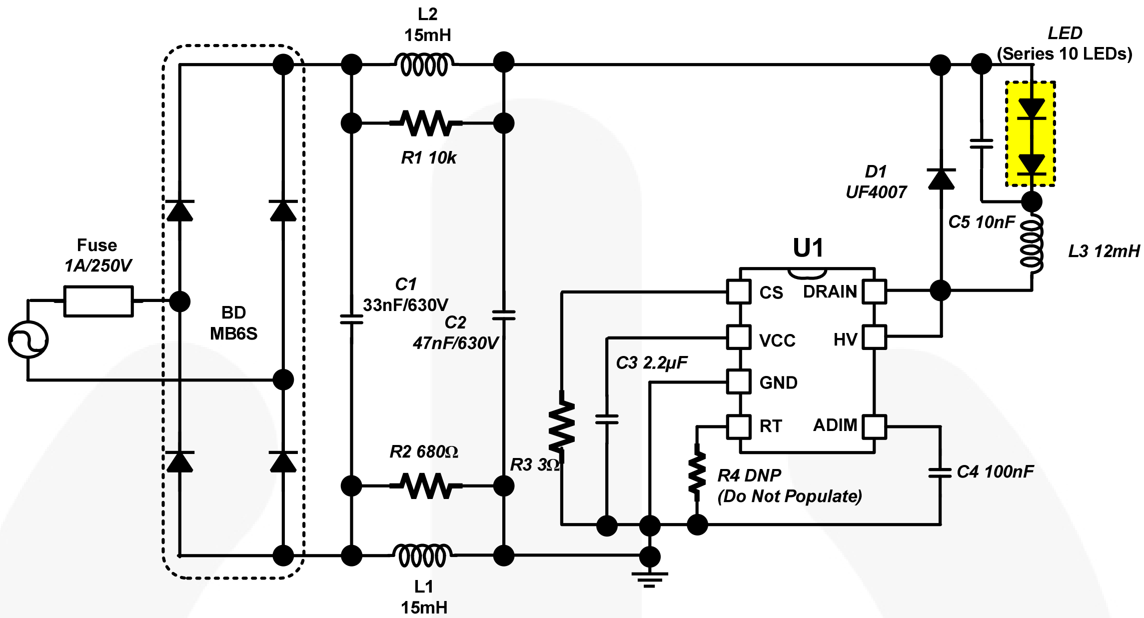


Figure 7. Evaluation Board Schematic

6. Bill Of Materials

Item No.	Part Reference	Part Number	Qty.	Description	Manufacturer
1	U1	FLS0116M	1	Controller	Fairchild Semiconductor
2	BD	MB6S	1	0.5 A / 600 V, Bridge Diode	Fairchild Semiconductor
3	C1	MPE 630V333K	1	33 nF / 630 V_{AC}, Film Capacitor	Sungho
4	C2	MPE 630V473K	1	47 nF / 630 V_{AC}, Film Capacitor	Sungho
5	C3	C1206C225K3PACTU	1	2.2 µF / 25 V SMD Capacitor 3216	Kemet
6	C4	C0805C104K3RACTU	1	0.1 µF / 25 V SMD Capacitor 2012	Kemet
7	C5	C1206C103KDRACTU	1	10 nF / 630 V SMD Capacitor 3216	Kemet
8	D1	UF4007	1	1 A / 1000 V, Ultra-Fast Recovery	Fairchild Semiconductor
9	L1,L2	R06153KT00	2	15 mH, Filter Inductor	Bosung
10	L3	RFB0810-123L	1	12mH, Inductor	Coil Craft
11	R1	RC1206JR-07103RL	1	10 kΩ, SMD Resistor 3216	Yageo
12	R2	RC1206JR-07680RL	1	680 Ω, SMD Resistor 3216	Yageo
13	R3	RC1206JR-073RL	1	3 Ω, SMD Resistor 3216	Yageo
-	R4	-	0	Open	

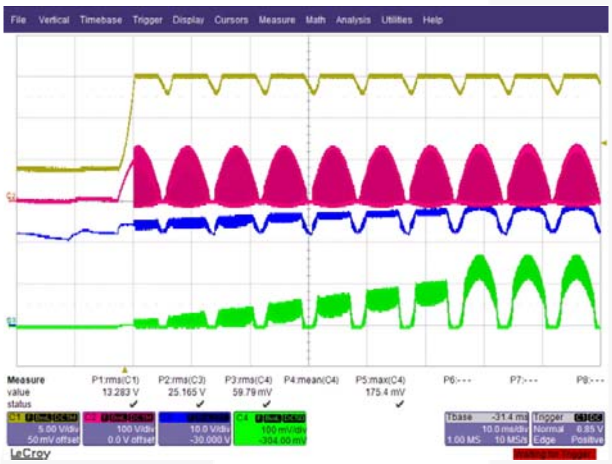
7. Performance of Evaluation Board

Table 2. Test Condition & Equipments

Test Temperature	T_A = 25°C
Test Equipment	AC Source : PCR500L by Kikusui Power Meter : PZ4000 by Yokogawa Oscilloscope : Waverunner 64Xi by LeCroy EMI Test Receiver: ESCS30 by ROHDE & SCHWARZ Two-Line V-Network: ENV216 by ROHDE & SCHWARZ Thermometer : CAM SC640 by FLIR SYSTEMS LED: EHP-AX08EL/GT01H-P03 (3 W) by Everlight

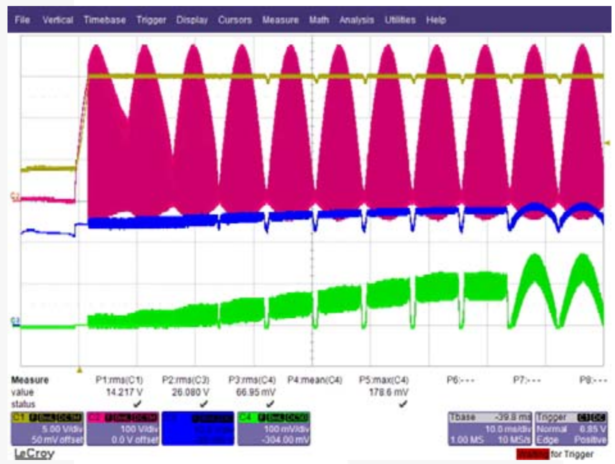
7.1. Typical Waveforms: Startup

Figure 8 through Figure 11 show the typical startup performance at different input voltage conditions. When AC input voltage is applied to the system, the FLS0116 automatically operates in AC Mode after finishing an internally fixed, seven-cycle, soft-start period. Figure 10 and Figure 11 show the soft-start characteristics when a DC input voltage is applied.



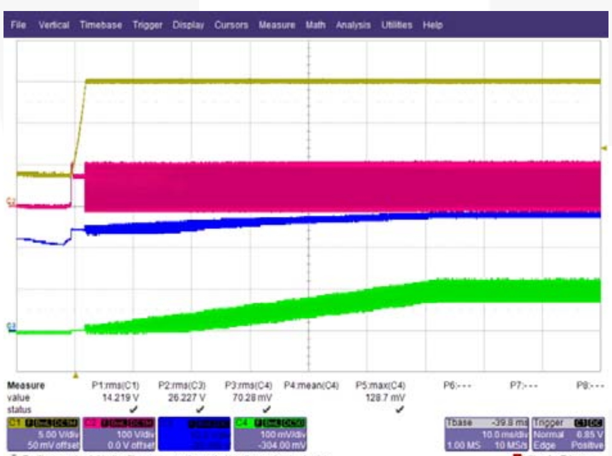
CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 8. Soft-Start, AC Mode, 90 V_{AC}



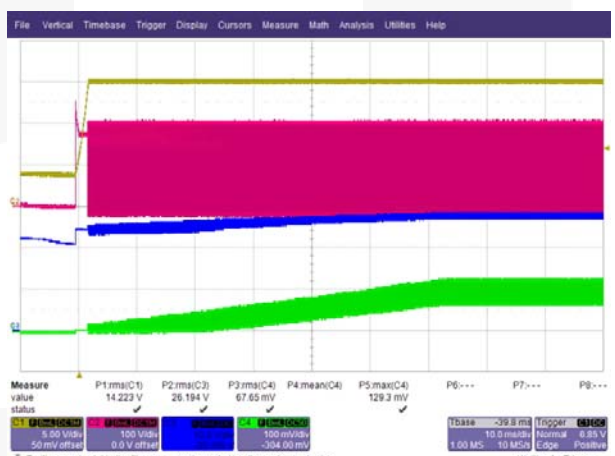
CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 9. Soft-Start, AC Mode, 265 V_{AC}



CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 10. Soft-Start, DC Mode, 100 V_{DC}



CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 11. Soft-Start, DC Mode, 200 V_{DC}

7.2. Operating Frequency & Minimum Duty

The programmable switching frequency is between 20 kHz ~ 250 kHz, determined by selecting the RT resistor value. If no RT resistor is used (RT pin OPEN), the FLS0116 default switching frequency is set to 45 kHz. The maximum duty ratio is fixed below 50% and has a fixed minimum typical on-time of 400 ns. There are two crucial points to design properly. The first is consideration of the minimum duty ratio at minimum input voltage because the FLS0116 is limited to 50% duty ratio. The second consideration is minimum on-time at maximum input voltage condition. The FLS0116 cannot control output power when the operating conditions are such that the required on-time is less than the 400 ns minimum on-time.

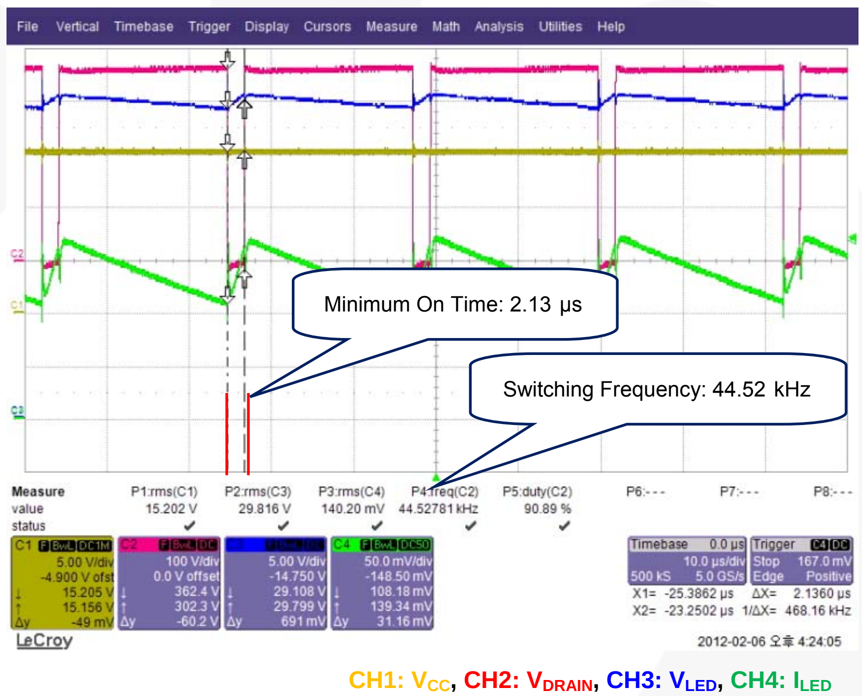
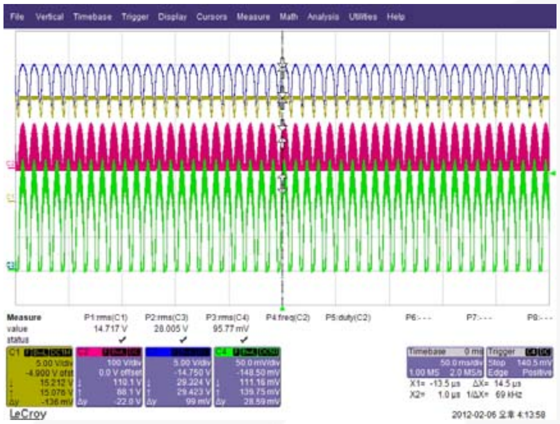


Figure 12. Operating Frequency & Minimum Duty Ratio

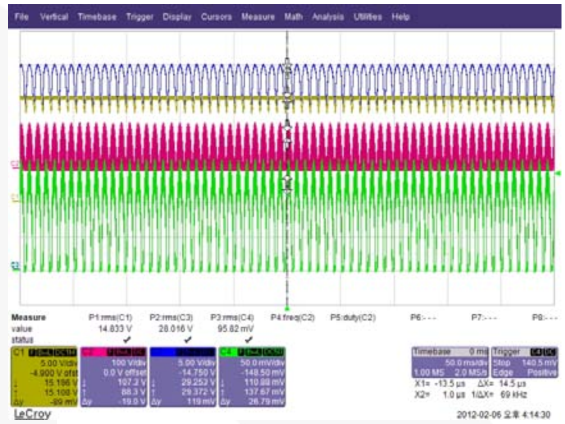
7.3. Typical Waveforms: Steady State

Figure 13 through Figure 22 show the normal operation waveform by input voltage and input frequency. The output voltage and current maintains a certain output level with 120 Hz ripple, as shown in the test results in Table 3.



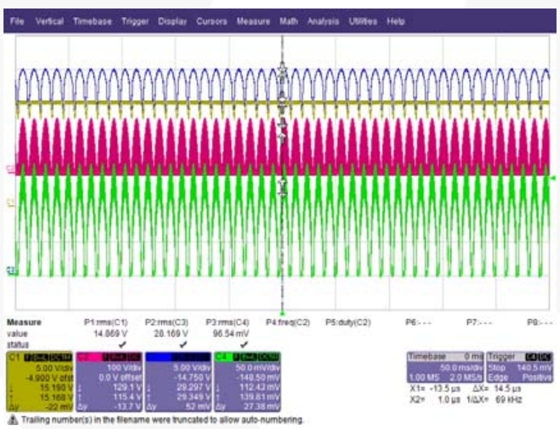
CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 13. Input Voltage: 90 V_{AC} / 47 Hz



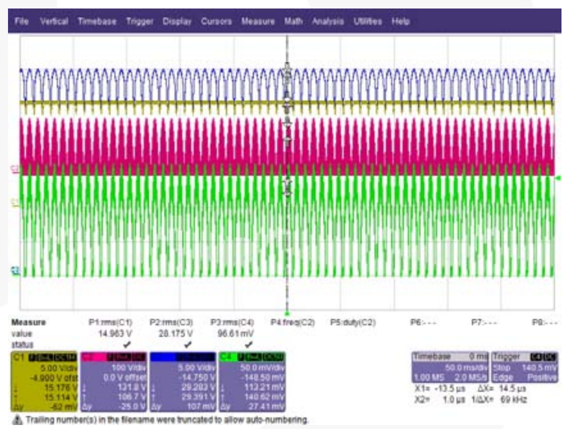
CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 14. Input Voltage: 90 V_{AC} / 64 Hz



CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 15. Input Voltage: 110 V_{AC} / 47 Hz



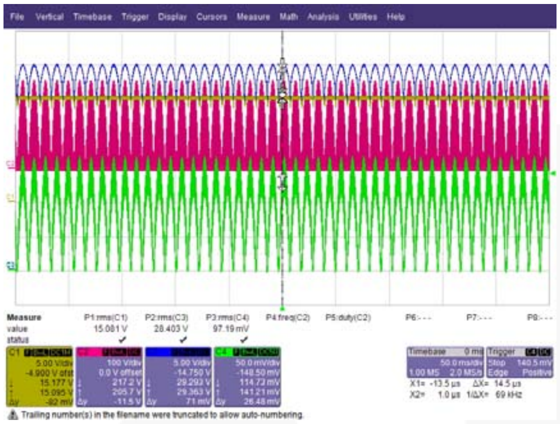
CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 16. Input Voltage: 110 V_{AC} / 64 Hz

Table 3. Output Characteristics by Input Voltage and Frequency

	47 Hz		64 Hz	
	$V_{LED(RMS)}$	$I_{LED(RMS)}$	$V_{LED(RMS)}$	$I_{LED(RMS)}$
90 V_{AC}	28.01 V	95.77 mA	28.02 V	95.82 mA
110 V_{AC}	28.17 V	96.54 mA	28.17 V	96.61 mA
180 V_{AC}	28.40 V	97.19 mA	28.37 V	97.11 mA
220 V_{AC}	28.41 V	97.60 mA	28.41 V	97.56 mA
265 V_{AC}	28.43 V	98.25 mA	28.43 V	98.23 mA

7.4. Typical Operating Waveforms: Output Characteristics

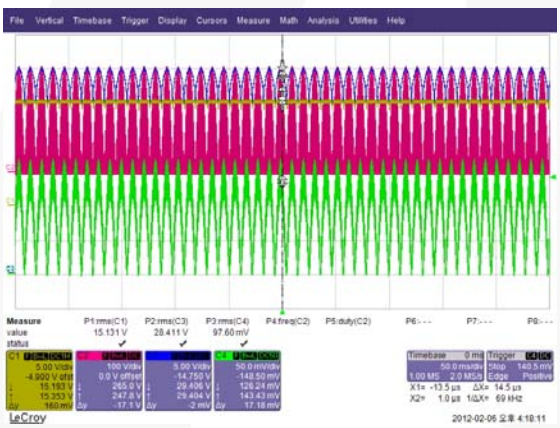


CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 17. Input Voltage: 180 V_{AC} / 47 Hz

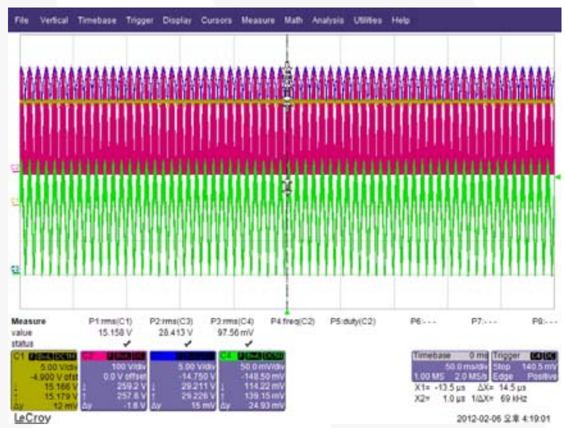
CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 18. Input Voltage: 180 V_{AC} / 64 Hz



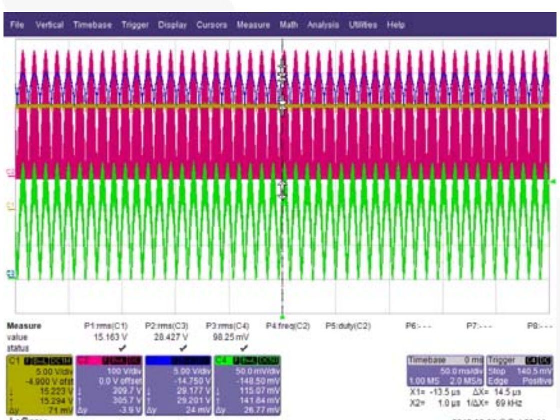
CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 19. Input Voltage: 220 V_{AC} / 47 Hz



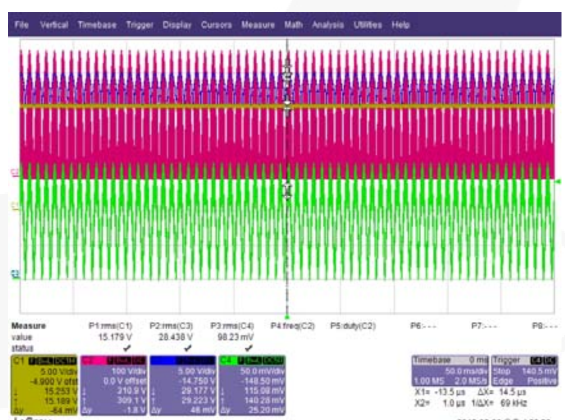
CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 20. Input Voltage: 220 V_{AC} / 64 Hz



CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 21. Input Voltage: 265 V_{AC} / 47 Hz



CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 22. Input Voltage: 265 V_{AC} / 64 Hz

7.5. Typical Waveforms: Abnormal Mode (LED Open)

Figure 23 and Figure 24 show the open-load condition test method and result. When the LED disconnects from the system, the IC cannot operate because the HV pin is disconnected.

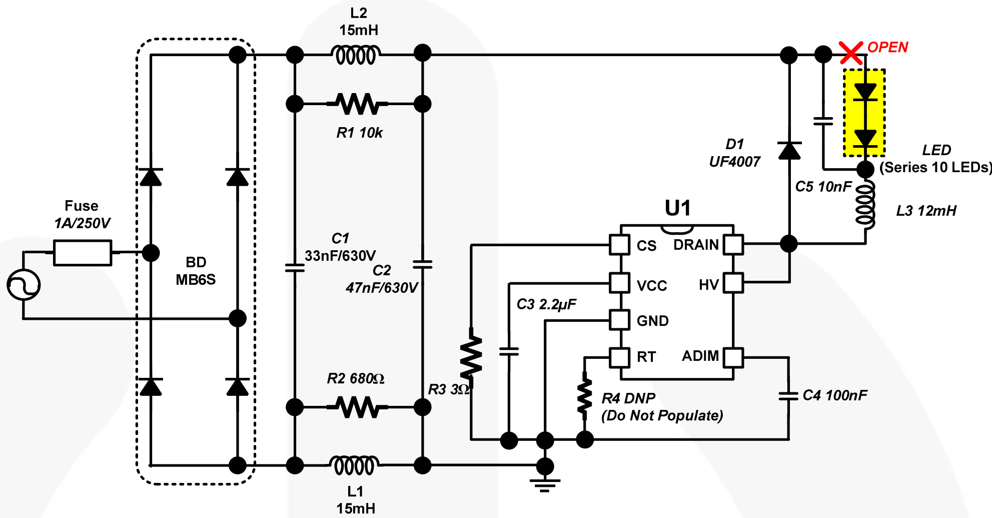
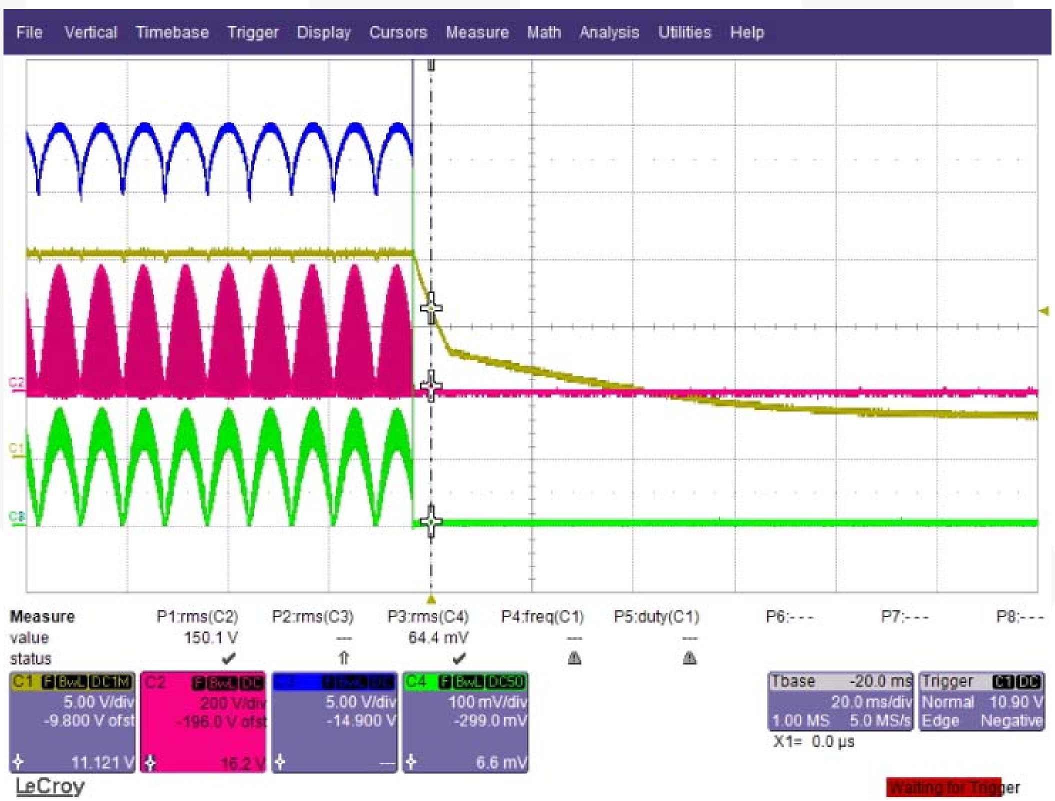


Figure 23. Open-Load Condition Test



CH1: V_{CC}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 24. Test Results of Open-Load Condition

7.6. Typical Waveforms: Abnormal Mode (Inductor Short)

Figure 25 and Figure 26 show the test method and result of an inductor short. The FLS0116 uses an abnormal over-current protection (AOCP) function, limiting the current on RCS in the event of an inductor short.

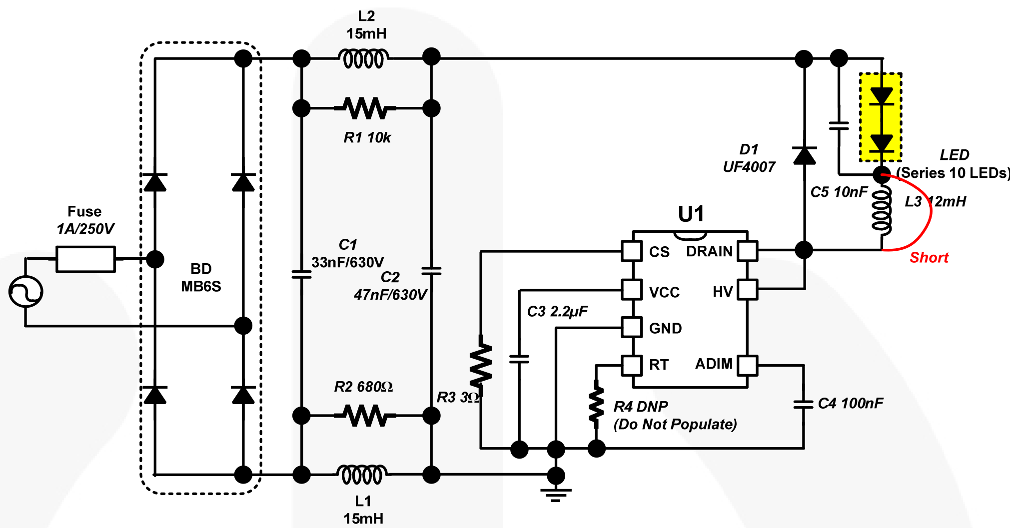
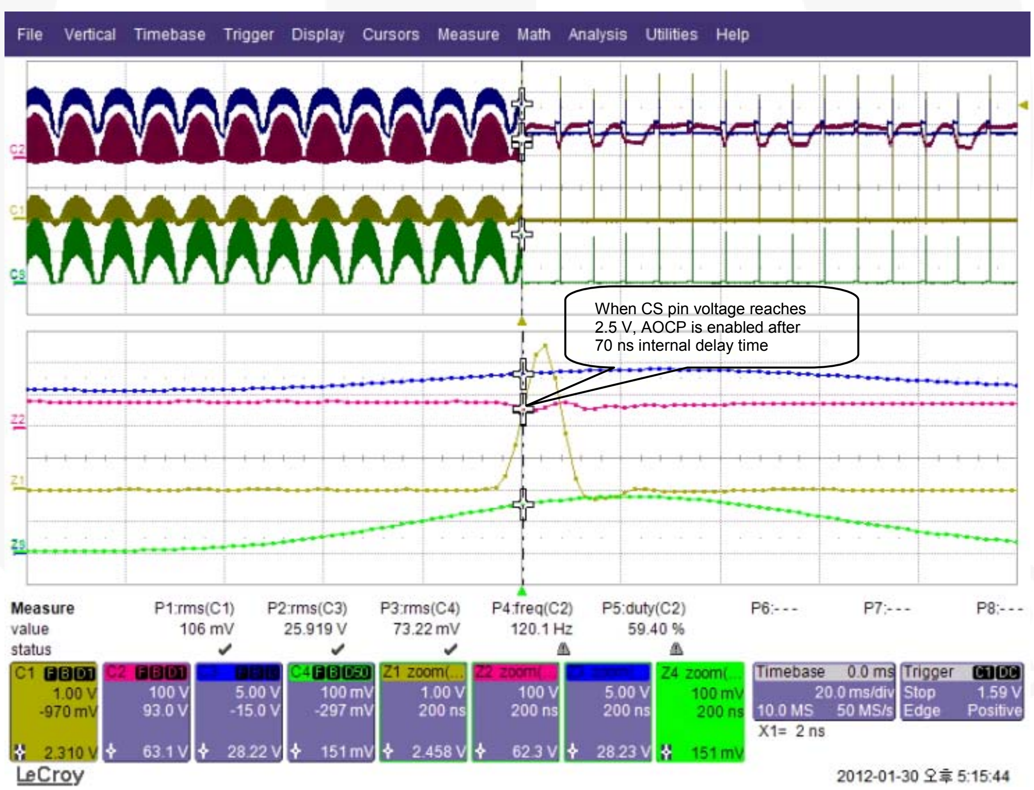


Figure 25. Inductor-Short Condition



CH1: V_{CS}, CH2: V_{DRAIN}, CH3: V_{LED}, CH4: I_{LED}

Figure 26. Test Results of Inductor Short Condition

7.7. System Efficiency

Figure 27 shows system efficiency results for different AC input voltage frequency conditions. As shown, the input frequency has negligible effect on system efficiency.

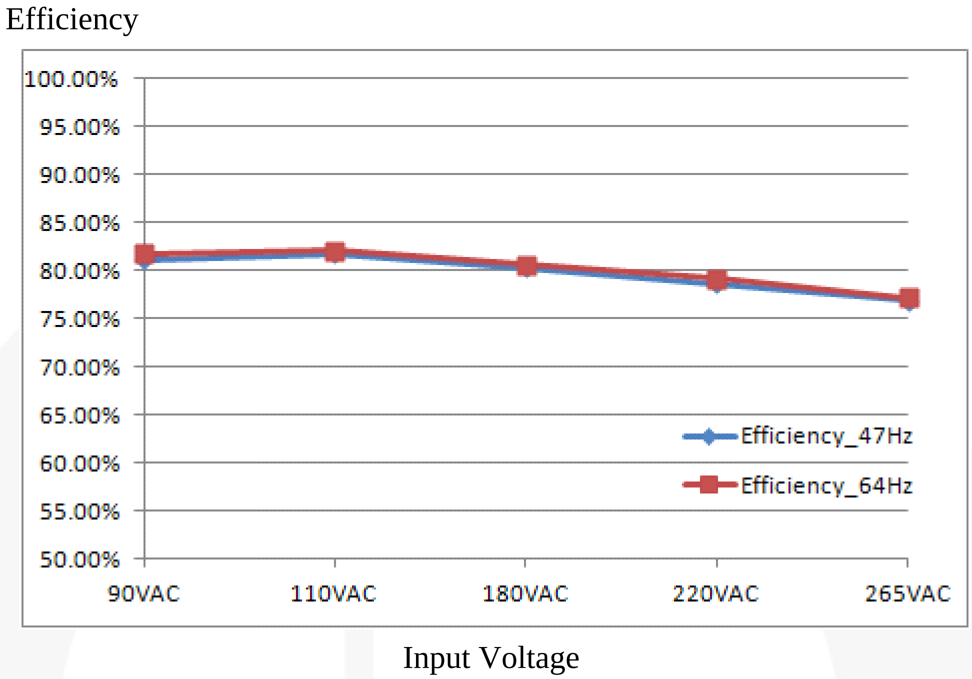


Figure 27. System Efficiency

Table 4. Efficiency Test Results

Input Voltage	Frequency	Efficiency (%)
90 V_{AC}	47 Hz	81.12
	64 Hz	81.73
110 V_{AC}	47 Hz	81.72
	64 Hz	82.08
180 V_{AC}	47 Hz	80.26
	64 Hz	82.57
220 V_{AC}	47 Hz	78.64
	64 Hz	79.12
265 V_{AC}	47 Hz	76.84
	64 Hz	77.14

7.8. Power Factor (PF) at Rated Load Condition

Figure 28 shows the system Power Factor (PF) performance for the entire input voltage range (90 V_{AC} to 265 V_{AC}) at different input frequency conditions (47 Hz, 64 Hz). The PF changes slightly according to the input frequency, but can achieve over 0.85 at 265 V_{AC} condition.

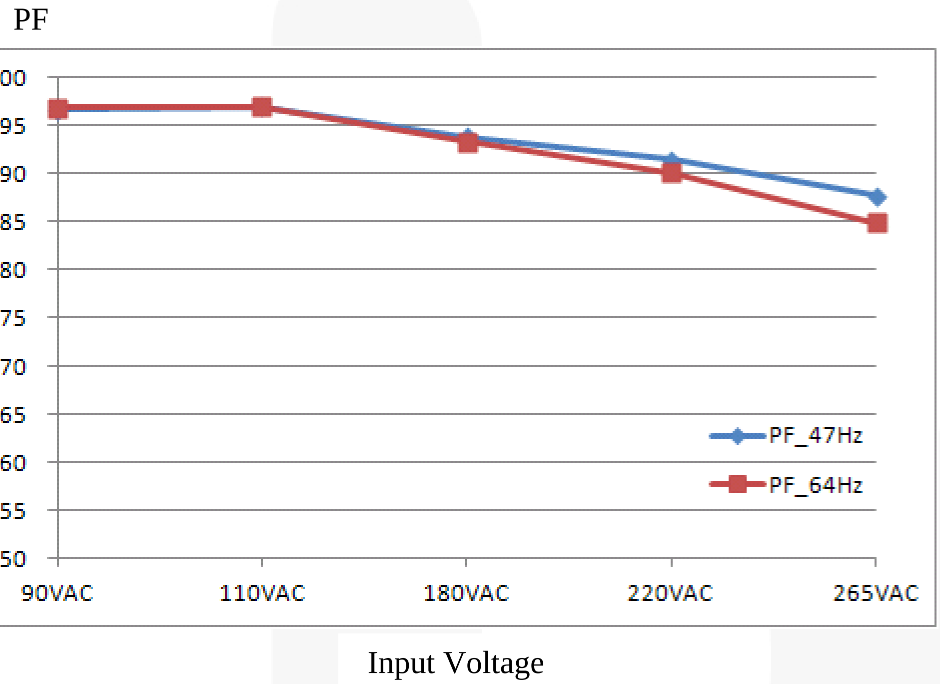


Figure 28. Power Factor

Table 5. Power Factor Test Results

Input Voltage		Power Factor
90 V_{AC}	47 Hz	0.97
	64 Hz	0.97
110 V_{AC}	47 Hz	0.97
	64 Hz	0.97
180 V_{AC}	47 Hz	0.94
	64 Hz	0.94
220 V_{AC}	47Hz	0.91
	64 Hz	0.90
265 V_{AC}	47 Hz	0.88
	64 Hz	0.85

7.9. Total Harmonic Distortion (THD)

Figure 29 shows the Total Harmonic Distortion (THD) performance at different input frequencies. Test results are quite similar, except the 90 V_{AC} condition, but meet international regulations (under 30%).

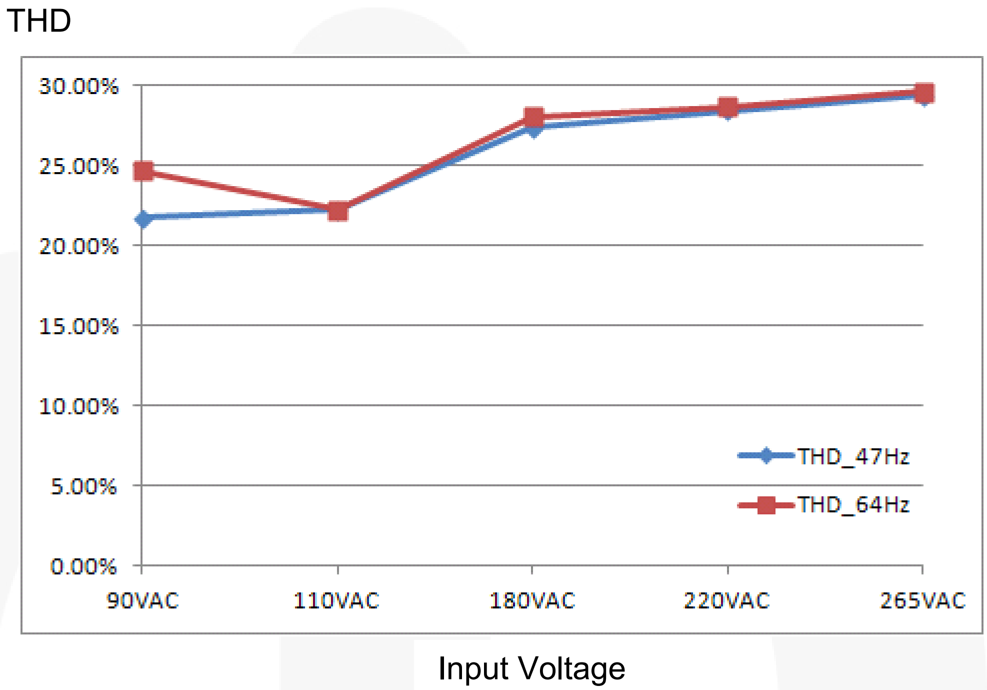


Figure 29. Total Harmonic Distortion Performance

Table 6. Total Harmonic Distortion Test Results

Input Voltage	Frequency	THD (%)
90 V_{AC}	47 Hz	21.74
	64 Hz	24.70
110 V_{AC}	47 Hz	22.24
	64 Hz	22.23
180 V_{AC}	47 Hz	27.38
	64 Hz	28.09
220 V_{AC}	47 Hz	28.46
	64 Hz	28.72
265 V_{AC}	47 Hz	29.37
	64 Hz	29.64

7.10. Operating Temperature

Figure 30 through Figure 37 show the steady-state thermal test results with different input voltage conditions. Inductor L3 has the highest temperature on the top side of the PCB due to copper resistance. The FLS0116 has the highest temperature on the bottom side of the PCB due to power loss associated with the high-voltage device. The IC temperature is 67.1°C for the 220 V_{AC} input condition.

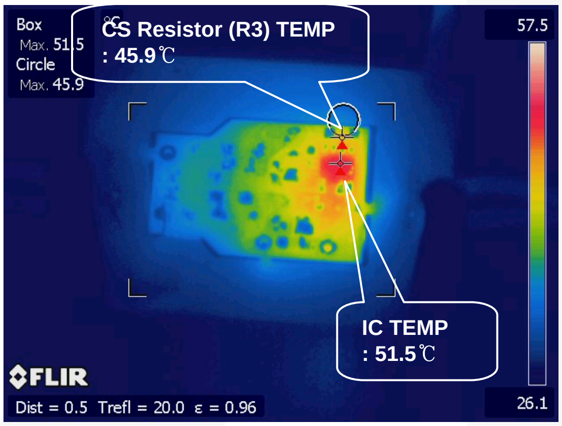


Figure 30. Bottom-Side Temperature at 90 V_{AC} Condition (IC)

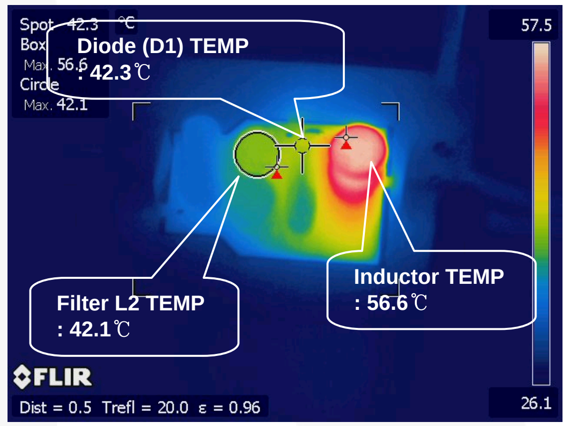


Figure 31. Top-Side Temperature at 90 V_{AC} Condition (Inductor)

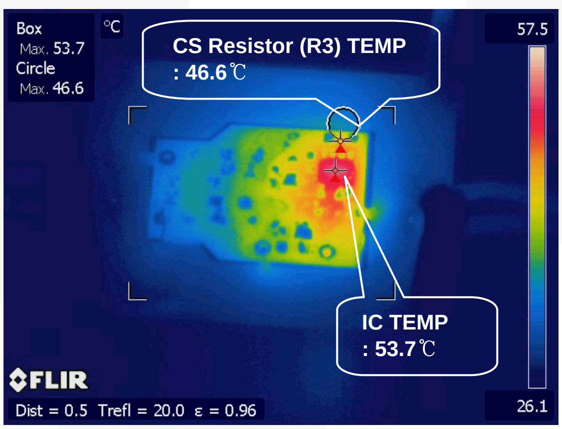


Figure 32. Bottom-Side Temperature at 110 V_{AC} Condition (IC)

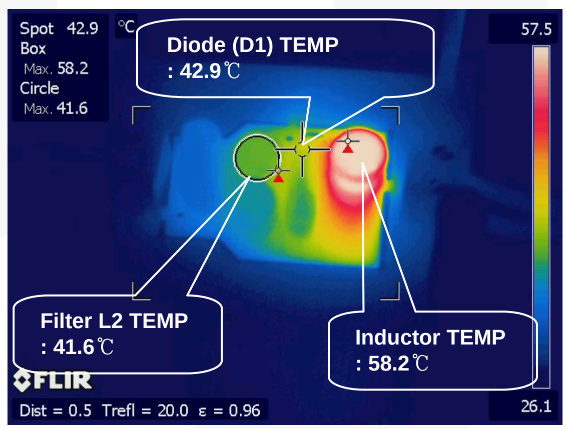


Figure 33. Top-Side Temperature at 110 V_{AC} Condition (Inductor)

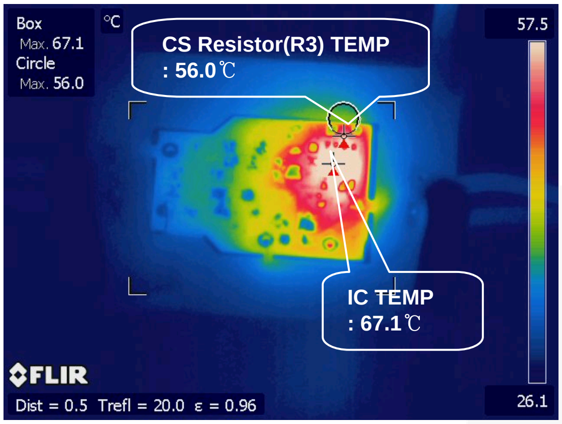


Figure 34. Bottom-Side Temperature at 220 V_{AC} Condition (IC)

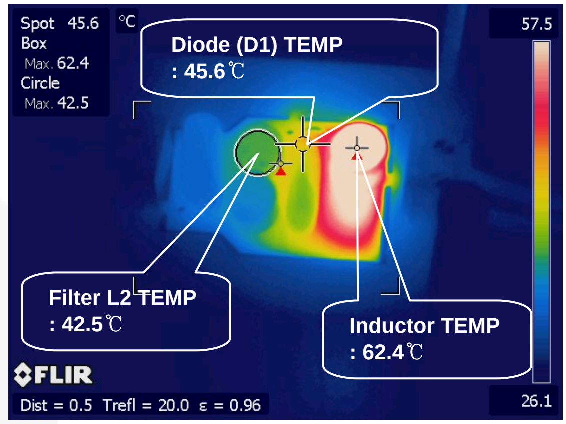


Figure 35. Top-Side Temperature at 220 V_{AC} Condition (Inductor)

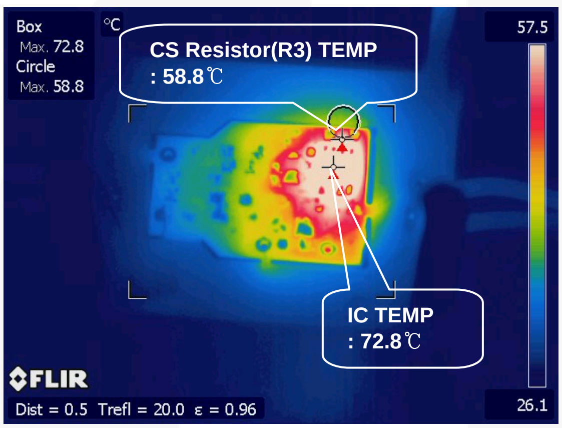


Figure 36. Bottom-Side Temperature at 264 V_{AC} Condition (IC)

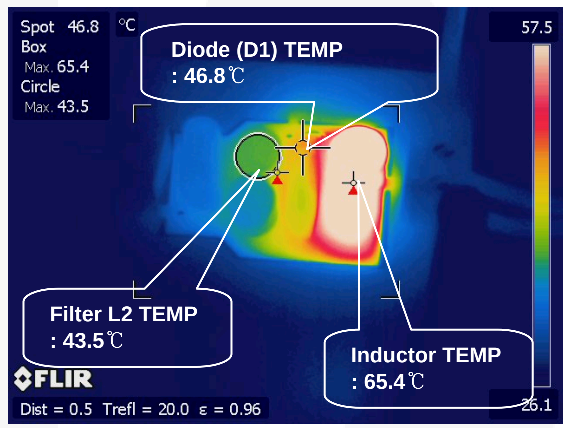


Figure 37. Top-Side Temperature at 264 V_{AC} Condition (Inductor)

Table 7. Temperature Performance by Input Voltage

Input Voltage	T_{IC}	$T_{INDUCTOR}$
90 V_{AC}	51.5°C	56.6°C
110 V_{AC}	53.7°C	58.2°C
220 V_{AC}	67.1°C	62.4°C
265 V_{AC}	72.8°C	65.4°C

7.11. Electromagnetic Interference (EMI)

EMI test measurements were conducted in observance of CISPR22 criteria, which has stricter limits than CISPR15 for lighting applications.

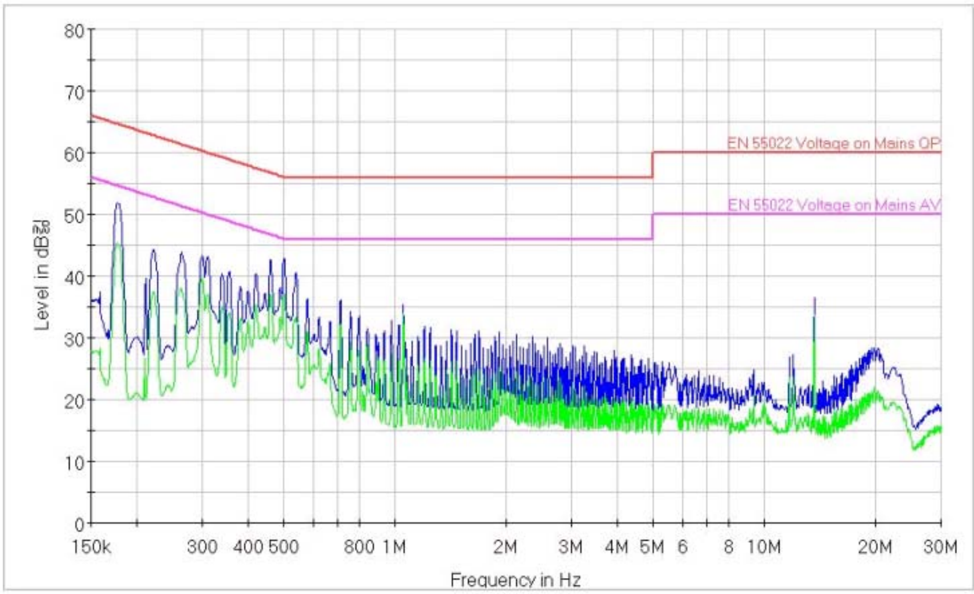


Figure 38. Conducted Emission-Line at 110 V_{AC} Input Condition, Full Load (10-LED Series)

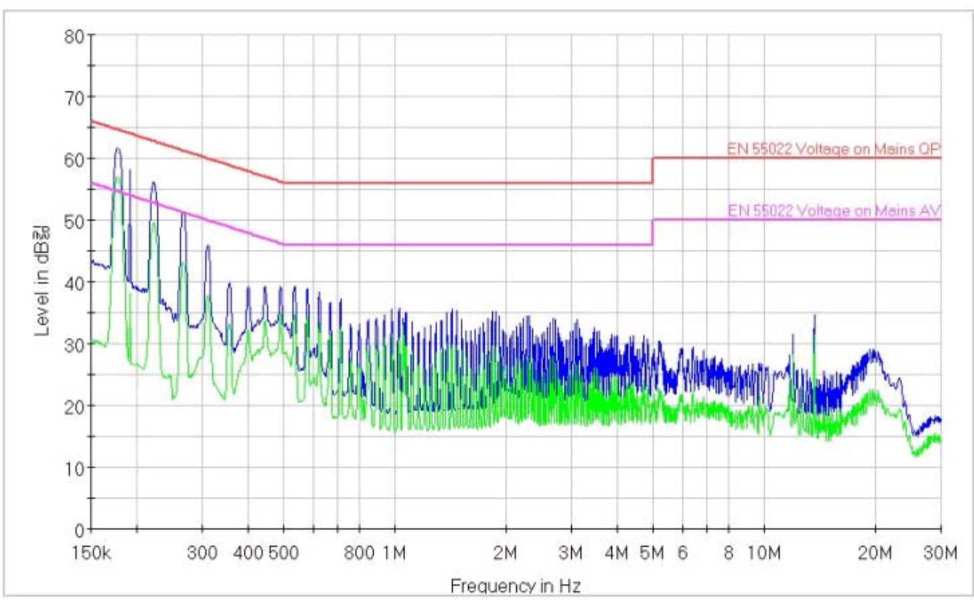


Figure 39. Conducted Emission-Line at 220 V_{AC} Input Condition, Full Load (10-LED Series)

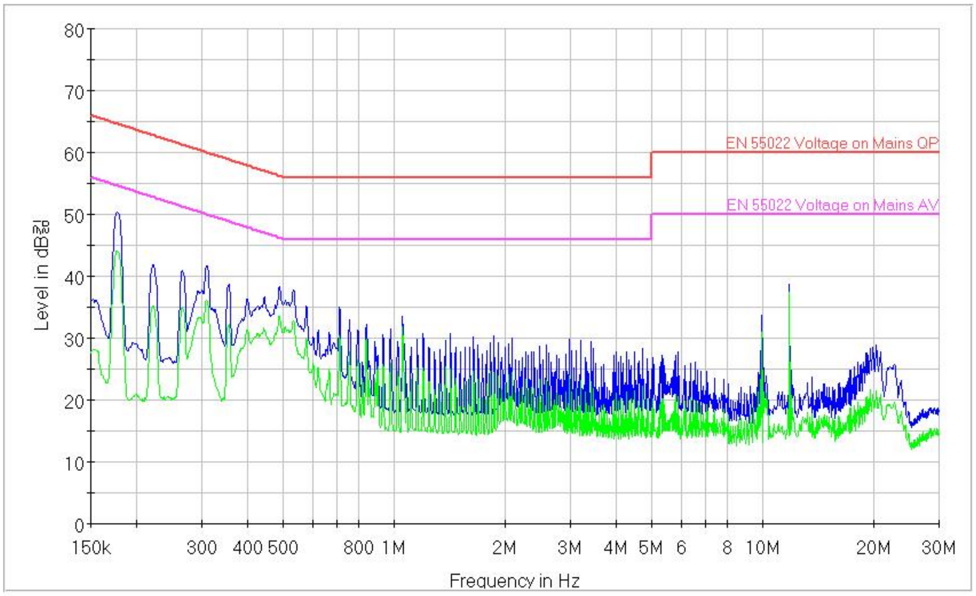


Figure 40. Conducted Emission-Line at 110 V_{AC} Input Condition, Full Load (10-LED Series)

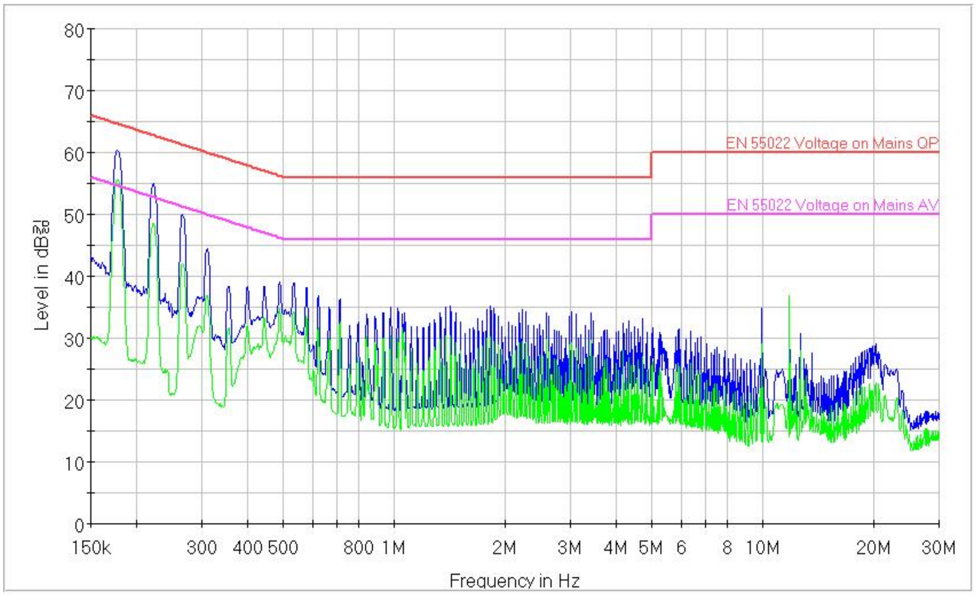


Figure 41. Conducted Emission-Neutral at 220 V_{AC} Input Condition, Full Load (10-LED Series)

8. Revision History

Rev.	Date	Description
1.0.0	May 2012	First Release
1.0.1	June 2012	Changing Power Rating & Template
1.0.2	Oct. 2012	Modified, edited, formatted document. Changed User Guide number from FEB-L032 to FEBFLS0116_L32U003A

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