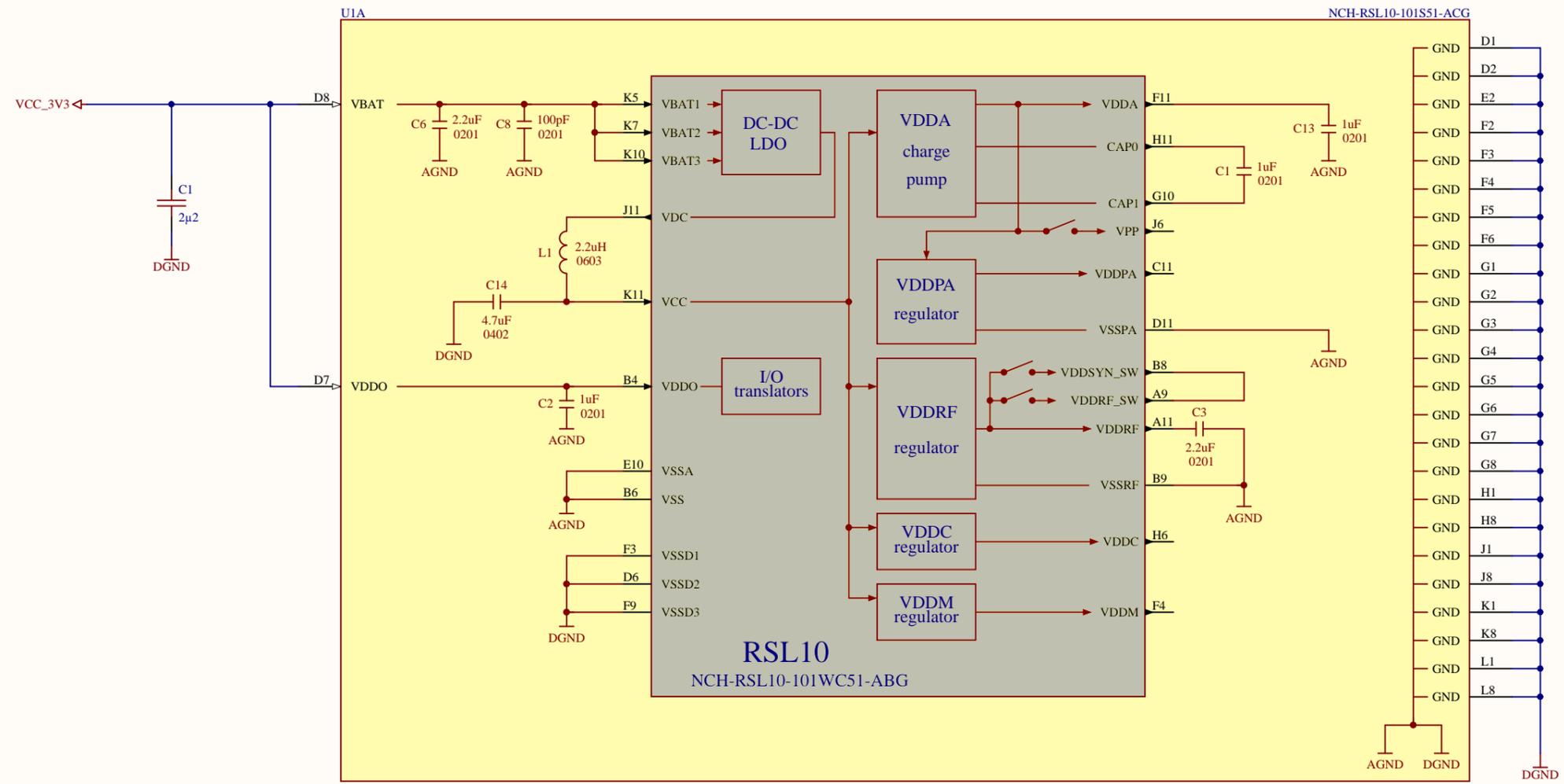
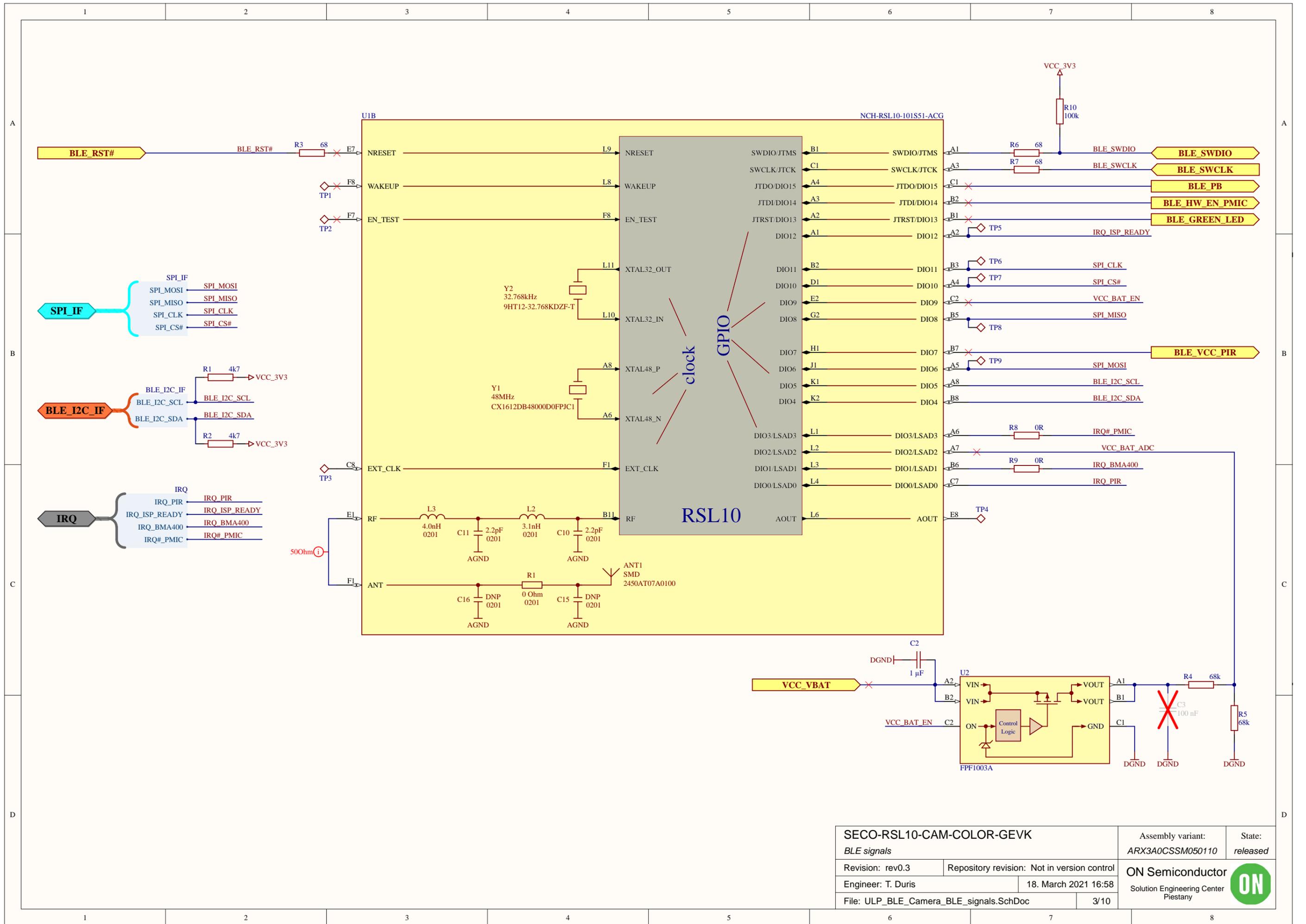


SECO-RSL10-CAM-COLOR-GEVK		Assembly variant:	State:
Block diagram		ARX3A0CSSM050110	released
Revision: rev0.3	Repository revision: Not in version control		ON Semiconductor Solution Engineering Center Piešťany
Engineer: T. Duris	18. March 2021 16:58		
File: ULP_BLE_Camera_block_diagram.SchDoc	1/10		



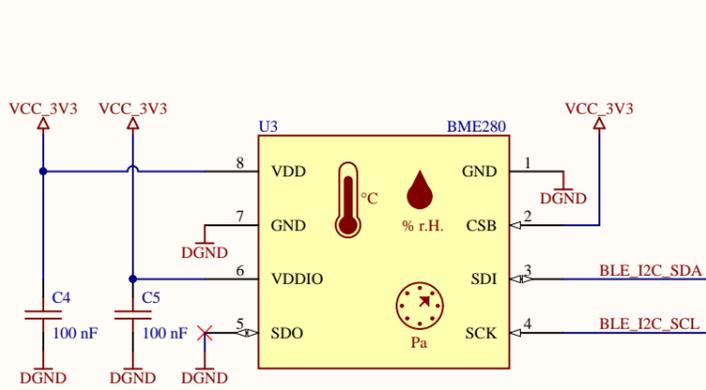


SECO-RSL10-CAM-COLOR-GEVK		Assembly variant:	State:
BLE power		ARX3A0CSSM050110	released
Revision: rev0.3	Repository revision: Not in version control		
Engineer: T. Duris	18. March 2021 16:58		
File: ULP_BLE_Camera_BLE_power.SchDoc	2/10		
		ON Semiconductor Solution Engineering Center Piešťany	

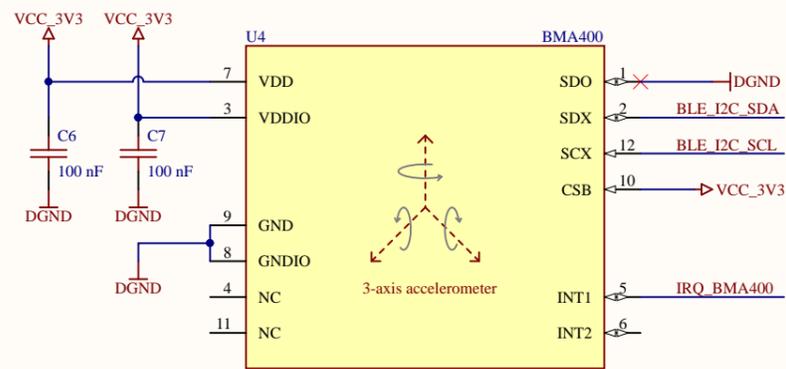


SECO-RSL10-CAM-COLOR-GEVK		Assembly variant:	State:
BLE signals		ARX3A0CSSM050110	released
Revision: rev0.3	Repository revision: Not in version control	ON Semiconductor Solution Engineering Center Piestany	
Engineer: T. Duris	18. March 2021 16:58		
File: ULP_BLE_Camera_BLE_signals.SchDoc	3/10		

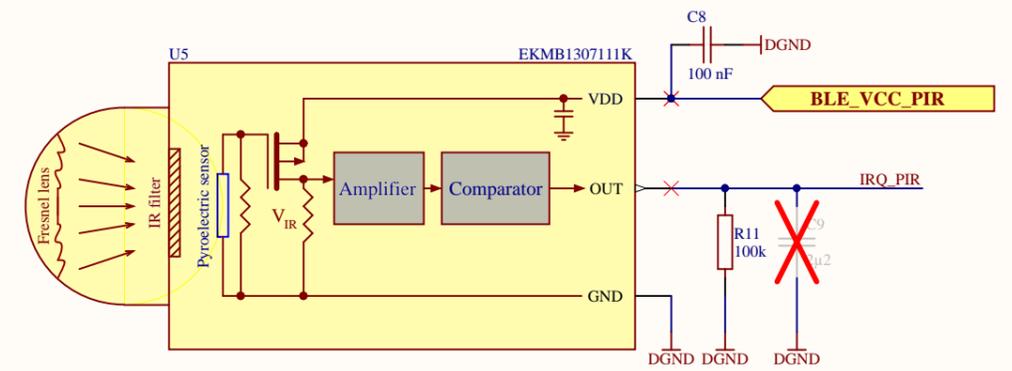




I2C slave address (7-bit): 0x76

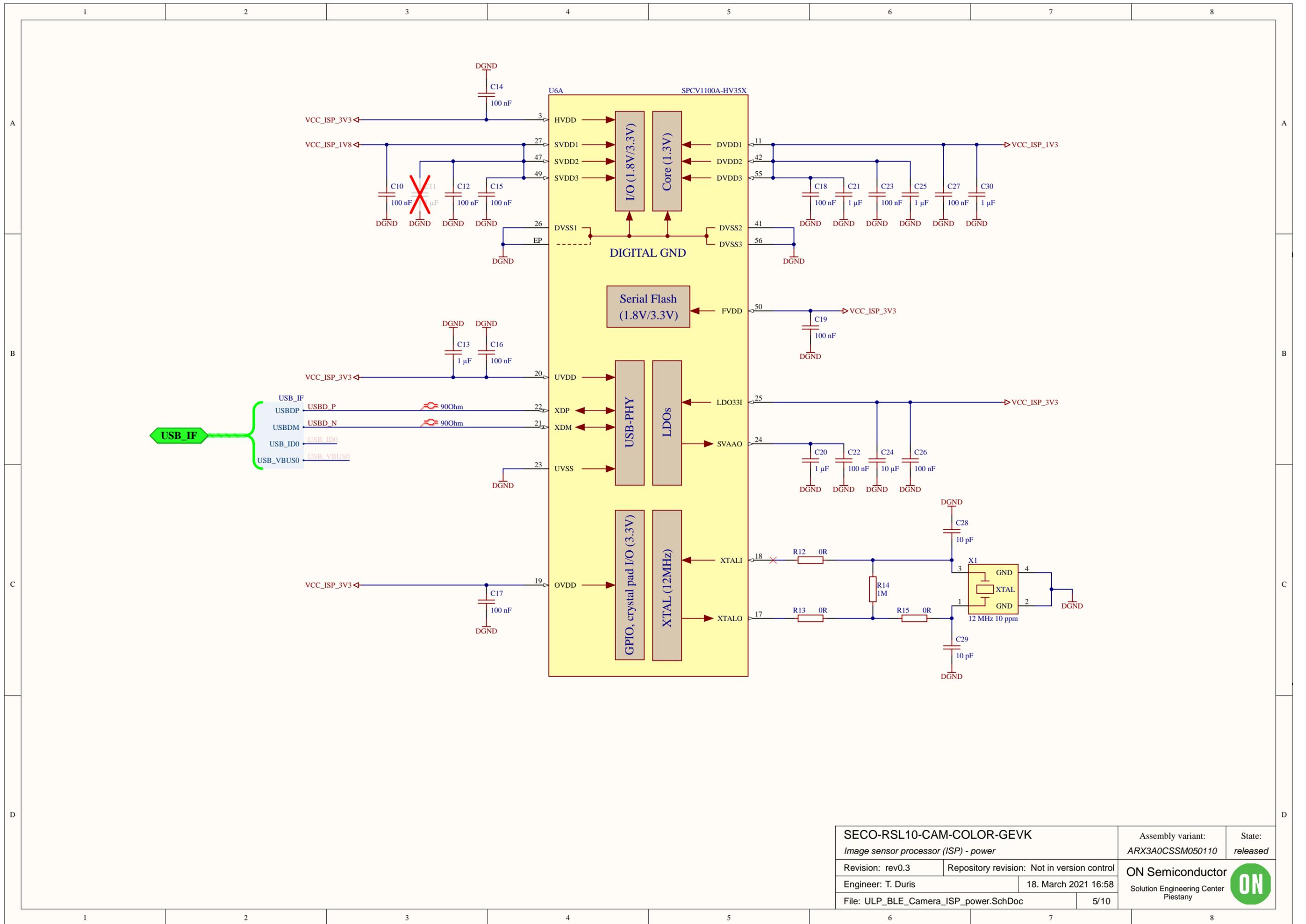


I2C slave address (7-bit): 0x14



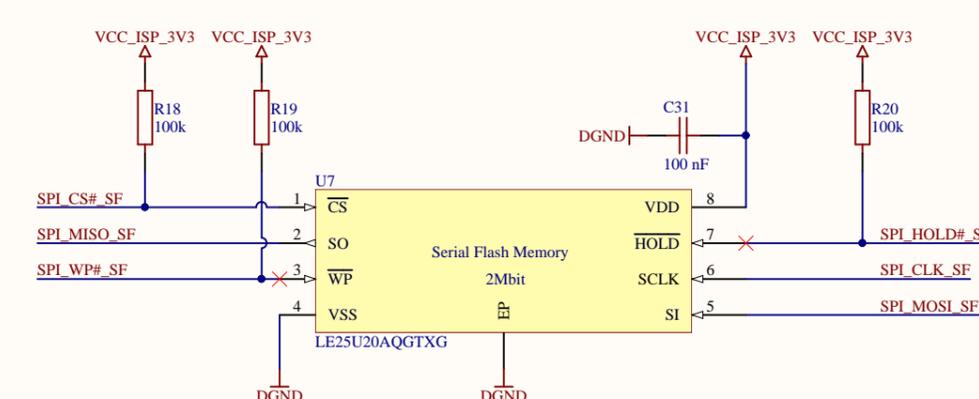
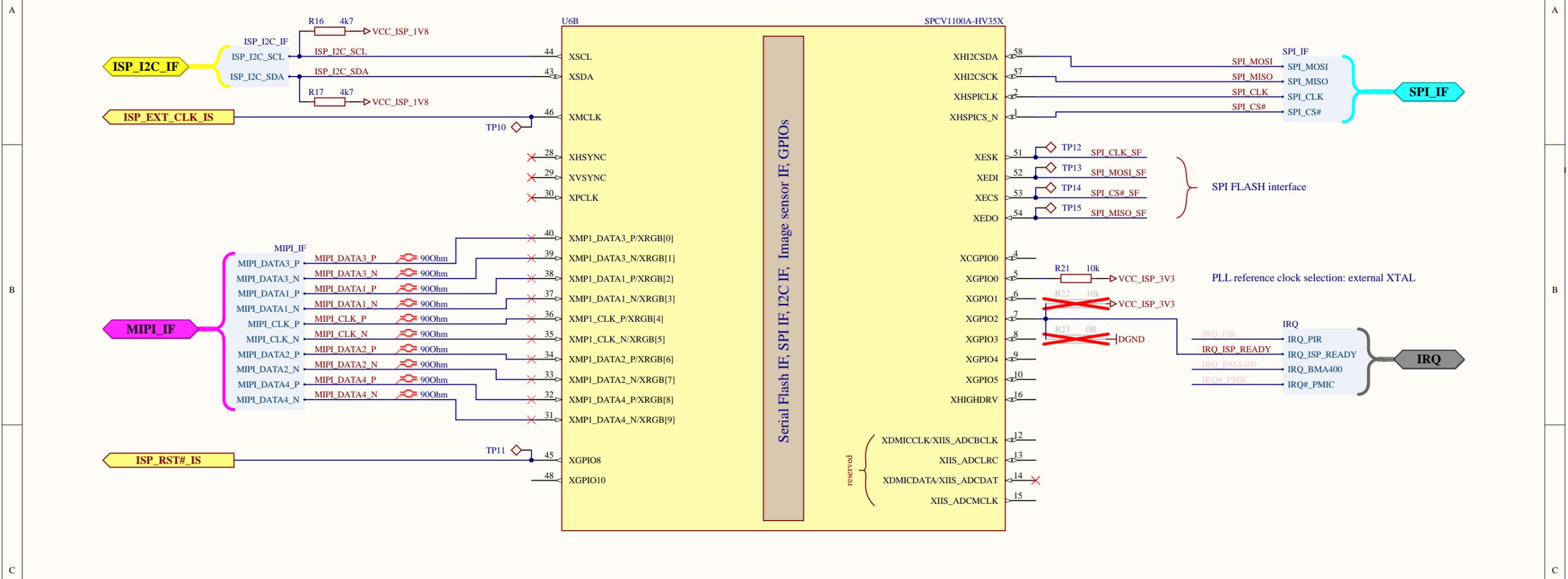
SECO-RSL10-CAM-COLOR-GEVK		Assembly variant:	State:
Sensors & actuators		ARX3A0CSSM050110	released
Revision: rev0.3	Repository revision: Not in version control		ON Semiconductor Solution Engineering Center Piešťany
Engineer: T. Duris	18. March 2021 16:58		
File: ULP_BLE_Camera_sensors_actuators.SchDoc	4/10		





SECO-RSL10-CAM-COLOR-GEVK <i>Image sensor processor (ISP) - power</i>		Assembly variant: ARX3A0CSSM050110	State: released
Revision: rev0.3	Repository revision: Not in version control		ON Semiconductor Solution Engineering Center Piešťany
Engineer: T. Duris	18. March 2021 16:58		
File: ULP_BLE_Camera_ISP_power.SchDoc	5/10		

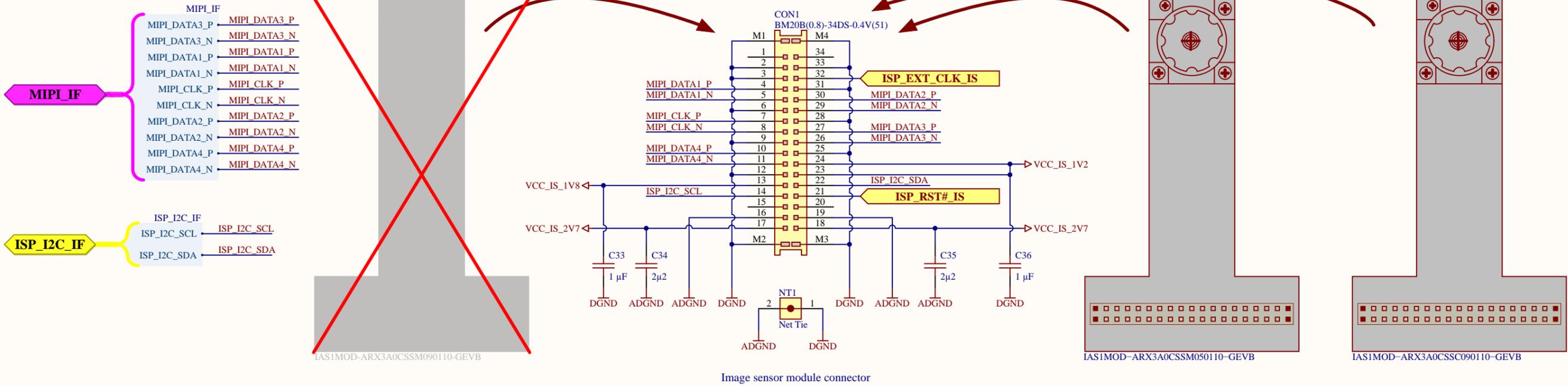




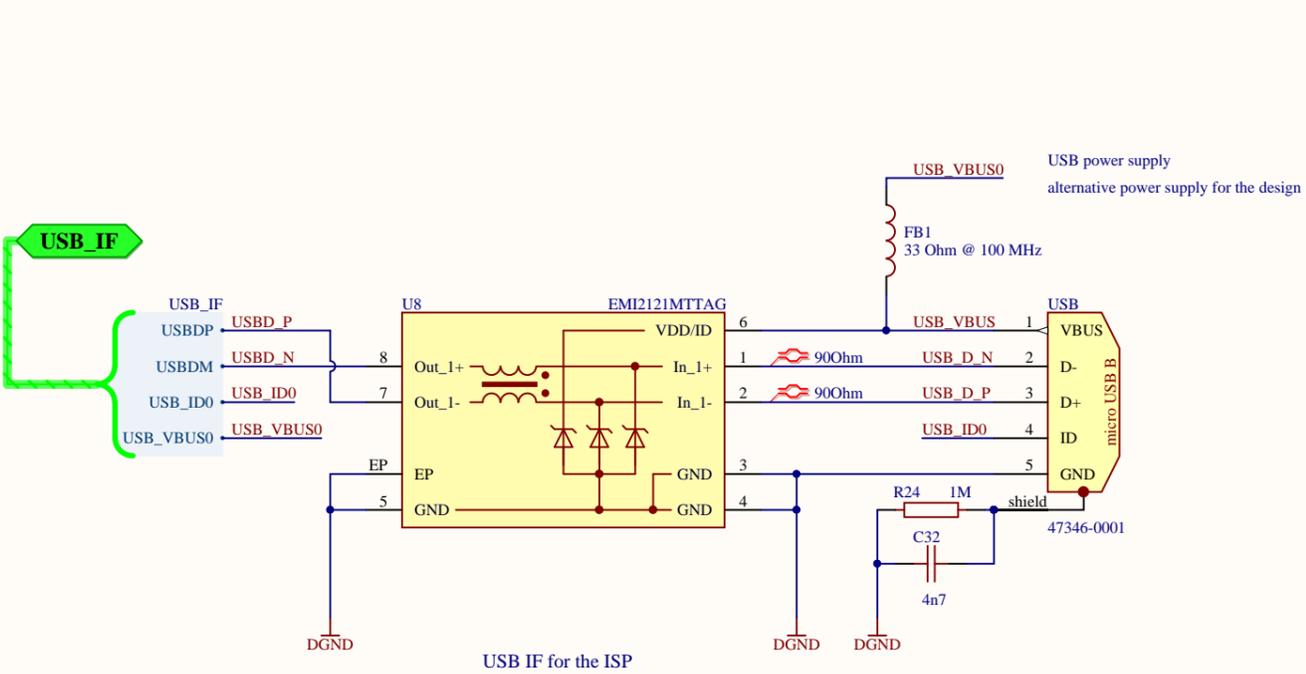
Serial FLASH for the ISP boot

SECO-RSL10-CAM-COLOR-GEVK		Assembly variant:	State:
<i>Image sensor processor (ISP) - signals</i>		ARX3A0CSSM050110	released
Revision: rev0.3	Repository revision: Not in version control	ON Semiconductor Solution Engineering Center Piešťany	
Engineer: T. Duris	18. March 2021 16:58		
File: ULP_BLE_Camera_ISP_signals.SchDoc	6/10		

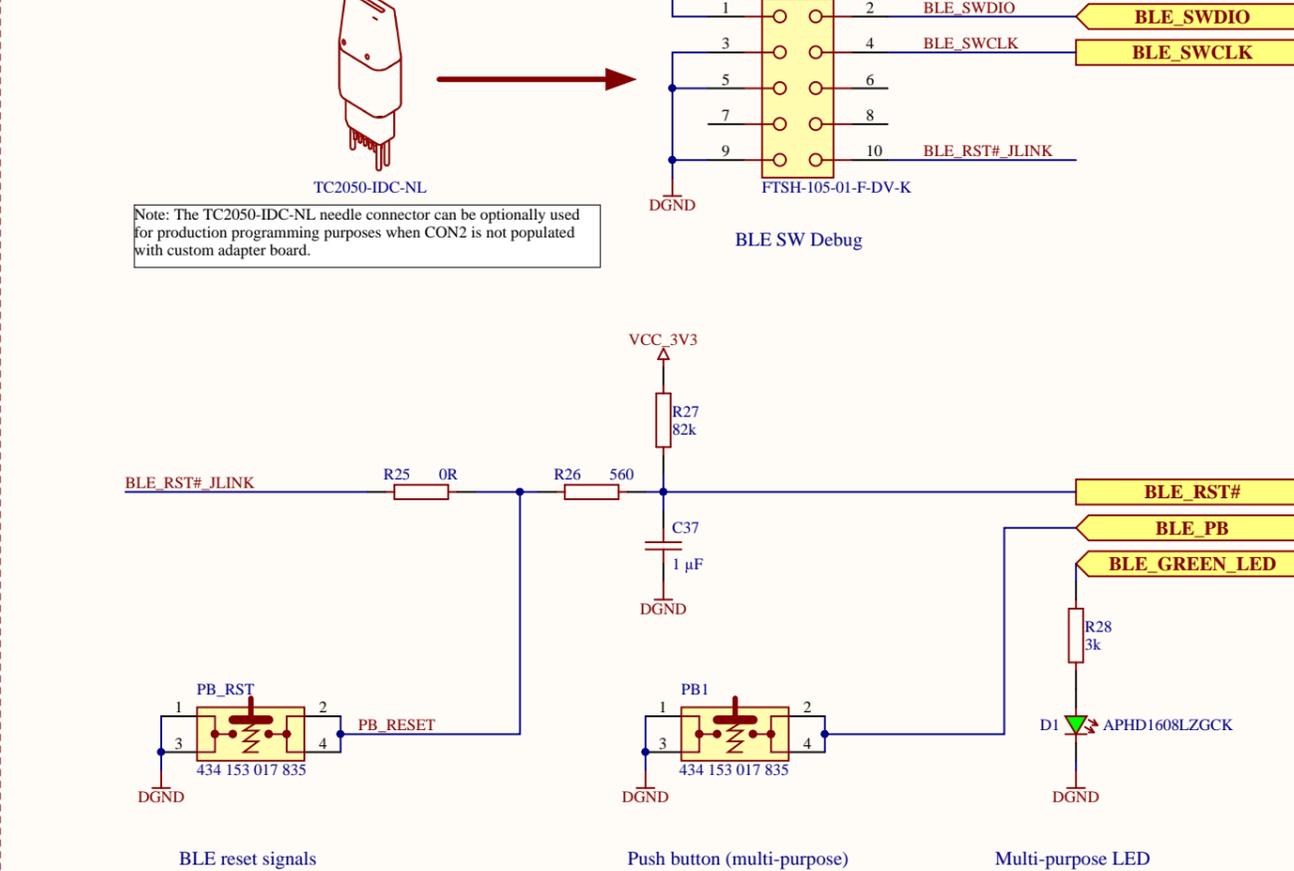
IS related



ISP related



BLE related



SECO-RSL10-CAM-COLOR-GEVK

User interface, image sensor (IS) interface

Revision: rev0.3

Repository revision: Not in version control

Engineer: T. Duris

18. March 2021 16:58

File: ULP_BLE_Camera_interface.SchDoc

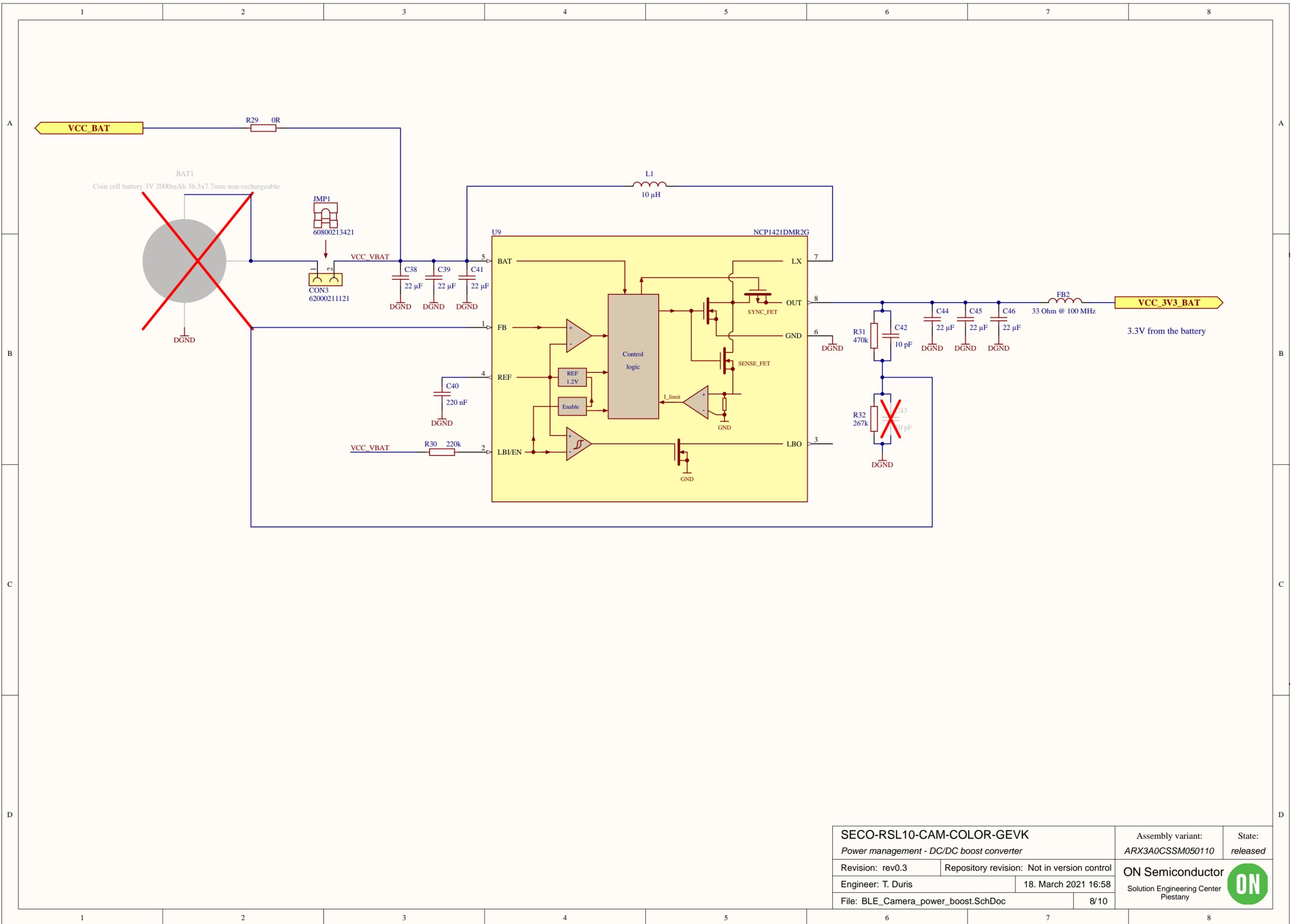
7/10

Assembly variant: ARX3A0CSSM050110

State: released

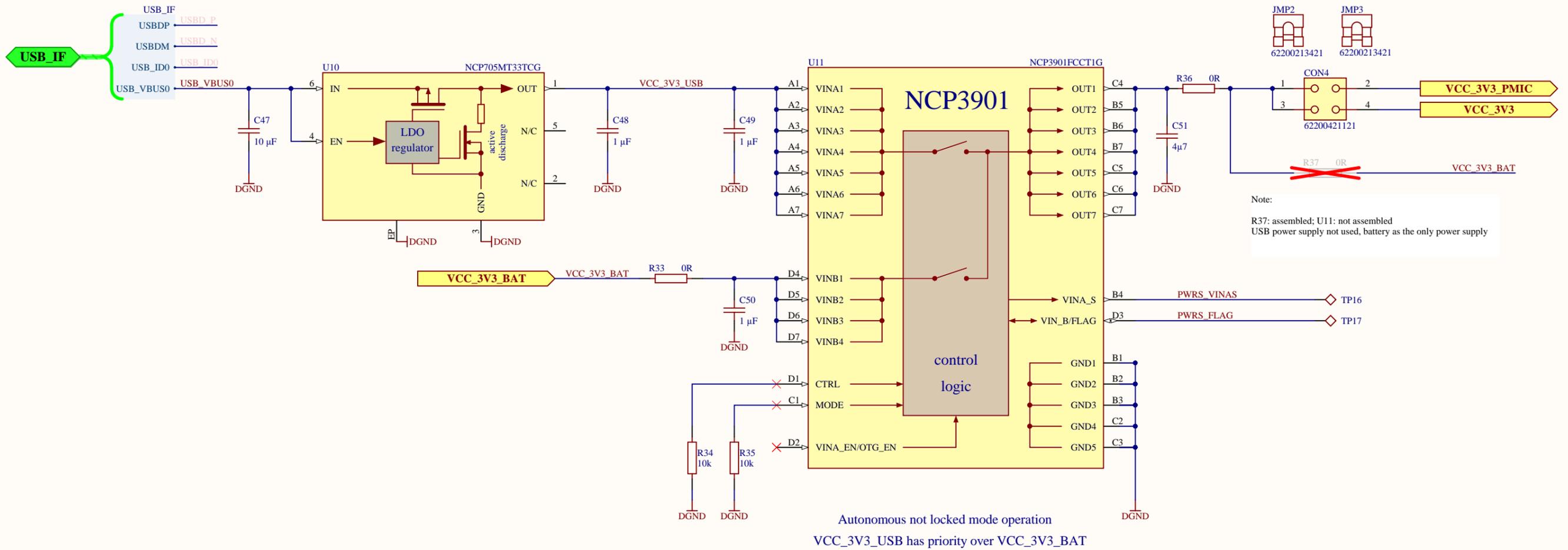
ON Semiconductor
Solution Engineering Center
Piestany





SECO-RSL10-CAM-COLOR-GEVK		Assembly variant:	State:
Power management - DC/DC boost converter		ARX3A0CSSM050110	released
Revision: rev0.3	Repository revision: Not in version control		ON Semiconductor Solution Engineering Center Piešťany
Engineer: T. Duris	18. March 2021 16:58		
File: BLE_Camera_power_boost.SchDoc	8/10		





SECO-RSL10-CAM-COLOR-GEVK Power management - power selection		Assembly variant: ARX3A0CSSM050110	State: released
Revision: rev0.3	Repository revision: Not in version control		ON Semiconductor Solution Engineering Center Piešťany
Engineer: T. Duris	18. March 2021 16:58		
File: BLE_Camera_power_selection.SchDoc	9/10		



A

B

C

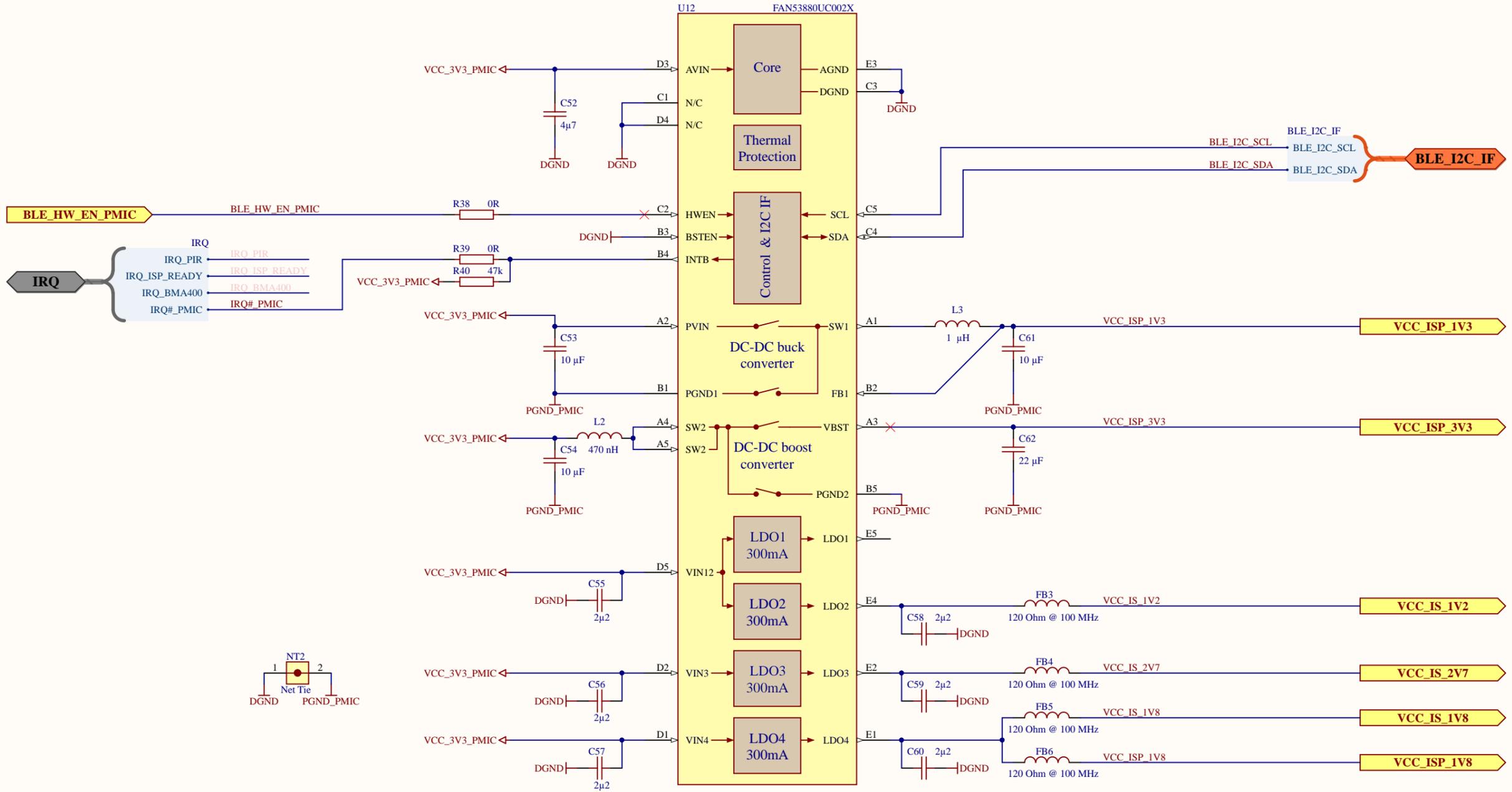
D

A

B

C

D



I2C address (7-bit): 0x35

ISP related	VCC_ISP_IV3	1.3V for the digital core
ISP related	VCC_ISP_IV8	1.8V for the sensor IF
ISP related	VCC_ISP_3V3	3.3V for the I/O, internal LDO, serial flash, SPI IF, USB IF
IS related	VCC_IS_IV2	1.2V for the digital core, PLL, MIPI IF
IS related	VCC_IS_IV8	1.8V for the I/O
IS related	VCC_IS_2V7	2.7V for the analog core, pixel array

SECO-RSL10-CAM-COLOR-GEVK		Assembly variant:	State:
Power management - power management IC (PMIC)		ARX3A0CSSM050110	released
Revision: rev0.3	Repository revision: Not in version control	ON Semiconductor Solution Engineering Center Piešťany	
Engineer: T. Duris	18. March 2021 16:58		
File: ULP_BLE_Camera_power_PMIC.SchDoc	10/10		